



|          |           |                                    |    |
|----------|-----------|------------------------------------|----|
| Title    |           | LDO-based Solution for MAX II CPLD |    |
| Size     | Number    | Rev                                |    |
| B        | PR358     |                                    |    |
| Date     | 8/19/04   | Drawn by                           |    |
| Filename | PR358.sch | Sheet                              | of |

| Filename: PR358_bom.xls |               |   |             |            |                    |
|-------------------------|---------------|---|-------------|------------|--------------------|
| Date: 08/24/2004        |               |   |             |            |                    |
| <b>PR358 BOM</b>        |               |   |             |            |                    |
|                         |               |   |             |            |                    |
| <b>COUNT</b>            | <b>RefDes</b> | <b>DESCRIPTION</b>  | <b>SIZE</b> | <b>MFR</b> | <b>Part Number</b> |
| 1                       | C1            | Capacitor, Ceramic, 0.1-uF, 16-V, X7R, 10%                          | 805         | muRata     | GRM188R71C104KA01  |
| 2                       | C2, C4        | Capacitor, Ceramic, 10-uF, 6.3-V, X5R, 10%                          | 805         | muRata     | GRM21BR60J106KE19  |
| 1                       | C3            | Capacitor, Ceramic, 0.01-uF, 25-V, X7R, 10%                         | 805         | muRata     | GRM188R71E103KA01  |
| 1                       | R1            | Resistor, Chip, 49.9k-Ohms, 1/16-W, 1%                              | 603         | Std        | Std                |
| 1                       | U1            | IC, Low Quiescent Current Programmable, 3.3-V, Delay Time 1ms to10s | SOT23-6     | TI         | TPS3808G33         |
| 1                       | U2            | IC, Ultra Low-Noise, High PSRR, Fast RF 1.5A LDO Linear Regulator   | DDPAK-5     | TI         | TPS78633KTT        |

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