

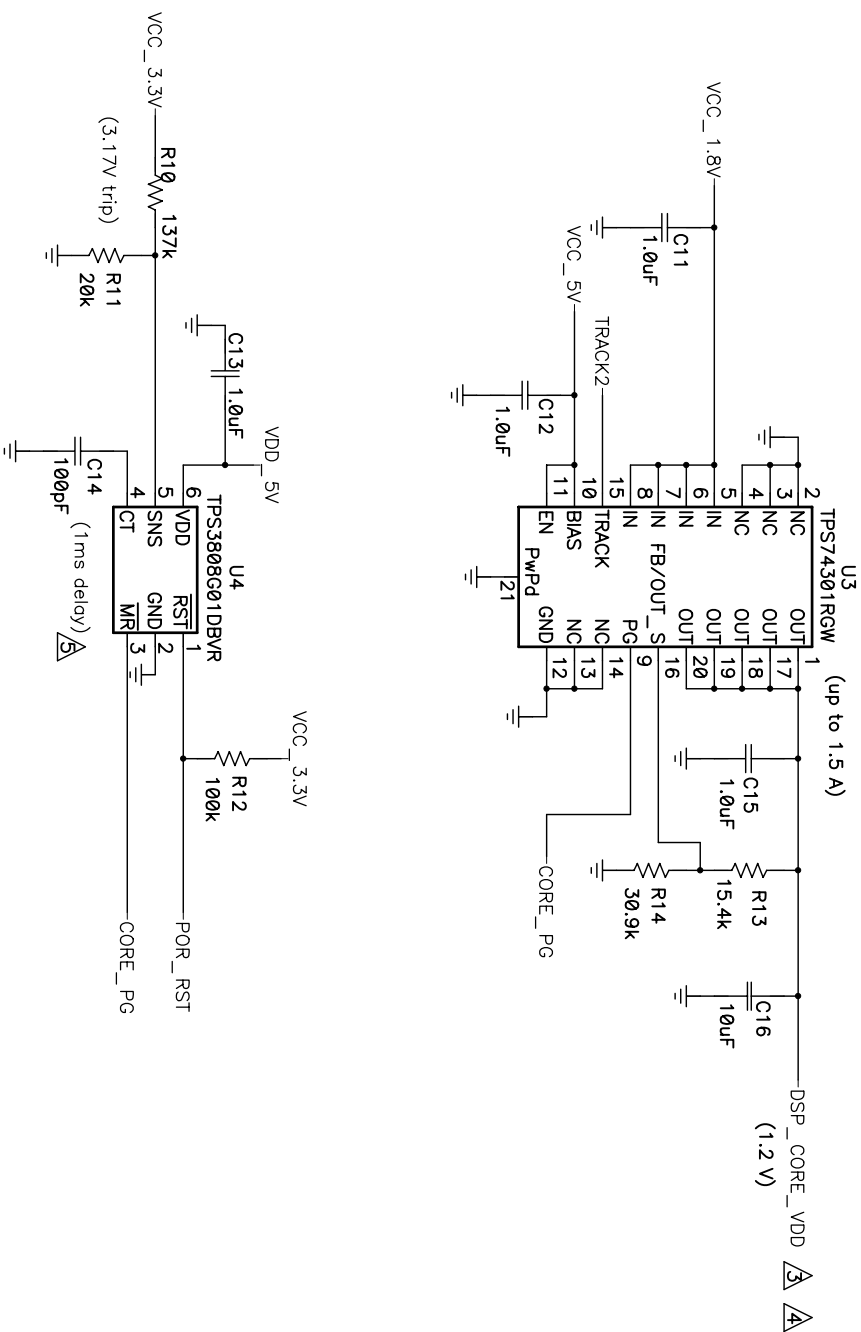
⚠ Additional input capacitance may be needed depending on U1 and U3 proximity to the input supply.

⚠ Additional bulk and bypass capacitance on each rail will be required. See DSP datasheet.

⚠ The sum of the currents for the VCC_3.3V, VCC_1.8V and DSP_CORE_VDD rails cannot exceed 3 A.

⚠ The sum of the currents for the VCC_1.8V and DSP_CORE_VDD rails cannot exceed 1.5 A.

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Adjustable from 1ms to 300ms using C14

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