



NOTES:
1)

RevB: nonsync. rectification
(800mAavg / 1.8Apk)

- ASSEMBLY RevC:
- 1) built on PCB PMP3891RevA
 - 2) L101, D101, C101 is mounted externally
 - 3) T2, L101 needs rework
 - 4) C13, C17, C20, C26, R17, R18 OPEN
 - 5) C16 does not meet pattern

- RevD: 1) tuned ZERO inner loop
C22 = 470nF
2) tuned POLE outer loop
C21 = 1nF

Schematic as shown is for Adapter Priority.
For PoE Priority, populate D4 and DNP D9, R26, R27, D101

Title PoE Low Cost Flyback			
Size C	Number PMP5593	TEXAS INSTRUMENTS	Rev D
Date 12/13/2010	Drawn by B.Geck		
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