

PMP7872

PMP7872 Fabrication Drawings



Literature Number: SNVR173

Layer Stack Up Detail for: PMP7872_REVA.PcbDoc

Layer Name	Serber Document	Copper Thickness	Dielectric Material
Top Solder Mask	(.GTS)		Solder Resist
Top Layer	(.GTL)	2.8mil	FR-4
MidLayer1	(.G1)	1.4mil	FR-4
MidLayer2	(.G2)	1.4mil	FR-4
Bottom Layer	(.GBL)	2.8mil	FR-4
Bottom Solder Mask	(.GBS)		Solder Resist

DESIGN INFORMATION

BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)
 2400MIL X 2800MIL

Number of Layers : 4
 MIN. TRACK WIDTH: 8 MIL
 MIN. CLEARANCE: 8 MIL
 MIN. VIA PAD SIZE: 24 MIL

MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL
 PER IPC-D-275 CLASS 2 LEVEL C
 REGISTRATION TOLERANCES: METAL +/- 5 MIL HOLES +/- 3 MIL

MATERIAL:
 FR-4 FR-4 High Tg OTHER _____
 THICKNESS: 62 MIL (1.6mm) +/-10% OTHER _____
 TOLERANCE: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____
 BOW & TWIST: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____

COPPER THICKNESS (FINISHED):
 OUTER: 1.4MIL (1oz) 2MIL (1.4oz) 2.8MIL (2oz)
 INNER SIGNAL: 1.4MIL (1oz) 2.8MIL (2oz)

DRILLING:
 REFERENCE: AS SHOWN NC_DRILL FILES
 PTH MIN COPPER THICKNESS: 1MIL OTHER _____

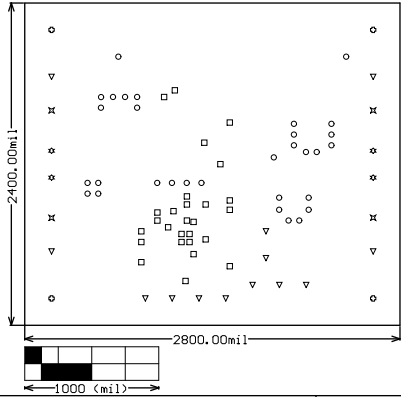
BOARD FINISH:
 SILKSCREEN: TOP BOTTOM
 SILKSCREEN COLOR: WHITE OTHER _____
 SOLDER RESIST COLOR:
 GREEN BLUE OTHER _____

SURFACE FINISH:
 IMMERSION GOLD (ENIG) Pb-FREE HASL
 OTHER _____

ARRAY/PANEL:
 CUT AND TRIM PER MECH LAYER 1
 N.C. ROUTE V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:
 ANSI IPC-A-600F CLASS -> 1 2 3
 UL 94V-0 RoHS OTHER PER ORDER

ADDITIONAL REQUIREMENTS:
 MICROSECTION: YES
 BARE BOARD ELEC. TEST: NONE REQUIRED PER ORDER
 MANUFACTURER'S ID/LOGO: RAL METAL SILK



Symbol	Hit Count	Tool Size	Plated	Hole Type
□	27	20mil (0.508mm)	PTH	Round
○	31	30mil (0.762mm)	PTH	Round
▽	13	40mil (1.016mm)	PTH	Round
*	4	51.181mil (1.3mm)	PTH	Round
⊗	4	98.425mil (2.5mm)	PTH	Round
⊘	4	125.984mil (3.2mm)	PTH	Round
	83 Total			

Drill Table

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP7872	REV: A	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.	ENGINEER: Robert Sheehan	LAYOUT BY: Robert Sheehan
LAYER NAME = Fabrication Drawings	GENERATED : 10/4/2012 5:32:25 PM		TEXAS INSTRUMENTS	SCALE: 0.70	ALTIM DESIGNER VERSION: 10.0.0.24817

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