AM572x Industrial Development Kit
Industrial Ethernet LED's

Status LED's
Catalog Processors - Vpp Programming Circuit
Assembly Notes:
1. These assemblies are ESD sensitive. ESD precautions shall be observed.
2. These assemblies must be clean and free from flux and all contaminants. Use of 'no clean flux' is not acceptable.
3. These assemblies must comply with workmanship standard IPC-A-610 Class 2, unless otherwise specified.
4. Each finished board will receive a serial number sticker during final testing.
5. Each finished board will receive a MAC Address sticker during final testing.
6. Install header shunt M1 (on J49) and M2 (on J50) during final testing.

Install label in silkscreened box after final wash.
Text shall be 8 pt. Text shall be per the label table in the PDF schematics
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Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
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