Input Voltage Range

Min.: 8 V  Typ.: 14 V  Max.: 16 V

- PMEG4050EP
- EMI Filter
- C-C Filter
- LM63625-Q1
  3.3 V @ 2.2 A
  7.6 W
- TPS22918-Q1
  Load Switch
- LM2775-Q1
  5 V @ 43 mA
  0.21 W
- TLV73312-Q1
  Capacitor Free LDO
  1.2 V @ 0.1 A
  0.12 W
- TPS62813-Q1
  Sync Buck, 2.1 MHz, 8 S, n1=98%
  1.03 V @ 3 A
  3.1 W
- TPS62811-Q1
  Sync Buck, 2.1 MHz, n7=94%
  1.8 V @ 0.5 A
  0.9 W
- TPS22918-Q1
  Load Switch
  3.3 V @ 0.2 A
  0.67 W
- TPS62812-Q1
  Sync Buck, 2.1 MHz, n7=92%
  1.5 V @ 1.2 A
  1.8 W
- TPS51200-Q1
  Termination
  1.8 W
- CAN
- Des
- SoC
- DDR3

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Not in version control
SVN Rev:

PMP30785
Number:

Rev:

B

TID #: N/A

Orderable: ChangeMe in variant

### Revision History

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<th>Approved Date</th>
<th>Approver</th>
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<td>10/29</td>
<td>I. Weiss</td>
<td>*** PRELIMINARY SCHEMATIC ***</td>
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<tr>
<td>B</td>
<td>11/29</td>
<td>I. Weiss</td>
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<td>*** Add Electrolyic Caps, Layout ***</td>
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Supply for SoC - E2

Optional Input Filter

Input Voltage:
8V ... 16V (14V typ)

1V8_BUCK = 1.8V @ 0.5A

3V1_BUCK0 = 3.3V @ 2.2A

3V3_BUCK = 3.3V @ 2.2A

1V03_BUCK = 1.03V @ 3A

3V3_SoC = 3.3V @ 0.2A

*Assembly Notes:
*R10, TP6, TP9 only for testing
*R4, TP1, TP2 only for testing
*Use GRT-Series for capacitors

Contact: http://www.ti.com/support
Variant/Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
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<td>001</td>
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<tr>
<td>002</td>
<td>ChangeMe!</td>
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Assembly Note

- ZZ1: The assembly notes ZZ1 are for PCB labels only.
- ZZ2: These assemblies are ESD sensitive, ESD precautions shall be observed.
- ZZ3: These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.
- ZZ4: These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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