BOM

TI Power Reference Design for Xilinx® Artix®-7
• Xilinx Artix-7 FPGA AC701 Evaluation Kit

• BOM for AC701
  link

User Guide P8
User Guide P70
<table>
<thead>
<tr>
<th>Device Type</th>
<th>Reference Designator</th>
<th>Description</th>
<th>Power Rail Net Name</th>
<th>Power Rail Voltage</th>
<th>Schematic Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>UCD090120A (4 Rails)</td>
<td>U8</td>
<td>PMBus Controller - PMBus Addr = 101</td>
<td>VCCINT</td>
<td>1.00V</td>
<td>39</td>
</tr>
<tr>
<td>LMZ22010TZ</td>
<td>U49</td>
<td>10A 0.8V - 6V Adj. Switching Regulator</td>
<td>VCCINT</td>
<td>1.00V</td>
<td>39</td>
</tr>
<tr>
<td>TPS84621RUQ</td>
<td>U53</td>
<td>6A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>VCCAUX</td>
<td>1.80V</td>
<td>41</td>
</tr>
<tr>
<td>TPS84320RUQ</td>
<td>U54</td>
<td>3A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>VCCBRAM</td>
<td>1.00V</td>
<td>42</td>
</tr>
<tr>
<td>TPS84621RUQ</td>
<td>U55</td>
<td>6A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>FPGA_1V5</td>
<td>1.50V</td>
<td>43</td>
</tr>
<tr>
<td>UCD090120A (5 Rails)</td>
<td>U9</td>
<td>PMBus Controller - PMBus Addr = 102</td>
<td>VCCINT</td>
<td>2.50V</td>
<td>46</td>
</tr>
<tr>
<td>TPS84621RUQ</td>
<td>U56</td>
<td>6A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>VCCO_VADJ</td>
<td>2.50V</td>
<td>46</td>
</tr>
<tr>
<td>TPS84320RUQ</td>
<td>U57</td>
<td>3A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>VCC1V8/</td>
<td>1.80V</td>
<td>47</td>
</tr>
<tr>
<td>TPS84621RUQ</td>
<td>U58</td>
<td>6A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>VCC2V3/</td>
<td>3.30V</td>
<td>48</td>
</tr>
<tr>
<td>TPS84320RUQ</td>
<td>U59</td>
<td>3A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>FPGA_3V3</td>
<td>1.00V</td>
<td>49</td>
</tr>
<tr>
<td>TPS84320RUQ</td>
<td>U60</td>
<td>3A 0.6V - 5.5V Adj. Switching Regulator</td>
<td>MGTAVTT</td>
<td>1.20V</td>
<td>50</td>
</tr>
<tr>
<td>LMZ12002</td>
<td>U46</td>
<td>2A 0.8V - 6V Adj. Linear Regulator</td>
<td>VCC5V0</td>
<td>5.00V</td>
<td>34</td>
</tr>
<tr>
<td>TL1963ADCQR</td>
<td>U62</td>
<td>1.5A 1.21V - 5V Adj. LDO Linear Regulator</td>
<td>VCC2V5</td>
<td>2.50V</td>
<td>48</td>
</tr>
<tr>
<td>TPS512000DR</td>
<td>U37</td>
<td>3A Source-Sink DDR Termination Regulator</td>
<td>VTTDDR</td>
<td>0.75V</td>
<td>44</td>
</tr>
<tr>
<td>TPS512000DR</td>
<td>U36</td>
<td>3A Source-Sink DDR Termination Regulator</td>
<td>DDR3_VTERM_ R_0V75</td>
<td>0.75V</td>
<td>44</td>
</tr>
<tr>
<td>TPS79433DCQ</td>
<td>U61</td>
<td>0.25A 1.2V - 5.5V Adj. LDO Linear Regulator</td>
<td>V33D_CNTL1</td>
<td>3.30V</td>
<td>39</td>
</tr>
<tr>
<td>TPS79433DCQ</td>
<td>U63</td>
<td>0.25A 1.2V - 5.5V Adj. LDO Linear Regulator</td>
<td>V33D_CNTL2</td>
<td>3.30V</td>
<td>45</td>
</tr>
<tr>
<td>ADP123</td>
<td>U10</td>
<td>0.3A 0.8V - 5V Adj. Linear Regulator</td>
<td>XADC_VCC</td>
<td>1.85V</td>
<td>29</td>
</tr>
<tr>
<td>REF3012</td>
<td>U35</td>
<td>50uA Fixed 1.25V Voltage Reference</td>
<td>XADC_VREF</td>
<td>1.25V</td>
<td>29</td>
</tr>
</tbody>
</table>

**User Guide P71**
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