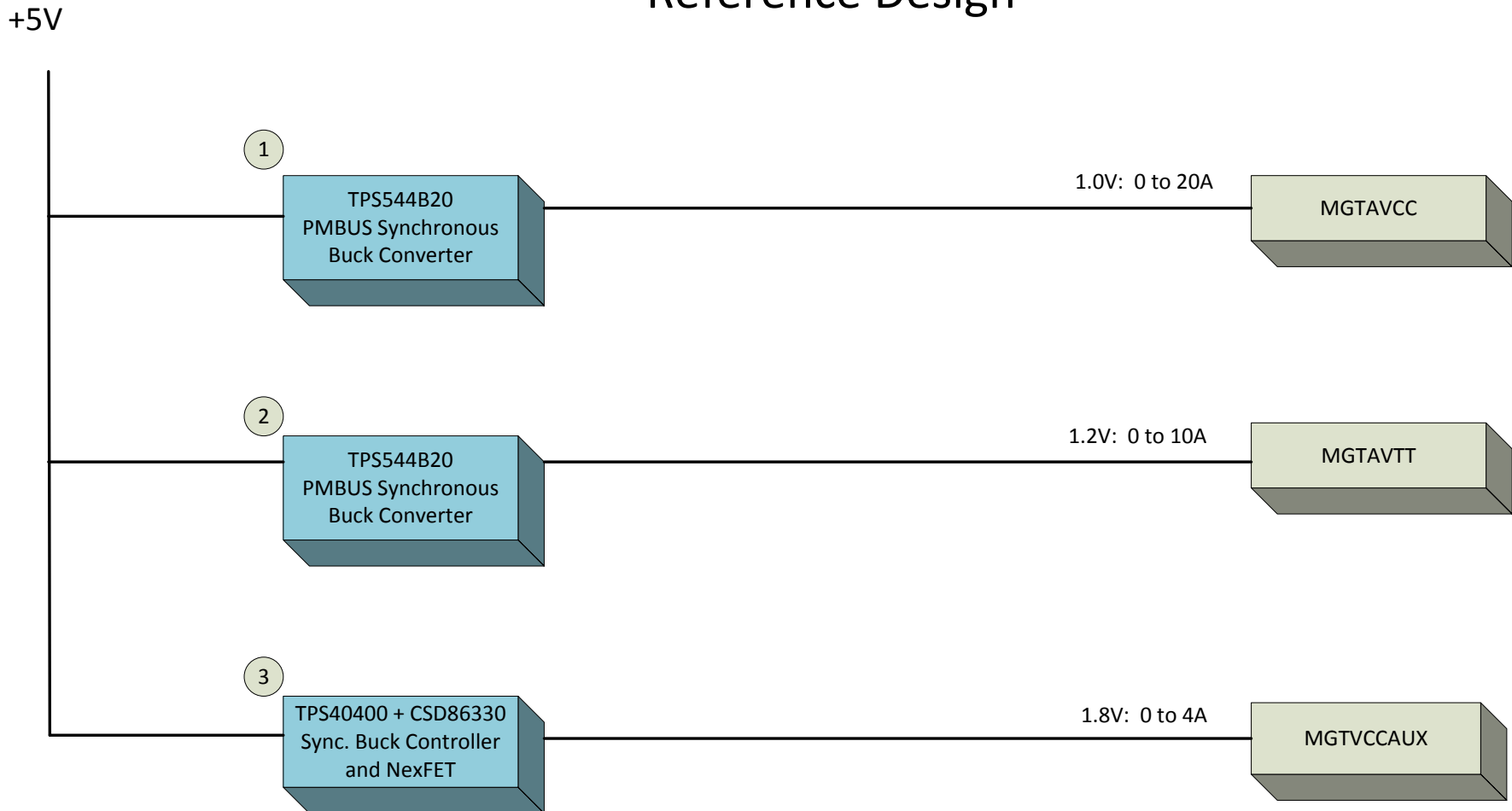


Xilinx Ultrascale Virtex MGT Reference Design



(X) = Sequence Order

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