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Designed for: Public Release	Mod. Date: 8/14/2014	
Project Title: 0-10V DimmingHardware Daughter Board	Sheet Title: 0-10V DimmingHardware Daughter Board	
Number: PMP6023-2	Rev: E2	Sheet: 1 of 2
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Size: B
Drawn By: David Zhang	File: PMP6023-2_E1.SchDoc	http://www.ti.com
Engineer: David Zhang	Contact: http://www.ti.com/support	© Texas Instruments 2014

PCB  
LOGO  
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ZZ1

Assembly Note

These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2

Assembly Note

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3

Assembly Note

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Project Title: 0-10V DimmingHardware Daughter Board			
Number: PMP6023-2		Rev: E2	Sheet Title: Hardware
SVN Rev: Not in version control		Assembly Variant: [No Variations]	Sheet: 2 of 2
Drawn By: David Zhang		File: PMP6023-2_E1_Hardware.SchDoc	Size: B
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