

Figure 1: PMP8750_RevB_1 Top-Side Layout

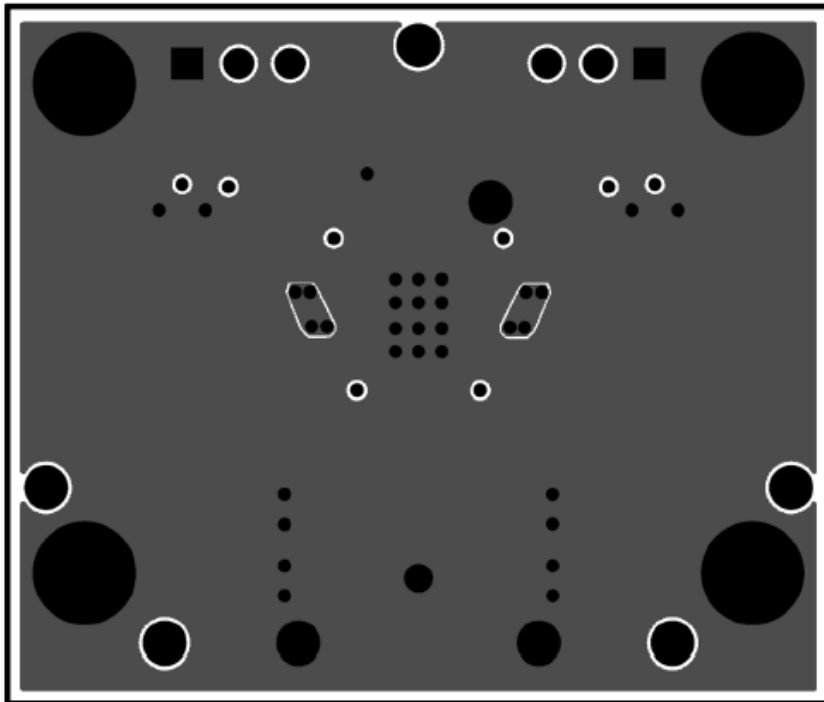


Figure 2: PMP8750_RevB_7 inner Layer 1

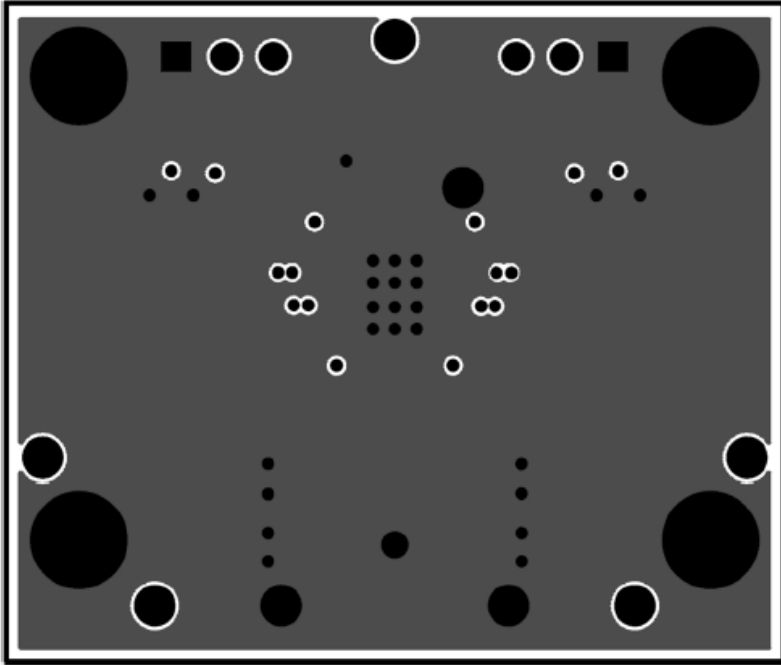


Figure 3: PMP8750_RevB_7 inner Layer 2

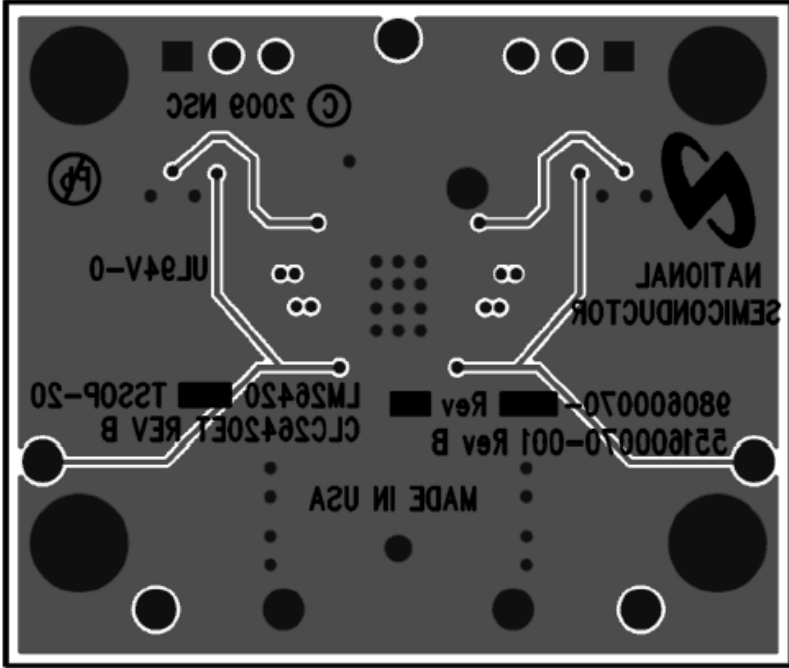


Figure 4: PMP8750_RevB_7 Bottom Side Layout

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