These assemblies are ESD sensitive. ESD precautions shall be observed.

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Components marked "X" should not be populated.

Assembly Variants: D01

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<th>PCB Viewed From Top Side</th>
<th>BOARD #</th>
<th>PNB#</th>
<th>REV</th>
<th>CHK</th>
<th>SUN REV</th>
<th>Not In Version Control</th>
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Plot Name: Top Assembly Drawing

Generated: 1

Date: 6/26/2015

Time: 3:35:52 PM

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