Isolated IGBT Gate Driver Evaluation Platform

Designed for: [Assembly Variant]

Variant name not interpreted

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Drawn By: [Engineer]

Contact: http://www.ti.com/support

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Not in version control

SVN Rev:

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1. Non-inverting Configuration

2. GATE_Y_TOP_RTN

3. DNP While testing with twisted pair

4. DNP While testing with twisted pair

Gate Driver Top IGBT Y

Gate Driver Bottom IGBT Y
Gate Driver Brake IGBT

**NOTE:**
1. DNP While testing with twisted pair

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Ready Indication

Fault Indication

Reset Control

SMPS Power Rails LED indications

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