These assemblies are ESD sensitive, ESD precautions shall be observed.
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
Place SH-J1 on J4 pins 2 and 3.

COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001

<table>
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<th>PCB VIEWED FROM TOP SIDE</th>
<th>BOARD #: ISE4045</th>
<th>REV: E2</th>
<th>SUN REV: Not In Version Control</th>
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COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001

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