



basic calculations for nominal input 12V:

Fsw 300kHz, Tsw 3.33usec, duty 44.44%  
Ton 1.48usec, Toff 1.85usec, di 220mApp

RevC - "low noise":  
1) reduced reflected ripple by L111/C112  
2) reduced switchnode ringing by RC snubber

RevB:  
1) R2, 50milliOhm, startup w/ 1Aout & 8Vin  
2) set bw to 15kHz  
3) corrected testpoints

NOTES:  
1) R8, TP8, TP5 for test purposes only  
2) PSU is design example for FAE training  
3) built on uniboard PMP1278 Rev. C

ASSEMBLY:  
C1, C9, C10 OPEN  
L1, Q1 OPEN  
(R111, C111 mounted externally)  
scratch PCB for placing L111

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