Page 2  BLOCK DIAGRAM

Page 3  POWER SUPPLY (LDOs and INVERTER FOR NEGATIVE SUPPLY)

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Page 8  ADC2-ADS131A04 + ADC104G021

Page 9  OSCILLATOR, DUAL OUTPUT CLOCK BUFFER
SAR_ADC inputs connected to ΔΣ input pins
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Update the Label Text in the Label Table as needed for each Assembly Variant.

You should delete this note too.

**Variant/Label Table**

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<td>002</td>
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**Assembly Notes**

- **ZZ1**: These assemblies are ESD sensitive. ESD precautions shall be observed.
- **ZZ2**: These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
- **ZZ3**: These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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