

Filename: PMP30059RevD - BoM.xls
 Variant: None
 Generated: 7/7/2016 3:43:08 PM
 TID #: <Parameter TID not found>



PMP30059 REV D Bill of Materials

Item #	Designator	Quantity	Value	PartNumber	Manufacturer	Description	PackageReference
1	C1, C2, C3, C15, C16, C27, C28, C32, C37, C38	10	0.1uF	GRM188R71H104KA93D	MuRata	CAP, CERM, 0.1 µF, 50 V, +/- 10%, X7R, 0603	0603
2	C4, C5	2	47uF	EEHZA1V470P	Panasonic	CAP, Polymer Hybrid, 47 µF, 35 V, +/- 20%, 60 ohm, 6.3x5.8 SMD	6.3x5.8
3	C6, C7	2	4.7uF	GRM32ER71H475KA88L	MuRata	CAP, CERM, 4.7 µF, 50 V, +/- 10%, X7R, 1210	1210
4	C10, C13	2	2.2uF	GRM188R71A225KE15D	MuRata	CAP, CERM, 2.2 µF, 10 V, +/- 10%, X7R, 0603	0603
5	C11	1	1uF	GRM31MR71H105KA88L	MuRata	CAP, CERM, 1 µF, 50 V, +/- 10%, X7R, 1206	1206
6	C17, C19	2	220uF	PCJ0J221MCL1GS	Nichicon	CAP, Aluminum Polymer, 220 µF, 6.3 V, +/- 20%, 0.015 ohm, SMD	SMT Radial D
7	C21, C22	2	470pF	GRM188R71H471KA01D	MuRata	CAP, CERM, 470 pF, 50 V, +/- 10%, X7R, 0603	0603
8	C23, C24	2	33pF	GRM1885C1H330JA01D	MuRata	CAP, CERM, 33 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603
9	C25, C26	2	100pF	GRM1885C1H101JA01D	MuRata	CAP, CERM, 100 pF, 50 V, +/- 5%, C0G/NP0, 0603	0603
10	C29	1	0.022uF	GRM188R71H223KA01D	MuRata	CAP, CERM, 0.022 µF, 50 V, +/- 10%, X7R, 0603	0603
11	C30	1	0.047uF	GRM188R71H473KA61D	MuRata	CAP, CERM, 0.047 µF, 50 V, +/- 10%, X7R, 0603	0603
12	C31, C101	2	0.01uF	GRM188R71H103KA01D	MuRata	CAP, CERM, 0.01 µF, 50 V, +/- 10%, X7R, 0603	0603
13	C35, C36	2	10uF	GRM21BR71A106KE51K	MuRata	CAP, CERM, 10 µF, 10 V, +/- 10%, X7R, 0805	0805
14	D1, D2	2	60V	PMEG6010CEH,115	NXP Semiconductor	Diode, Schottky, 60 V, 1 A, SOD-123F	SOD-123F
15	D3, D4	2	40V	DFLS240-7	Diodes Inc.	Diode, Schottky, 40 V, 2 A, AEC-Q101, PowerDI123	PowerDI123
16	FID1, FID2, FID3, FID4	4		N/A	N/A	Fiducial mark. There is nothing to buy or mount.	N/A
17	J1, J2	2		61300311121	Würth Elektronik	Header, 2.54 mm, 3x1, Gold, TH	Header, 2.54mm, 3x1, TH
18	L1, L2	2	2.5uH	ETQP5M2R5YFK	Panasonic	Inductor, Wirewound, Metal Composite, 2.5 µH, 11.9 A, 0.0084 ohm, AEC-Q200 Grade 0, SMD	8.5x8mm
19	Q1, Q2, Q3, Q4	4	40V	SQJ858AEP-T1-GE3	Vishay-Siliconix	MOSFET, N-CH, 40 V, 58 A, PowerPAK_SO-8L	PowerPAK_SO-8L
20	R1	1	10.0	CRCW060310R0FKEA	Vishay-Dale	RES, 10.0, 1%, 0.1 W, 0603	0603
21	R2, R3, R4, R5, R6, R11, R14, R21, R28, R30, R31, R32, R33	13	0	CRCW06030000Z0EA	Vishay-Dale	RES, 0, 5%, 0.1 W, 0603	0603
22	R7, R8	2	0.003	CSNL1206FT3L00	Stackpole Electronics Inc	RES, 0.003, 1%, 1 W, 1206	1206
23	R9, R10, R15, R16	4	2.21	CRCW06032R21FKEA	Vishay-Dale	RES, 2.21, 1%, 0.1 W, 0603	0603
24	R12, R13, R26, R27	4	49.9	CRCW060349R9FKEA	Vishay-Dale	RES, 49.9, 1%, 0.1 W, 0603	0603
25	R17, R18	2	10.0	CRCW080510R0FKEA	Vishay-Dale	RES, 10.0, 1%, 0.125 W, 0805	0805
26	R19	1	4.75k	CRCW06034K75FKEA	Vishay-Dale	RES, 4.75 k, 1%, 0.1 W, 0603	0603
27	R20	1	2.80k	CRCW06032K80FKEA	Vishay-Dale	RES, 2.80 k, 1%, 0.1 W, 0603	0603
28	R22, R25	2	17.4k	CRCW060317K4FKEA	Vishay-Dale	RES, 17.4 k, 1%, 0.1 W, 0603	0603
29	R23, R24	2	10.0k	CRCW060310K0FKEA	Vishay-Dale	RES, 10.0 k, 1%, 0.1 W, 0603	0603
30	TP1, TP2, TP3, TP4, TP5, TP6	6		5002	Keystone	Test Point, Miniature, White, TH	White Miniature Testpoint
31	TP7, TP8, TP9, TP10	4		1503-2	Keystone	Terminal, Turret, TH, Double	Keystone1503-2
32	U1	1		LM5140QRWGRQ1	Texas Instruments	Wide Input Range Dual Synchronous Buck Controller, RWG0040A	RWG0040A
33	U2	1		LMC7101QM5	Texas Instruments	Automotive CMOS Low Power with Rail-to-Rail Input and Output, DBV0005A	DBV0005A

IMPORTANT NOTICE FOR TI REFERENCE DESIGNS

Texas Instruments Incorporated ("TI") reference designs are solely intended to assist designers ("Designer(s)") who are developing systems that incorporate TI products. TI has not conducted any testing other than that specifically described in the published documentation for a particular reference design.

TI's provision of reference designs and any other technical, applications or design advice, quality characterization, reliability data or other information or services does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such reference designs or other items.

TI reserves the right to make corrections, enhancements, improvements and other changes to its reference designs and other items.

Designer understands and agrees that Designer remains responsible for using its independent analysis, evaluation and judgment in designing Designer's systems and products, and has full and exclusive responsibility to assure the safety of its products and compliance of its products (and of all TI products used in or for such Designer's products) with all applicable regulations, laws and other applicable requirements. Designer represents that, with respect to its applications, it has all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. Designer agrees that prior to using or distributing any systems that include TI products, Designer will thoroughly test such systems and the functionality of such TI products as used in such systems. Designer may not use any TI products in life-critical medical equipment unless authorized officers of the parties have executed a special contract specifically governing such use. Life-critical medical equipment is medical equipment where failure of such equipment would cause serious bodily injury or death (e.g., life support, pacemakers, defibrillators, heart pumps, neurostimulators, and implantables). Such equipment includes, without limitation, all medical devices identified by the U.S. Food and Drug Administration as Class III devices and equivalent classifications outside the U.S.

Designers are authorized to use, copy and modify any individual TI reference design only in connection with the development of end products that include the TI product(s) identified in that reference design. HOWEVER, NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of the reference design or other items described above may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI REFERENCE DESIGNS AND OTHER ITEMS DESCRIBED ABOVE ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY DESIGNERS AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS AS DESCRIBED IN A TI REFERENCE DESIGN OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF THE REFERENCE DESIGNS OR USE OF THE REFERENCE DESIGNS, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

TI's standard terms of sale for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>) apply to the sale of packaged integrated circuit products. Additional terms may apply to the use or sale of other types of TI products and services.

Designer will fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of Designer's non-compliance with the terms and provisions of this Notice.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2016, Texas Instruments Incorporated