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CAUTION: A is the inverted phase current. Swapped IN+ and IN- for each INA240 to optimize layout.

CAUTION: B is the inverted phase current. Swapped IN+ and IN- for each INA240 to optimize layout.

CAUTION: C is the inverted phase current. Swapped IN+ and IN- for each INA240 to optimize layout.

CAUTION: D is the inverted phase current. Swapped IN+ and IN- for each INA240 to optimize layout.

Version control disabled
SVN Rev: TIDA-00913
Number: Rev: E1
TID #: TIDA-00913
Orderable: N/A

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Note: Additional bulk caps C6, C7 are populated for test and debug. Not required for normal operation.

Note: Additional bulk caps C22, C23 are populated for test and debug.
Not required for normal operation.

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Variant/Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
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<td>001</td>
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</tr>
<tr>
<td>002</td>
<td>ChangeMe!</td>
</tr>
</tbody>
</table>

Assembly Note ZZ1

This Assembly Note is for PCB labels only.

Assembly Note ZZ2

These assemblies are ESD sensitive, ESD precautions shall be observed.

Assembly Note ZZ3

These assemblies must be clean and free from flux and all contaminants. Use of no-clean flux is not acceptable.

Assembly Note ZZ4

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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