Two phase Inductor on Top DC/DC TPS546C20 60A Max

Project Title:

Designed for:

Public Release

Assembly Variant:

001

Contact:

http://www.ti.com/support

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Drawn By:

Engineer:

Josh Mandelcorn

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Revision History

<table>
<thead>
<tr>
<th>Rev</th>
<th>ECN #</th>
<th>Approved Date</th>
<th>Approved by</th>
<th>Notes</th>
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Designator

PMP20292_2xTPS546C20.SchDoc

PMP20292_TestInterface&HDWR.SchDoc

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R12 only needed for Vout >1.5V where divider needed
C34 & C35 not populated first build
These assemblies are ESD sensitive, ESD precautions shall be observed.

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

Ground of J541 must be very near ground of J502.
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