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### Variant/Label Table

<table>
<thead>
<tr>
<th>Variant</th>
<th>Label Text</th>
</tr>
</thead>
<tbody>
<tr>
<td>001</td>
<td>Verification phase</td>
</tr>
<tr>
<td>002</td>
<td>Release phase</td>
</tr>
</tbody>
</table>

### Assembly Notes

- **ZZ1**
  - Label Assembly Note
  - This Assembly Note is for PCB labels only

- **ZZ2**
  - Assembly Note
  - These assemblies are ESD sensitive, ESD precautions shall be observed.

- **ZZ3**
  - Assembly Note
  - These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

- **ZZ4**
  - Assembly Note
  - These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
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