For remote voltage sense at main target load, slave B is intended. VIN = 4.5 - 16.5 VDC

With TPS546C23 defaults are Vref = 600mV; SS rise time 3msec and Pin 30 is Power Good
Hence, ADDR1 resistor 51.1k for "4" and ADDR0 resistor 121k for "6"

R12 only needed for Vout > 1.5V where divider needed

target Master PMBus address 38 decimal or 46 octal
Hence, ADDR1 resistor 51.1k for "4" and ADDR0 resistor 121k for "6"

For remote voltage sense at main target load
Hence, ADDR1 resistor 51.1k for "4" and ADDR0 resistor 121k for "6"

With TPS546C23 defaults are Vref = 600mV; SS rise time 3msec and Pin 30 is Power Good
Hence, for 1.2Vout EEPROM will need to be updated.

target Slave PMBus address 39 decimal or 47 octal
Hence, ADDR1 resistor 51.1k for "4" and ADDR0 resistor 163k for "7"

1.2V at 60A

C34 not populated first build
The assemblies are ESD sensitive. ESD precautions shall be observed.

These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

This design was not created for production use. You should completely validate and test your design implementation to confirm the system functionality for your application.
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