



NOTES

- DNP = "DO NOT POPULATE"
- REFDES >100 added on board
- built on PMP30364_RevC
- During startup 15V output current must be lower 5mA
Alternative: assembling D101, C102 for 15V output

Orderable:	Designed for: Public Release	Mod. Date: 12/15/2017
TID #:	Project Title: non-isolated PSR Flyback	
Number: PMP30364	Rev: C	Sheet Title:
SNV Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:	File: PMP30364_RevC.SchDoc	Size: B
Engineer: Enter name of project lead	Contact: http://www.ti.com/support	

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