

COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: [No Variations]

DESIGN INFORMATION	
MIN. TRACK WIDTH:	8 MIL
MIN. CLEARANCE:	0.2 mm
MIN. VIA PAD SIZE:	24 MIL
MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C	
REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL	
HOLE SIZE TOLERANCE (UNLESS OTHERWISE SPECIFIED): +/- 3 MIL	
MATERIAL:	
<input type="checkbox"/> FR-408 <input checked="" type="checkbox"/> FR-4 High Tg <input type="checkbox"/> OTHER _____	
THICKNESS:	<input checked="" type="checkbox"/> 62 MIL (1.6mm) +/-10% <input type="checkbox"/> OTHER _____
TOLERANCE:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____
BOW & TWIST:	<input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2 <input type="checkbox"/> OTHER +/- _____
DRILLING:	
REFERENCE:	<input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC_DRILL FILES
PTH COPPER THICKNESS:	<input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER _____
BOARD FINISH:	
SILKSCREEN:	<input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM
SILKSCREEN COLOR:	<input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER _____
SOLDER RESIST COLOR:	<input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER _____ <input checked="" type="checkbox"/> MATTE <input type="checkbox"/> SEMI-GLOSS
SURFACE FINISH:	<input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENEPIG <input type="checkbox"/> IMM. TIN/SILVER OR EQUIV <input type="checkbox"/> OTHER _____
ARRAY/PANEL:	<input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE <input type="checkbox"/> N.C. ROUTE <input checked="" type="checkbox"/> V. SCORE
CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:	
<input checked="" type="checkbox"/> ANSI IPC-A-600F CLASS -> <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3 <input checked="" type="checkbox"/> RoHS <input type="checkbox"/> OTHER PER ORDER	
ALL BOARDS MUST MEET OR EXCEED UL94-V0 REQUIREMENTS. PCB MUST BEAR THE UL94V-0 UL REGISTERED MATERIAL ID NUMBER	
ADDITIONAL REQUIREMENTS:	
MICROSECTION:	<input type="checkbox"/> YES
BARE BOARD ELEC. TEST:	<input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER
<input type="checkbox"/> XX MIL VIAS REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> XX MIL VIAS REQUIRE CONDUCTIVE FILL AND PLANARIZE	
<input type="checkbox"/> OUTER XX MIL TRACES REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE	
<input type="checkbox"/> LAYER 2 & 3 (INNER LAYERS) XX MIL WIDE, XX MIL SPACE TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE	



PROJECT TITLE:	90VAC-264VAC 310W LLC
DESIGNED FOR:	Public Release
FILE NAME:	PMP30521 Rev_A PCB.PcbDoc

PCB VIEWED FROM TOP SIDE	BOARD #: PMP30521	REV: A	SUN REV: Not In VersionControl
	TID #: N/A		
PLOT NAME = Top LayerAssembly Drawing	GENERATED : 3/27/2019 5:11:27 PM	TEXAS INSTRUMENTS	

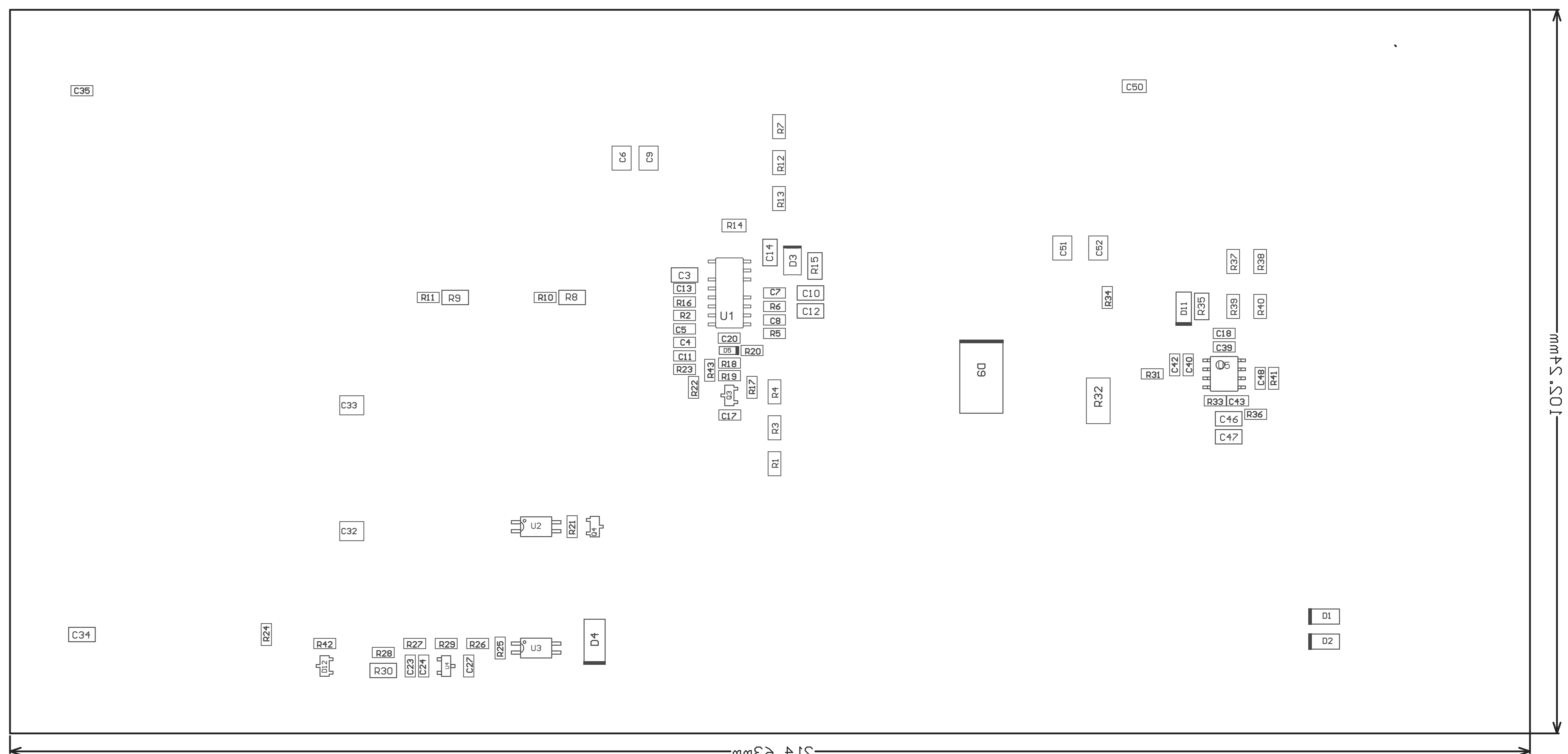
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ENGINEER:	R. Scibilia	LAYOUT BY:	R. Scibilia
SCALE: 1.00		ALTIUM DESIGNER VERSION:	18.1.9.240

SCALE: 1:100		ENGINEER: R. Scidilia		LAYOUT BY: R. Scidilia	
ALTIM DESIGNER VERSION: 18.1.9.240		FILE NAME: PMP30521_Rev_A_PCB_PcbDoc		DESIGNED FOR: Public Release	
PROJECT TITLE: SOUAC-264VAC 310W LLC		TEXAS INSTRUMENTS		DESIGNED FOR: Public Release	
<p>TRACES REQUIRE 100 OHM DIFFERENTIAL IMPEDANCE</p> <p>LAYER 2 & 3 (INNER LAYERS) XX MILL WIDE XX MILL SPACE</p> <p>OUTER XX MILL TRACES REQUIRE 50 OHM SINGLE-ENDED IMPEDANCE</p> <p>XX MILL REQUIRE CONDUCTIVE FILL AND PLANARIZE</p> <p>XX MILL REQUIRE NON-CONDUCTIVE FILL AND PLANARIZE</p> <p>BARE BOARD ELEC. TEST: <input type="checkbox"/> NONE <input checked="" type="checkbox"/> REQUIRED <input type="checkbox"/> PER ORDER</p> <p>MICROSECTION: <input type="checkbox"/> YES <input type="checkbox"/> NO</p> <p>ADDITIONAL REQUIREMENTS:</p> <p>PCB MUST BEAR THE UPL-0 UL REGISTERED MATERIAL ID NUMBER</p> <p>ALL BOARDS MUST MEET OR EXCEED UPL-0 REQUIREMENTS.</p> <p><input checked="" type="checkbox"/> ANSI IPC-A-800F CLASS -- < <input type="checkbox"/> 1 <input checked="" type="checkbox"/> 2 <input type="checkbox"/> 3</p> <p>CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:</p> <p>ARRAY PANEL: <input type="checkbox"/> CUT AND TRIM PER M1 BOARD OUTLINE</p> <p><input type="checkbox"/> IMM. TINNED OR EQUIV. <input type="checkbox"/> OTHER</p> <p>SURFACE FINISH: <input checked="" type="checkbox"/> IMMERSION GOLD (ENIG) <input type="checkbox"/> ENIG</p> <p>SOLDER RESIST COLOR: <input checked="" type="checkbox"/> GREEN <input type="checkbox"/> OTHER</p> <p>SILKSCREEN COLOR: <input checked="" type="checkbox"/> WHITE <input type="checkbox"/> OTHER</p> <p>SILKSCREEN: <input checked="" type="checkbox"/> TOP <input checked="" type="checkbox"/> BOTTOM</p> <p>BOARD FINISH: <input type="checkbox"/> OTHER +\-</p> <p>PTH COPPER THICKNESS: <input checked="" type="checkbox"/> 20-30 um <input type="checkbox"/> OTHER</p> <p>REFERENCE: <input checked="" type="checkbox"/> AS SHOWN <input checked="" type="checkbox"/> NC DRILL FILES</p> <p>DRILLING: <input type="checkbox"/> OTHER +\-</p> <p>BOW & TWIST: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2</p> <p>TOLERANCE: <input checked="" type="checkbox"/> ANSI IPC-6012 TYPE 3 CLASS 2</p> <p>THICKNESS: <input checked="" type="checkbox"/> 62 MIL (+/-10% OTHER</p> <p>MATERIAL: <input type="checkbox"/> FR-4 High Tg <input checked="" type="checkbox"/> OTHER</p> <p>MINIMUM ANNUAL RING 0.02mm (SMIL) EXTERNAL</p> <p>MIN. VIA PAD SIZE: 24 MIL</p> <p>MIN. CLEARANCE: 0.2 mm</p> <p>MIN. TRACK WIDTH: 8 MIL</p> <p>DESIGN INFORMATION</p>					

SCALE: 1:100		ENGINEER: R. Scidilia		LAYOUT BY: R. Scidilia	
ALTIM DESIGNER VERSION: 18.1.9.240		FILE NAME: PMP30521_Rev_A_PCB_PcbDoc		DESIGNED FOR: Public Release	
PROJECT TITLE: SOUAC-264VAC 310W LLC		TEXAS INSTRUMENTS		DESIGNED FOR: Public Release	
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PROJECT TITLE: SOUAC-264VAC 310W LLC		TEXAS INSTRUMENTS		DESIGNED FOR: Public Release	
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PCB VIEWED FROM BOTTOM SIDE		BOARD #: PMP30521	REV: A	SUN REV: Not In VersionControl
PLOT NAME = Bottom Layer Assembly Drawing		TID #: N/A		
GENERATED : 3/27/2019 5:11:29 PM		TEXAS INSTRUMENTS		

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