

5.2 EVM Board Layout

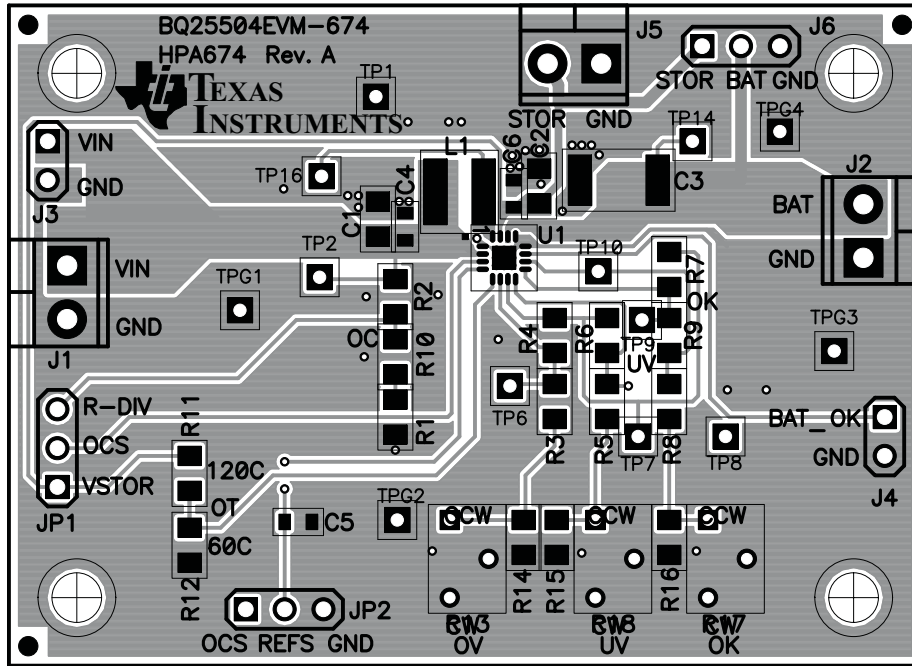


Figure 8. EVM PCB Top Assembly

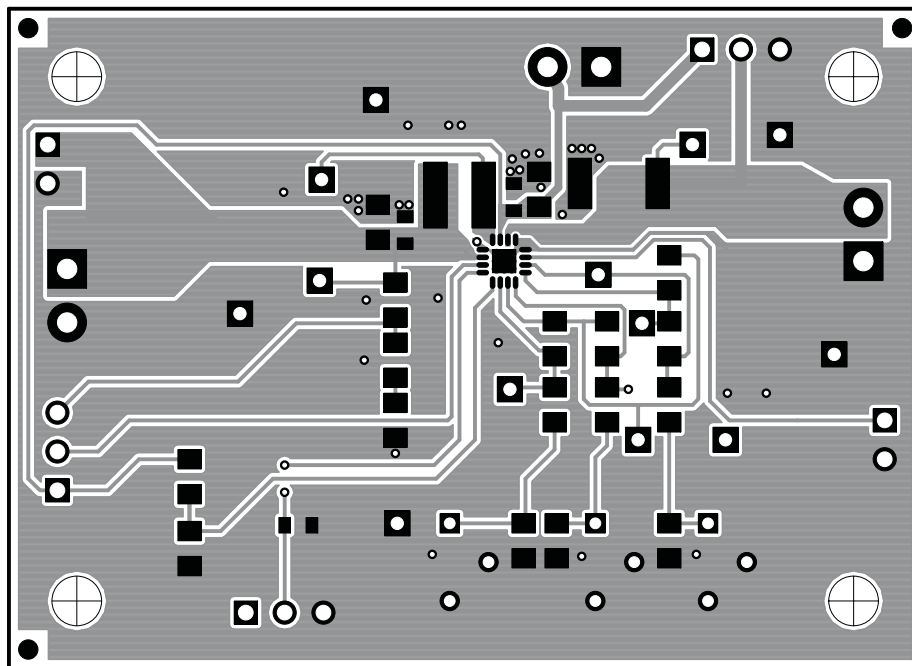


Figure 9. EVM PCB Top Layer

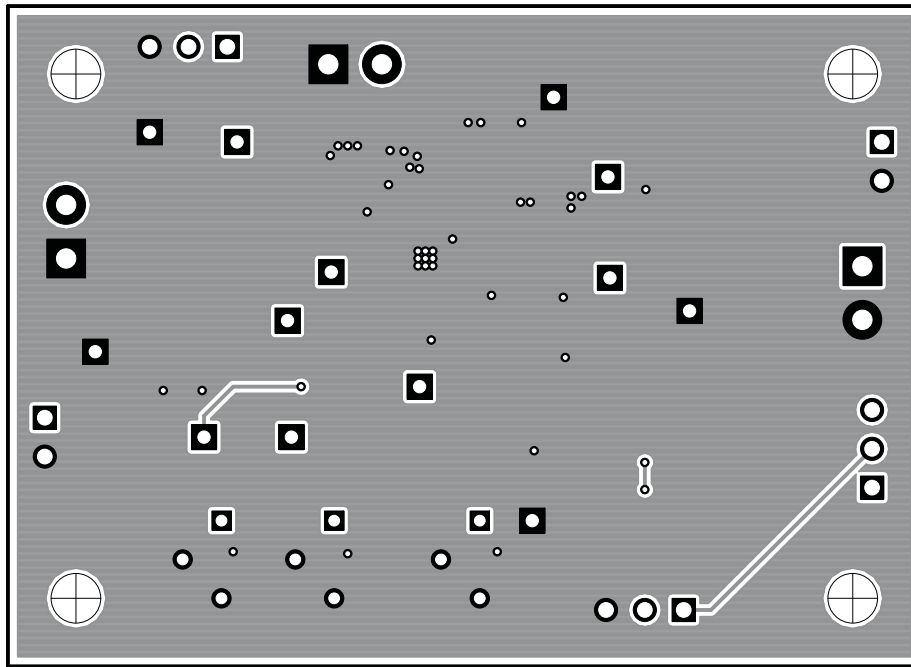


Figure 10. EVM PCB Bottom Layer

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