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RI-I11-114A-S1 RI-I11-114B-S1

SCBS822C - NOVEMBER 2006 - REVISED JUNE 2014

# RI-I11-114x-S1 Tag-it<sup>™</sup> HF-I Pro Transponder Inlays Square Not Recommended for New Designs

#### Features 1

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- ISO/IEC 15693-2, -3; ISO/IEC 18000-3 Compliant
- 13.56-MHz Operating Frequency
- 256-Bit User Memory in 8 blocks × 32-Bit
- User and Factory Lock Per Block
- Application Family Identifier (AFI)
- Fast Simultaneous Identification (Anti-Collision)
- Password Protected Write Command
- Command to Disable IC Functionality

#### 2 Applications

- **Product Authentication**
- Ticketing
- Stored Value

# 3 Description

Texas Instruments Tag-it<sup>™</sup> HF-I pro transponder inlays consist of 13.56-MHz high-frequency (HF) transponders that are compliant with the ISO/IEC 15693 and ISO/IEC 18000-3 global open standards. These products offer a user-accessible memory of 256 bits, organized in eight blocks, and an extended command set including password protect write available in five different antenna shapes, with frequency offset for integration into paper, PVC, or other substrates.

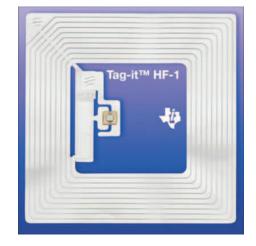
The Tag-it HF-I pro transponder inlays are manufactured with TI's patented laser tuning process to provide consistent read performance. Prior to delivery, the transponders undergo complete functional and parametric testing, in order to provide the high quality that customers have come to expect from TI.

The Tag-it HF-I pro transponder inlays are well suited for a variety of applications including, but not limited to, product authentication, library, supply-chain management, management, asset and ticketing/stored value applications.

### Device Information<sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RI-I11-114A-S1	TFB	45.00 mm x 45.00 mm
RI-I11-114B-S1	TFB	45.00 mm x 45.00 mm

(1)For all available packages, see the orderable addendum at the end of the datasheet.





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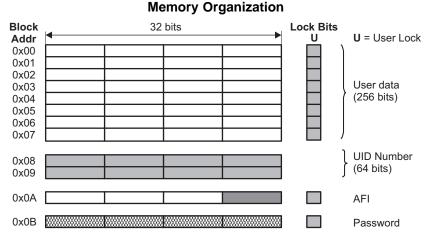
# 4 Revision History

Cł	Changes from Revision B (Sept 2011) to Revision C Page									
•	Changed feature from 8 bits x 32-bit blocks to 8 blocks x 32-Bit	1								
•	Changed spec from 8 bits x 32-bit blocks to 8 blocks x 32-Bit	3	6							

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# 5 Pin Configuration and Functions



# 6 Specifications

## Table 1. Specifications<sup>(1)</sup>

	PART NUMB	ER				
	RI-I11-114A-S1	RI-I11-114B-S1				
Supported standard	ISO/IEC 15693-2, -3; ISO	D/IEC 18000-3				
Recommended operating frequency	13.56 MHz					
Passive resonance frequency (at 25°C)	13.86 MHz ±200 kHz (includes frequency offset to compensate further integration into paper)	14.4 MHz ±200 kHz (includes frequency offset to compensate PVC lamination)				
Typical required activation field strength to read (at 25°C)	98 dBµA/m <sup>(2)</sup>	98 dBµA/m <sup>(3)</sup>				
Typical required activation field strength to write (at 25°C)	101 dBµA/m <sup>(2)</sup>	101 dBµA/m <sup>(3)</sup>				
Factory-programmed read-only number	64 bits					
Memory (user programmable)	256 bits organized in 8 b	olocks × 32-Bit				
Typical programming cycles (at 25°C)	100,000					
Data retention time (at 55°C)	>10 years					
Simultaneous identification of tags	Up to 50 tags per second (reader/antenna dependent)					
Antenna size	45 mm × 45 mm (~1.77	in × ~1.77 in)				
Foil width	48 mm ± 0.5 mm (1.89	in ± 0.02 in)				
Foil pitch	50.8 mm +0.1 mm/–0	.4 mm (2 in)				
Base material	Substrate: PET (polyethylenethereph	talate); Antenna: aluminum				
Thickness	Chip area: 0.34 m Antenna area (Al both sides Antenna area (Al one side):	): 0.085 mm ±0.01				
Operating temperature	–25°C to 70	C				
Storage temperature (single inlay)	-40°C to 85°C (warpage may occur a	t upper temperature range)				
Storage temperature (on reel)	-40°C to 40°C					
Delivery	Single-row tape wound on cardboard reel with 500-mm diameter Reel outer width: approximately 60 mm (about 2.36 inches) Reel inner width: approximately 50 mm (about 1.97 inches) Hub diameter: 76.2 mm (3 in)					
Typical quantity of good units per reel	5000					

(1) For highest possible read-out coverage, operate readers at a modulation depth of 20% or higher.

(2) After integration into paper

(3) After PVC lamination

## RI-I11-114A-S1 RI-I11-114B-S1

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## Table 2. Supported Command Set

DEOLICET	REQUEST MODE <sup>(1)</sup>								
REQUEST	REQUEST CODE	INVENTORY	ADDRESSED	NON-ADDRESSED	AFI	OPT. FLAG			
ISO 15693 Mandatory and Optional Commands									
Inventory	0x01	1	-	-	1	0			
Stay Quiet	0x02	-	$\checkmark$	-	_	0			
Read_Single_Block	0x20	-	$\checkmark$	1	_	1			
Write_Single_Block	0x21	-	$\checkmark$	1	-	1			
Lock_Block	0x22	-	$\checkmark$	1	_	1			
TI Custom Commands									
Kill	0xA4	-	$\checkmark$	-	-	1			
WriteSingleBlockPwd	0xA5	-	$\checkmark$	-	-	1			

(1)  $\checkmark$  = Implemented, - = Not applicable

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## 7 Device and Documentation Support

## 7.1 Documentation Support

### 7.1.1 Related Links

The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

PARTS	PRODUCT FOLDER	SAMPLE & BUY	TECHNICAL DOCUMENTS	TOOLS & SOFTWARE	SUPPORT & COMMUNITY	
RI-I11-114A-S1	Click here	Click here	Click here	Click here	Click here	
RI-I11-114B-S1	Click here	Click here	Click here	Click here	Click here	

#### Table 3. Related Links

## 7.2 Trademarks

Tag-it is a trademark of Texas Instruments.

## 7.3 Electrostatic Discharge Caution



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

## 7.4 Glossary

SLYZ022 — TI Glossary.

This glossary lists and explains terms, acronyms, and definitions.

## 8 Mechanical, Packaging, and Orderable Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.



1-Jun-2017

# PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
RI-I11-114B-S1	NRND	RFIDN	TFB	0		TBD	Call TI	Call TI	-25 to 70		

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

<sup>(3)</sup> MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

<sup>(4)</sup> There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

<sup>(5)</sup> Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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