1 Features
- Ultra-small X2SON, SOT-23, DSBGA or TO-92 package
- High magnetic sensitivity: ±2 mT (typical)
- Robust hysteresis: 4 mT (typical)
- Fast sensing bandwidth: 30-kHz
- V\text{CC} operating range: 2.5-V to 5.5-V
- Push-pull CMOS output
  - Capable of 5-mA sourcing, 20-mA sinking
- Operating temperature: −40°C to +135°C

2 Applications
- Brushless dc motor sensors
- Incremental rotary encoding:
  - Brushed dc motor feedback
  - Motor speed (tachometer)
  - Mechanical travel
  - Fluid measurement
  - Knob turning
  - Wheel speed
- E-bikes
- Flow meters

3 Description
The DRV5011 device is a digital-latch Hall effect sensor designed for motors and other rotary systems. Thedevice has an efficient low-voltage architecture that operates from 2.5 V to 5.5 V. The device is offered in standard SOT-23, low-profile X2SON, DSBGA and TO-92 packages. The output is a push-pull driver that requires no pullup resistor, enabling more compact systems.

When a south magnetic pole is near the top of the package and the B\text{OP} threshold is exceeded, the device drives a low voltage. The output stays low until a north pole is applied and the B\text{RP} threshold is crossed, which causes the output to drive a high voltage. Alternating north and south poles are required to toggle the output, and integrated hysteresis separates B\text{OP} and B\text{RP} to provide robust switching.

The device produces consistent performance across a wide ambient temperature range of −40°C to +135°C.

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>DRV5011</td>
<td>DSBGA (4)</td>
<td>0.80 mm × 0.80 mm</td>
</tr>
<tr>
<td></td>
<td>SOT-23 (3)</td>
<td>2.92 mm × 1.30 mm</td>
</tr>
<tr>
<td></td>
<td>X2SON (4)</td>
<td>1.10 mm × 1.40 mm</td>
</tr>
<tr>
<td></td>
<td>TO-92 (3)</td>
<td>4.00 mm × 3.15 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the package option addendum at the end of the data sheet.
Table of Contents

1 Features .................................................................. 1
2 Applications ........................................................... 1
3 Description ............................................................. 1
4 Revision History ..................................................... 2
5 Pin Configuration and Functions ......................... 3
6 Specifications .......................................................... 4
   6.1 Absolute Maximum Ratings .................................. 4
   6.2 ESD Ratings ....................................................... 4
   6.3 Recommended Operating Conditions .................. 4
   6.4 Thermal Information .......................................... 5
   6.5 Electrical Characteristics .................................... 5
   6.6 Magnetic Characteristics .................................... 5
   6.7 Typical Characteristics ...................................... 6
7 Detailed Description .............................................. 7
   7.1 Overview .......................................................... 7
   7.2 Functional Block Diagram .................................. 7
   7.3 Feature Description ........................................... 7
   7.4 Device Functional Modes ................................... 10
8 Application and Implementation ....................... 11
   8.1 Application Information ...................................... 11
   8.2 Typical Applications ......................................... 11
   8.3 Dos and Don'ts ................................................ 14
9 Power Supply Recommendations .................. 15
10 Layout ................................................................. 15
   10.1 Layout Guidelines ............................................ 15
   10.2 Layout Examples ............................................. 15
11 Device and Documentation Support ........ 16
   11.1 Device Support ................................................ 16
   11.2 Documentation Support .................................. 16
   11.3 Receiving Notification of Documentation Updates 16
   11.4 Community Resources .................................... 16
   11.5 Trademarks ..................................................... 16
   11.6 Electrostatic Discharge Caution ....................... 16
   11.7 Glossary ......................................................... 16
12 Mechanical, Packaging, and Orderable Information ........................................................................ 16

4 Revision History

Changes from Revision A (April 2019) to Revision B Page

• Added LPG (TO-92) package to the data sheet .................................................................................................................... 1

Changes from Original (December 2017) to Revision A Page

• Added YBH (DSBGA) package to data sheet ........................................................................................................................ 1

• Added recommendation to limit power supply voltage variation to less than 50 mV_{pp} to Power Supply Recommendations section ........................................................................................................ 15
## 5 Pin Configuration and Functions

### Pin Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>GND</td>
<td>A1</td>
<td>Ground reference</td>
</tr>
<tr>
<td>NC</td>
<td>A2</td>
<td>No-connect. This pin is not connected to the silicon. Leave this pin floating or tied to ground, and soldered to the board for mechanical support.</td>
</tr>
<tr>
<td>OUT</td>
<td>B2</td>
<td>Push-pull CMOS output. Drives a ( V_{CC} ) or ground level.</td>
</tr>
<tr>
<td>( V_{CC} )</td>
<td>B1</td>
<td>2.5-V to 5.5-V power supply. TI recommends connecting this pin to a ceramic capacitor to ground with a value of at least 0.01 ( \mu F ).</td>
</tr>
<tr>
<td>Thermal Pad</td>
<td>—</td>
<td>Leave thermal pad floating or tied to ground, and soldered to the board for mechanical support.</td>
</tr>
</tbody>
</table>

### Diagrams
- **DBZ Package**: 3-Pin SOT-23 Top View
- **DMR Package**: 4-Pin X2SON With Exposed Thermal Pad Top View
- **YBH Package**: 4-Pin DSBGA Top View
- **LPG Package**: 3-Pin TO-92 Top View
6 Specifications

6.1 Absolute Maximum Ratings
over operating free-air temperature range (unless otherwise noted)\(^{(1)}\)

<table>
<thead>
<tr>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{CC})</td>
<td>Power-supply voltage</td>
<td>(V_{CC})</td>
</tr>
<tr>
<td>(V_{O})</td>
<td>Output voltage</td>
<td>OUT</td>
</tr>
<tr>
<td>(I_{O})</td>
<td>Output current</td>
<td>OUT</td>
</tr>
<tr>
<td>(B)</td>
<td>Magnetic flux density</td>
<td>Unlimited</td>
</tr>
<tr>
<td>(T_{J})</td>
<td>Operating junction temperature</td>
<td>Unlimited</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

6.2 ESD Ratings

<table>
<thead>
<tr>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{(ESD)})</td>
<td>Electrostatic discharge</td>
</tr>
<tr>
<td>Human-body model (HBM), per ANSI/ESDA/JEDEC JS-001(^{(1)})</td>
<td>(\pm6000)</td>
</tr>
<tr>
<td>Charged-device model (CDM), per JEDEC specification JESD22-C101(^{(2)})</td>
<td>(\pm750)</td>
</tr>
</tbody>
</table>

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions
over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>(V_{CC})</td>
<td>Power supply voltage</td>
<td>(V_{CC})</td>
</tr>
<tr>
<td>(V_{O})</td>
<td>Output voltage</td>
<td>OUT</td>
</tr>
<tr>
<td>(I_{O})</td>
<td>Output current(^{(1)})</td>
<td>OUT</td>
</tr>
<tr>
<td>(T_{J})</td>
<td>Operating junction temperature</td>
<td>Unlimited</td>
</tr>
<tr>
<td>(T_{A})</td>
<td>Operating ambient temperature</td>
<td>For SOT-23 (DBZ), X2SON (DMR) and TO-92 (LPG)</td>
</tr>
<tr>
<td></td>
<td>For DSBGA (YBH)</td>
<td>(-40)</td>
</tr>
</tbody>
</table>

(1) Device-sourced current is negative. Device-sunk current is positive.
6.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC(1)</th>
<th>DBZ (SOT-23)</th>
<th>DMR (X2SON)</th>
<th>YBH (DSBGA)</th>
<th>LPG (TO-92)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Junction-to-ambient thermal resistance</td>
<td>356</td>
<td>159</td>
<td>194.1</td>
<td>183.1</td>
</tr>
<tr>
<td>Junction-to-case (top) thermal resistance</td>
<td>128</td>
<td>77</td>
<td>1.6</td>
<td>74.2</td>
</tr>
<tr>
<td>Junction-to-board thermal resistance</td>
<td>94</td>
<td>102</td>
<td>68</td>
<td>158.8</td>
</tr>
<tr>
<td>Junction-to-top characterization parameter</td>
<td>11.4</td>
<td>0.9</td>
<td>0.8</td>
<td>15.2</td>
</tr>
<tr>
<td>Junction-to-board characterization parameter</td>
<td>92</td>
<td>100</td>
<td>67.9</td>
<td>158.8</td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see the Semiconductor and IC Package Thermal Metrics application report.

6.5 Electrical Characteristics

for $V_{CC} = 2.5$ V to 5.5 V, over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{CC}$</td>
<td>Operating supply current</td>
<td>2.3</td>
<td>3</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>$t_{ON}$</td>
<td>Power-on time (see Figure 10)</td>
<td>40</td>
<td>70</td>
<td>µs</td>
<td></td>
</tr>
<tr>
<td>$t_d$</td>
<td>Propagation delay time</td>
<td>13</td>
<td>25</td>
<td>µs</td>
<td></td>
</tr>
<tr>
<td>$V_{OH}$</td>
<td>High-level output voltage $I_O = -1$ mA</td>
<td>$V_{CC} - 0.35$</td>
<td>$V_{CC} - 0.1$</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$V_{OL}$</td>
<td>Low-level output voltage $I_O = 20$ mA</td>
<td>0.15</td>
<td>0.4</td>
<td>V</td>
<td></td>
</tr>
</tbody>
</table>

6.6 Magnetic Characteristics

for $V_{CC} = 2.5$ V to 5.5 V, over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$f_{BW}$</td>
<td>Sensing bandwidth</td>
<td>30</td>
<td></td>
<td></td>
<td>kHz</td>
</tr>
<tr>
<td>$B_{OP}$</td>
<td>Magnetic threshold operate point (see Figure 8)</td>
<td>0.6</td>
<td>2</td>
<td>3.8</td>
<td>mT</td>
</tr>
<tr>
<td>$B_{RP}$</td>
<td>Magnetic threshold release point (see Figure 8)</td>
<td>$-3.8$</td>
<td>$-2$</td>
<td>$-0.6$</td>
<td>mT</td>
</tr>
<tr>
<td>$B_{HYS}$</td>
<td>Magnetic hysteresis: $</td>
<td>B_{OP} - B_{RP}</td>
<td>$</td>
<td>2</td>
<td>4</td>
</tr>
</tbody>
</table>
6.7 Typical Characteristics

Figure 1. $B_{OP}$ vs Temperature

Figure 2. $B_{RP}$ vs Temperature

Figure 3. $B_{OP}$ vs $V_{CC}$

Figure 4. $B_{RP}$ vs $V_{CC}$

Figure 5. $I_{CC}$ vs Temperature
7 Detailed Description

7.1 Overview
The DRV5011 is a magnetic sensor with a digital output that latches the most recent pole measured. Applying a south magnetic pole near the top of the package causes the output to drive low, whereas a north magnetic pole causes the output to drive high, and the absence of a magnetic field causes the output to continue to drive the previous state, whether low or high.

The device integrates a Hall effect element, analog signal conditioning, offset cancellation circuits, amplifiers, and comparators. This provides stable performance across a wide temperature range and resistance to mechanical stress.

7.2 Functional Block Diagram

7.3 Feature Description

7.3.1 Magnetic Flux Direction
The DRV5011 is sensitive to the magnetic field component that is perpendicular to the top of the package, as shown in Figure 6.
Feature Description (continued)

The magnetic flux that travels from the bottom to the top of the package is considered positive in this data sheet. This condition exists when a south magnetic pole is near the top of the package. The magnetic flux that travels from the top to the bottom of the package results in negative millitesla values. Figure 7 shows the flux direction polarity.

![Flux Direction Polarity Diagram](image)

Figure 7. Flux Direction Polarity

7.3.2 Magnetic Response

Figure 8 shows the device functionality and hysteresis.

![Device Functionality Diagram](image)

Figure 8. Device Functionality
Feature Description (continued)

7.3.3 Output Driver

Figure 9 shows the device push-pull CMOS output that can drive a $V_{CC}$ or ground level.

![Diagram of push-pull output](image)

Figure 9. Push-Pull Output (Simplified)

7.3.4 Power-On Time

Figure 10 shows that after the $V_{CC}$ voltage is applied, the DRV5011 measures the magnetic field and sets the output within the $t_{ON}$ time.

![Diagram of t_ON definition](image)

Figure 10. $t_{ON}$ Definition
Feature Description (continued)

7.3.5 Hall Element Location

The sensing element inside the device is in the center of both packages when viewed from the top. Figure 11 shows the tolerances and side-view dimensions.

Figure 11. Hall Element Location

7.4 Device Functional Modes

The DRV5011 has one mode of operation that applies when the Recommended Operating Conditions are met.
8 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information
The DRV5011 is typically used in rotary applications for brushless DC (BLDC) motor sensors or incremental rotary encoding.

For reliable functionality, the magnet must apply a flux density at the sensor greater than the maximum $B_{OP}$ and less than the minimum $B_{RP}$ thresholds. Add additional margin to account for mechanical tolerance, temperature effects, and magnet variation. Magnets generally produce weaker fields as temperature increases.

8.2 Typical Applications

8.2.1 BLDC Motor Sensors Application

![BLDC Motor System Diagram]

Copyright © 2017, Texas Instruments Incorporated

Figure 12. BLDC Motor System

8.2.1.1 Design Requirements
For this design example, use the parameters listed in Table 1.

<table>
<thead>
<tr>
<th>DESIGN PARAMETER</th>
<th>EXAMPLE VALUE</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of motor phases</td>
<td>3</td>
</tr>
<tr>
<td>Motor RPM</td>
<td>15 k</td>
</tr>
<tr>
<td>Number of magnet poles on the rotor</td>
<td>12</td>
</tr>
<tr>
<td>Magnetic material</td>
<td>Bonded Neodymium</td>
</tr>
<tr>
<td>Maximum temperature inside the motor</td>
<td>125°C</td>
</tr>
<tr>
<td>Magnetic flux density peaks at the Hall sensors at maximum temperature</td>
<td>±11 mT</td>
</tr>
<tr>
<td>Hall sensor $V_{CC}$</td>
<td>5 V ±10%</td>
</tr>
</tbody>
</table>
8.2.1.2 Detailed Design Procedure

Three-phase brushless DC motors often use three Hall effect latch devices to measure the electrical angle of the rotor and tell the controller how to drive the three wires. These wires connect to electromagnet windings, which generate magnetic fields that apply forces to the permanent magnets on the rotor.

Space the three Hall sensors across the printed-circuit board (PCB) so that they are 120 electrical degrees apart. This configuration creates six 3-bit states with equal time duration for each electrical cycle, which consists of one north and one south magnetic pole. From the center of the motor axis, the number of degrees to space each sensor equals \( \frac{2}{\text{number of poles}} \times 120° \). In this design example, the first sensor is placed at 0°, the second sensor is placed 20° rotated, and the third sensor is placed 40° rotated. Alternatively, a 3× degree offset can be added or subtracted to any sensor, meaning the third sensor could alternatively be placed at 40° – (3 × 20°) = –20°.

8.2.1.3 Application Curve

![Figure 13. Phase Voltages and Hall Signals for 3-Phase BLDC Motor](image-url)
### 8.2.2 Incremental Rotary Encoding Application

Figure 14. Incremental Rotary Encoding System

#### 8.2.2.1 Design Requirements

For this design example, use the parameters listed in Table 2.

<table>
<thead>
<tr>
<th>DESIGN PARAMETER</th>
<th>EXAMPLE VALUE</th>
</tr>
</thead>
<tbody>
<tr>
<td>RPM range</td>
<td>0 to 45 k</td>
</tr>
<tr>
<td>Number of magnet poles</td>
<td>8</td>
</tr>
<tr>
<td>Magnetic material</td>
<td>Ferrite</td>
</tr>
<tr>
<td>Air gap above the Hall sensors</td>
<td>2.5 mm</td>
</tr>
<tr>
<td>Magnetic flux density peaks at the Hall sensors at maximum temperature</td>
<td>±7 mT</td>
</tr>
</tbody>
</table>

#### 8.2.2.2 Detailed Design Procedure

Incremental encoders are used on knobs, wheels, motors, and flow meters to measure relative rotary movement. By attaching a ring magnet to the rotating component and placing a DRV5011 nearby, the sensor generates voltage pulses as the magnet turns. If directional information is also needed (clockwise versus counterclockwise), a second DRV5011 can be added with a phase offset, and then the order of transitions between the two signals describes the direction.

Creating this phase offset requires spacing the two sensors apart on the PCB, and an ideal $90^\circ$ quadrature offset is attained when the sensors are separated by half the length of each magnet pole, plus any integer number of pole lengths. Figure 14 shows this configuration, as the sensors are 1.5 pole lengths apart. One of the sensors changes its output every $360^\circ / 8$ poles / 2 sensors = $22.5^\circ$ of rotation. For reference, TI Design TIDA-00480, *Automotive Hall Sensor Rotary Encoder*, uses a 66-pole magnet with changes every $2.7^\circ$.

The maximum rotational speed that can be measured is limited by the sensor bandwidth. Generally, the bandwidth must be faster than two times the number of poles per second. In this design example, the maximum speed is 45000 RPM, which involves 6000 poles per second. The DRV5011 sensing bandwidth is 30 kHz, which is five times the pole frequency. In systems where the sensor sampling rate is close to two times the number of poles per second, most of the samples measure a magnetic field that is significantly lower than the peak value, because the peaks only occur when the sensor and pole are perfectly aligned. In this case, add margin by applying a stronger magnetic field that has peaks significantly higher than the maximum $B_{OP}$. 
8.2.2.3 Application Curve
Two signals in quadrature provide movement and direction information. Figure 15 shows how each 2-bit state has unique adjacent 2-bit states for clockwise and counterclockwise.

![Figure 15. Quadrature Output (2-Bit)](image)

8.3 Dos and Don'ts
The Hall element is sensitive to magnetic fields that are perpendicular to the top of the package; therefore, the correct magnet orientation must be used for the sensor to detect the field. Figure 16 shows correct and incorrect orientations when using a ring magnet.

![Figure 16. Correct and Incorrect Magnet Orientations](image)
9 Power Supply Recommendations

The DRV5011 is powered from 2.5-V to 5.5-V dc power supplies. A 0.01-μF (minimum) ceramic capacitor rated for \( V_{CC} \) must be placed as close to the DRV5011 device as possible. Larger values of the bypass capacitor may be needed to attenuate any significant high-frequency ripple and noise components generated by the power source. TI recommends limiting the supply voltage variation to less than 50 mV_{PP}.

10 Layout

10.1 Layout Guidelines

Magnetic fields pass through most nonferromagnetic materials with no significant disturbance. Embedding Hall effect sensors within plastic or aluminum enclosures and sensing magnets on the outside is common practice. Magnetic fields also easily pass through most PCBs, which makes placing the magnet on the opposite side possible.

10.2 Layout Examples

![Layout Examples](image-url)

Figure 17. Layout Examples
11 Device and Documentation Support

11.1 Device Support

11.1.1 Development Support
For additional design reference, see the Automotive Hall Sensor Rotary Encoder TI design (TIDA-00480).
TI also offers the following evaluation modules (EVMs) for the DRV5011:
• Texas Instruments, DRV5011 Ultra-Low Power, Digital-Latch Hall Effect Sensor Evaluation Module
• Texas Instruments, Breakout Adapter for SOT-23 and TO-92 Hall Sensor Evaluation

11.2 Documentation Support

11.2.1 Related Documentation
For related documentation see the following:
• DRV5011-5012EVM user's guide
• HALL-ADAPTER-EVM user's guide

11.3 Receiving Notification of Documentation Updates
To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on Alert me to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

11.4 Community Resources
TI E2E™ support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

11.5 Trademarks
E2E is a trademark of Texas Instruments.
All other trademarks are the property of their respective owners.

11.6 Electrostatic Discharge Caution
This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

11.7 Glossary
SLYZ022 — Ti Glossary.
This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
## PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>DRV5011ADDBZR</td>
<td>ACTIVE</td>
<td>SOT-23</td>
<td>DBZ</td>
<td>3</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 135</td>
<td>1AD</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADDBZT</td>
<td>ACTIVE</td>
<td>SOT-23</td>
<td>DBZ</td>
<td>3</td>
<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 135</td>
<td>1AD</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADDMRR</td>
<td>ACTIVE</td>
<td>X2SON</td>
<td>DMR</td>
<td>4</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 135</td>
<td>1AD</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADDMRT</td>
<td>ACTIVE</td>
<td>X2SON</td>
<td>DMR</td>
<td>4</td>
<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 135</td>
<td>1AD</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADLPG</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LPG</td>
<td>3</td>
<td>1000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>-40 to 135</td>
<td>11AD</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADLPGM</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LPG</td>
<td>3</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>-40 to 135</td>
<td>11AD</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADYBHR</td>
<td>ACTIVE</td>
<td>DSBGA</td>
<td>YBH</td>
<td>4</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SAC396</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 125</td>
<td>A</td>
<td>Samples</td>
</tr>
<tr>
<td>DRV5011ADYBHT</td>
<td>ACTIVE</td>
<td>DSBGA</td>
<td>YBH</td>
<td>4</td>
<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SAC396</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 125</td>
<td>A</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
- **RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
- **Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
# TAPE AND REEL INFORMATION

## TAPE DIMENSIONS

| A0 | Dimension designed to accommodate the component width |
| B0 | Dimension designed to accommodate the component length |
| K0 | Dimension designed to accommodate the component thickness |
| W  | Overall width of the carrier tape |
| P1 | Pitch between successive cavity centers |

## REEL DIMENSIONS

- Reel Diameter
- Reel Width (W1)

## QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

- Sprocket Holes
- User Direction of Feed
- Pocket Quadrants

*All dimensions are nominal.*

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
</thead>
<tbody>
<tr>
<td>DRV5011ADDBZR</td>
<td>SOT-23</td>
<td>DBZ</td>
<td>3</td>
<td>3000</td>
<td>180.0</td>
<td>8.4</td>
<td>3.15</td>
<td>2.77</td>
<td>1.22</td>
<td>4.0</td>
<td>8.0</td>
<td>Q3</td>
</tr>
<tr>
<td>DRV5011ADDBZT</td>
<td>SOT-23</td>
<td>DBZ</td>
<td>3</td>
<td>250</td>
<td>180.0</td>
<td>8.4</td>
<td>3.15</td>
<td>2.77</td>
<td>1.22</td>
<td>4.0</td>
<td>8.0</td>
<td>Q3</td>
</tr>
<tr>
<td>DRV5011ADDMRR</td>
<td>X2SON</td>
<td>DMR</td>
<td>4</td>
<td>3000</td>
<td>180.0</td>
<td>8.4</td>
<td>1.27</td>
<td>1.57</td>
<td>0.5</td>
<td>4.0</td>
<td>8.0</td>
<td>Q1</td>
</tr>
<tr>
<td>DRV5011ADDMRT</td>
<td>X2SON</td>
<td>DMR</td>
<td>4</td>
<td>250</td>
<td>180.0</td>
<td>8.4</td>
<td>1.27</td>
<td>1.57</td>
<td>0.5</td>
<td>4.0</td>
<td>8.0</td>
<td>Q1</td>
</tr>
<tr>
<td>DRV5011ADYBHR</td>
<td>DSBGA</td>
<td>YBH</td>
<td>4</td>
<td>3000</td>
<td>180.0</td>
<td>8.4</td>
<td>0.85</td>
<td>0.89</td>
<td>0.51</td>
<td>2.0</td>
<td>8.0</td>
<td>Q2</td>
</tr>
<tr>
<td>DRV5011ADYBHT</td>
<td>DSBGA</td>
<td>YBH</td>
<td>4</td>
<td>250</td>
<td>180.0</td>
<td>8.4</td>
<td>0.85</td>
<td>0.89</td>
<td>0.51</td>
<td>2.0</td>
<td>8.0</td>
<td>Q2</td>
</tr>
</tbody>
</table>
### TAPE AND REEL BOX DIMENSIONS

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>DRV5011ADDBZR</td>
<td>SOT-23</td>
<td>DBZ</td>
<td>3</td>
<td>3000</td>
<td>183.0</td>
<td>183.0</td>
<td>20.0</td>
</tr>
<tr>
<td>DRV5011ADDBZT</td>
<td>SOT-23</td>
<td>DBZ</td>
<td>3</td>
<td>250</td>
<td>183.0</td>
<td>183.0</td>
<td>20.0</td>
</tr>
<tr>
<td>DRV5011ADDMRR</td>
<td>X2SON</td>
<td>DMRR</td>
<td>4</td>
<td>3000</td>
<td>203.0</td>
<td>203.0</td>
<td>35.0</td>
</tr>
<tr>
<td>DRV5011ADDMRT</td>
<td>X2SON</td>
<td>DMRR</td>
<td>4</td>
<td>250</td>
<td>203.0</td>
<td>203.0</td>
<td>35.0</td>
</tr>
<tr>
<td>DRV5011ADYBHR</td>
<td>DSBGA</td>
<td>YBH</td>
<td>4</td>
<td>3000</td>
<td>182.0</td>
<td>182.0</td>
<td>20.0</td>
</tr>
<tr>
<td>DRV5011ADYBHT</td>
<td>DSBGA</td>
<td>YBH</td>
<td>4</td>
<td>250</td>
<td>182.0</td>
<td>182.0</td>
<td>20.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Reference JEDEC registration TO-236, except minimum foot length.
NOTES: (continued)

4. Publication IPC-7351 may have alternate designs.
5. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

7. Board assembly site may have different recommendations for stencil design.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

5. Vias are optional depending on application, refer to device data sheet. If all or some are implemented, recommended via locations are shown. It is recommended that vias under paste be filled, plugged or tented.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
EXAMPLE BOARD LAYOUT

TO-92 - 5.05 mm max height

TRANSISTOR OUTLINE

LAND PATTERN EXAMPLE
NON-SOLDER MASK DEFINED
SCALE:20X
NOTES:
1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
NOTES: (continued)

3. Final dimensions may vary due to manufacturing tolerance considerations and also routing constraints. See Texas Instruments Literature No. SNVA009 (www.ti.com/lit/snva009).
NOTES: (continued)

4. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release.
IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2020, Texas Instruments Incorporated