LM317 3-Terminal Adjustable Regulator

1 Features

- Output voltage range adjustable from 1.25 V to 37 V
- Output current greater than 1.5 A
- Internal short-circuit current limiting
- Thermal overload protection
- Output safe-area compensation

2 Applications

- ATCA solutions
- DLP: 3D biometrics, hyperspectral imaging, optical networking, and spectroscopy
- DVR and DVS
- Desktop PCs
- Digital signage and still cameras
- ECG electrocardiograms
- EV HEV chargers: levels 1, 2, and 3
- Electronic shelf labels
- Energy harvesting
- Ethernet switches
- Femto base stations
- Fingerprint and iris biometrics
- HVAC: heating, ventilating, and air conditioning
- High-speed data acquisition and generation
- Hydraulic valves
- IP phones: wired and wireless
- Intelligent occupancy sensing
- Motor controls: brushed DC, brushless DC, low-voltage, permanent magnet, and stepper motors
- Point-to-point microwave backhauls
- Power bank solutions
- Power line communication modems
- Power over ethernet (PoE)
- Power quality meters
- Power substation controls
- Private branch exchanges (PBX)
- Programmable logic controllers
- RFID readers
- Refrigerators
- Signal or waveform generators
- Software-defined radios (SDR)
- Washing machines: high-end and low-end
- X-rays: baggage scanners, medical, and dental

3 Description

The LM317 device is an adjustable three-terminal positive-voltage regulator capable of supplying more than 1.5 A over an output-voltage range of 1.25 V to 37 V. It requires only two external resistors to set the output voltage. The device features a typical line regulation of 0.01% and typical load regulation of 0.1%. It includes current limiting, thermal overload protection, and safe operating area protection. Overload protection remains functional even if the ADJUST terminal is disconnected.

Device Information(1)

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM317DCY</td>
<td>SOT-223 (4)</td>
<td>6.50 mm × 3.50 mm</td>
</tr>
<tr>
<td>LM317KCS</td>
<td>TO-220 (3)</td>
<td>10.16 mm × 9.15 mm</td>
</tr>
<tr>
<td>LM317KCT</td>
<td>TO-220 (3)</td>
<td>10.16 mm × 8.59 mm</td>
</tr>
<tr>
<td>LM317KTT</td>
<td>TO-263 (3)</td>
<td>10.16 mm × 9.01 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Battery-Charger Circuit

![Battery-Charger Circuit Diagram](image-url)
Table of Contents

1 Features ................................................................. 1
2 Applications ............................................................ 1
3 Description ............................................................. 1
4 Revision History ....................................................... 2
5 Device Comparison Table ........................................... 3
6 Pin Configuration and Functions ................................. 4
7 Specifications .......................................................... 5
   7.1 Absolute Maximum Ratings ................................. 5
   7.2 ESD Ratings ....................................................... 5
   7.3 Recommended Operating Conditions ....................... 5
   7.4 Thermal Information ........................................... 5
   7.5 Electrical Characteristics ..................................... 6
   7.6 Typical Characteristics ........................................ 7
8 Detailed Description ................................................ 9
   8.1 Overview .......................................................... 9
   8.2 Functional Block Diagram .................................... 9
   8.3 Feature Description ............................................ 9
8.4 Device Functional Modes ....................................... 10
9 Application and Implementation ............................... 11
   9.1 Application Information ..................................... 11
   9.2 Typical Application ........................................... 11
   9.3 System Examples .............................................. 12
10 Power Supply Recommendations ............................ 18
11 Layout .................................................................... 18
   11.1 Layout Guidelines ............................................ 18
   11.2 Layout Example ............................................... 18
12 Device and Documentation Support .......................... 19
   12.1 Receiving Notification of Documentation Updates .... 19
   12.2 Support Resources ........................................... 19
   12.3 Trademarks ..................................................... 19
   12.4 Electrostatic Discharge Caution ......................... 19
   12.5 Glossary ........................................................ 19
13 Mechanical, Packaging, and Orderable Information .... 19

4 Revision History

Changes from Revision X (September 2016) to Revision Y ........................................ 3
   • Added Device Comparison Table ................................................................. 3
   • Changed V_{IN} to I_{OUT} in Load Transient Response figures .......................... 7
   • Added missing caption to second y-axis in second Load Transient Response figure 7
   • Changed V_{OUT} and output impedance equations in Battery-Charger Circuit section 14

Changes from Revision W (January 2015) to Revision X ........................................ 1
   • Changed body size dimensions for KCS TO-220 Package on Device information 1
   • Changed body size dimensions for KTT TO-263 Package on Device information 1
   • Changed V_{O} Output Voltage max value from 7 to 37 on Recommended Operating Conditions table 5
   • Added min value to I_{O} Output Current in Recommended Operating Conditions table 5
   • Changed values in the Thermal Information table to align with JEDEC standards 5
   • Added KCT package data to Thermal Information table 5
   • Deleted Section 9.3.6 "Adjusting Multiple On-Card Regulators with a Single Control" 14
   • Updated Adjustable 4-A Regulator Circuit graphic 16
   • Added Receiving Notification of Documentation Updates section and Community Resources section 19

Changes from Revision V (February 2013) to Revision W ........................................ 1
   • Added Applications, Device Information table, Pin Functions table, ESD Ratings table, Thermal Information table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section 1
   • Deleted Ordering Information table 1

Submit Documentation Feedback

Copyright © 1997–2020, Texas Instruments Incorporated

Product Folder Links: LM317
## 5 Device Comparison Table

<table>
<thead>
<tr>
<th>I&lt;sub&gt;OUT&lt;/sub&gt;</th>
<th>PARAMETER</th>
<th>LM317</th>
<th>LM317-N</th>
<th>LM317A</th>
<th>LM317HV</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.5 A</td>
<td>Input voltage range</td>
<td>4.25 - 40</td>
<td>4.25 - 40</td>
<td>4.25 - 40</td>
<td>4.25 - 60</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>Load regulation accuracy</td>
<td>1.5</td>
<td>1.5</td>
<td>1</td>
<td>1.5</td>
<td>%</td>
</tr>
<tr>
<td></td>
<td>PSRR (120 Hz)</td>
<td>64</td>
<td>80</td>
<td>80</td>
<td>65</td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>Recommended operating temperature</td>
<td>0 to 125</td>
<td>0 to 125</td>
<td>−40 to 125</td>
<td>0 to 125</td>
<td>°C</td>
</tr>
<tr>
<td></td>
<td>TO-220 (NDE) T&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>23.5</td>
<td>23.2</td>
<td>23.3</td>
<td>23</td>
<td>°C/W</td>
</tr>
<tr>
<td></td>
<td>TO-200 (KCT) T&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>37.9</td>
<td>N/A</td>
<td>N/A</td>
<td>65</td>
<td>°C/W</td>
</tr>
<tr>
<td></td>
<td>TO-252 T&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>N/A</td>
<td>54</td>
<td>54</td>
<td></td>
<td>°C/W</td>
</tr>
<tr>
<td></td>
<td>TO-263 T&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>38</td>
<td>41</td>
<td>N/A</td>
<td></td>
<td>°C/W</td>
</tr>
<tr>
<td></td>
<td>SOT-223 T&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>66.8</td>
<td>59.6</td>
<td>59.6</td>
<td></td>
<td>°C/W</td>
</tr>
<tr>
<td></td>
<td>TO-92 T&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>N/A</td>
<td>186</td>
<td>186</td>
<td></td>
<td>°C/W</td>
</tr>
</tbody>
</table>

### 0.5 A

|               | Input voltage range       | 3.75 - 40 |         |         |         | V    |
|               | Load regulation accuracy  | 1.5      |         |         |         | %    |
|               | PSRR (120 Hz)             | 80       |         |         |         | dB   |
|               | Recommended operating temperature | −40 - 125 |         |         |         | °C   |
|               | SOT-223 T<sub>JA</sub>    | 60.2     |         |         |         | °C/W |
|               | TO-252 T<sub>JA</sub>     | 56.9     |         |         |         | °C/W |

### 0.1 A

|               | Input voltage range       | 3.75 - 40 | 4.25 - 40 |         |         | V    |
|               | Load regulation accuracy  | 1        | 1.5      |         |         | %    |
|               | PSRR (120 Hz)             | 62       | 80       |         |         | dB   |
|               | Recommended operating temperature | −40 to 125 | −40 to 125 |         |         | °C   |
|               | SOT-23 T<sub>JA</sub>     | 167.8    | N/A      |         |         | °C/W |
|               | SO-8 T<sub>JA</sub>       | N/A      | 165      |         |         | °C/W |
|               | DSBGA T<sub>JA</sub>      | N/A      | 290      |         |         | °C/W |
|               | TO-92 T<sub>JA</sub>      | N/A      | 180      |         |         | °C/W |
6 Pin Configuration and Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADJUST</td>
<td>I</td>
<td>Output voltage adjustment pin. Connect to a resistor divider to set ( V_O )</td>
</tr>
<tr>
<td>INPUT</td>
<td>I</td>
<td>Supply input pin</td>
</tr>
<tr>
<td>OUTPUT</td>
<td>O</td>
<td>Voltage output pin</td>
</tr>
</tbody>
</table>

**DCY Package**
3-Pin SOT-223
Top View

**KCS or KCT Package**
3-Pin TO-220
Top View

**KTT Package**
3-Pin TO-263
Top View
7 Specifications

7.1 Absolute Maximum Ratings

over virtual junction temperature range (unless otherwise noted)\(^{(1)}\)

<table>
<thead>
<tr>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>40 V</td>
<td>150 °C</td>
<td>260 °C</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

7.2 ESD Ratings

<table>
<thead>
<tr>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>2500 V</td>
<td>V</td>
</tr>
<tr>
<td>1000 V</td>
<td>V</td>
</tr>
</tbody>
</table>

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

7.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.25 V</td>
<td>37 V</td>
<td></td>
</tr>
<tr>
<td>3 V</td>
<td>40 V</td>
<td></td>
</tr>
<tr>
<td>0.01 A</td>
<td>1.5 A</td>
<td></td>
</tr>
<tr>
<td>0 °C</td>
<td>125 °C</td>
<td></td>
</tr>
</tbody>
</table>

7.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC(^{(1)})</th>
<th>DCY (SOT-223)</th>
<th>KCS (TO-220)</th>
<th>KCT (TO-220)</th>
<th>KTT (TO-263)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Junction-to-ambient thermal resistance</td>
<td>66.8 °C/W</td>
<td>23.5 °C/W</td>
<td>37.9 °C/W</td>
<td>38.0 °C/W</td>
</tr>
<tr>
<td>Junction-to-case (top) thermal resistance</td>
<td>43.2 °C/W</td>
<td>15.9 °C/W</td>
<td>51.1 °C/W</td>
<td>36.5 °C/W</td>
</tr>
<tr>
<td>Junction-to-board thermal resistance</td>
<td>16.9 °C/W</td>
<td>7.9 °C/W</td>
<td>23.2 °C/W</td>
<td>18.9 °C/W</td>
</tr>
<tr>
<td>Junction-to-top characterization parameter</td>
<td>3.6 °C/W</td>
<td>3.0 °C/W</td>
<td>13.0 °C/W</td>
<td>6.9 °C/W</td>
</tr>
<tr>
<td>Junction-to-board characterization parameter</td>
<td>16.8 °C/W</td>
<td>7.8 °C/W</td>
<td>22.8 °C/W</td>
<td>17.9 °C/W</td>
</tr>
<tr>
<td>Junction-to-case (bottom) thermal resistance</td>
<td>NA °C/W</td>
<td>0.1 °C/W</td>
<td>4.2 °C/W</td>
<td>1.1 °C/W</td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see the Semiconductor and IC package thermal metrics application report.
7.5 Electrical Characteristics

over recommended ranges of operating virtual junction temperature (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS(1)</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line regulation(2)</td>
<td>V&lt;sub&gt;I&lt;/sub&gt; – V&lt;sub&gt;O&lt;/sub&gt; = 3 V to 40 V</td>
<td>T&lt;sub&gt;J&lt;/sub&gt; = 25°C</td>
<td>0.01</td>
<td>0.04</td>
<td>%/V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T&lt;sub&gt;J&lt;/sub&gt; = 0°C to 125°C</td>
<td>0.02</td>
<td>0.07</td>
<td></td>
</tr>
<tr>
<td>Load regulation</td>
<td>I&lt;sub&gt;O&lt;/sub&gt; = 10 mA to 1500 mA</td>
<td>C&lt;sub&gt;ADJ&lt;/sub&gt; = 10 μF, T&lt;sub&gt;J&lt;/sub&gt; = 25°C</td>
<td>V&lt;sub&gt;O&lt;/sub&gt; ≤ 5 V</td>
<td>25</td>
<td>mV</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>V&lt;sub&gt;O&lt;/sub&gt; ≥ 5 V</td>
<td>0.1</td>
<td>0.5</td>
</tr>
<tr>
<td></td>
<td></td>
<td>T&lt;sub&gt;J&lt;/sub&gt; = 0°C to 125°C</td>
<td>V&lt;sub&gt;O&lt;/sub&gt; ≤ 5 V</td>
<td>20</td>
<td>70</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>V&lt;sub&gt;O&lt;/sub&gt; ≥ 5 V</td>
<td>0.3</td>
<td>1.5</td>
</tr>
<tr>
<td>Thermal regulation</td>
<td>20-ms pulse, T&lt;sub&gt;J&lt;/sub&gt; = 25°C</td>
<td></td>
<td>0.03</td>
<td>0.07</td>
<td>%V&lt;sub&gt;O&lt;/sub&gt;/W</td>
</tr>
<tr>
<td>ADJUST terminal current</td>
<td></td>
<td>50</td>
<td>100</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>Change in ADJUST terminal current</td>
<td>V&lt;sub&gt;I&lt;/sub&gt; – V&lt;sub&gt;O&lt;/sub&gt; = 2.5 V to 40 V, P&lt;sub&gt;D&lt;/sub&gt; ≤ 20 W, I&lt;sub&gt;O&lt;/sub&gt; = 10 mA to 1500 mA</td>
<td></td>
<td>0.2</td>
<td>5</td>
<td>μA</td>
</tr>
<tr>
<td>Reference voltage</td>
<td>V&lt;sub&gt;I&lt;/sub&gt; – V&lt;sub&gt;O&lt;/sub&gt; = 3 V to 40 V, P&lt;sub&gt;D&lt;/sub&gt; ≤ 20 W, I&lt;sub&gt;O&lt;/sub&gt; = 10 mA to 1500 mA</td>
<td></td>
<td>1.2</td>
<td>1.25</td>
<td>1.3</td>
</tr>
<tr>
<td>Output-voltage temperature stability</td>
<td>T&lt;sub&gt;J&lt;/sub&gt; = 0°C to 125°C</td>
<td></td>
<td>0.7</td>
<td></td>
<td>%V&lt;sub&gt;O&lt;/sub&gt;</td>
</tr>
<tr>
<td>Minimum load current to maintain regulation</td>
<td>V&lt;sub&gt;I&lt;/sub&gt; – V&lt;sub&gt;O&lt;/sub&gt; = 40 V</td>
<td></td>
<td>3.5</td>
<td>10</td>
<td>mA</td>
</tr>
<tr>
<td>Maximum output current</td>
<td>V&lt;sub&gt;I&lt;/sub&gt; – V&lt;sub&gt;O&lt;/sub&gt; = 15 V, P&lt;sub&gt;D&lt;/sub&gt; &lt; P&lt;sub&gt;MAX&lt;/sub&gt;(4)</td>
<td></td>
<td>1.5</td>
<td>2.2</td>
<td>A</td>
</tr>
<tr>
<td></td>
<td>V&lt;sub&gt;I&lt;/sub&gt; – V&lt;sub&gt;O&lt;/sub&gt; ≤ 40 V, P&lt;sub&gt;D&lt;/sub&gt; &lt; P&lt;sub&gt;MAX&lt;/sub&gt;(4), T&lt;sub&gt;J&lt;/sub&gt; = 25°C</td>
<td></td>
<td>0.15</td>
<td>0.4</td>
<td></td>
</tr>
<tr>
<td>RMS output noise voltage (% of V&lt;sub&gt;O&lt;/sub&gt;)</td>
<td>f = 10 Hz to 10 kHz, T&lt;sub&gt;J&lt;/sub&gt; = 25°C</td>
<td></td>
<td>0.003</td>
<td></td>
<td>%V&lt;sub&gt;O&lt;/sub&gt;</td>
</tr>
<tr>
<td>Ripple rejection</td>
<td>V&lt;sub&gt;O&lt;/sub&gt; = 10 V, f = 120 Hz</td>
<td></td>
<td>57</td>
<td>57</td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td></td>
<td>C&lt;sub&gt;ADJ&lt;/sub&gt; = 0 μF(3)</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>C&lt;sub&gt;ADJ&lt;/sub&gt; = 10 μF(3)</td>
<td>62</td>
<td>64</td>
<td></td>
</tr>
<tr>
<td>Long-term stability</td>
<td>T&lt;sub&gt;J&lt;/sub&gt; = 25°C</td>
<td></td>
<td>0.3</td>
<td>1</td>
<td>%/1k hr</td>
</tr>
</tbody>
</table>

(1) Unless otherwise noted, the following test conditions apply: |V<sub>I</sub> – V<sub>O</sub>| = 5 V and I<sub>OMAX</sub> = 1.5 A, T<sub>J</sub> = 0°C to 125°C. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible.

(2) Line regulation is expressed here as the percentage change in output voltage per 1-V change at the input.

(3) C<sub>ADJ</sub> is connected between the ADJUST terminal and GND.

(4) Maximum power dissipation is a function of T<sub>J</sub>(max), θ<sub>JA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any allowable ambient temperature is P<sub>D</sub> = (T<sub>J</sub>(max) – T<sub>A</sub>) / θ<sub>JA</sub>. Operating at the absolute maximum T<sub>J</sub> of 150°C can affect reliability.
7.6 Typical Characteristics

Figure 1. Load Regulation

Figure 2. Load Regulation

Figure 3. Load Transient Response

Figure 4. Load Transient Response

Figure 5. Line Regulation

Figure 6. Ripple Rejection vs Output Current
Typical Characteristics (continued)

Figure 7. Ripple Rejection vs Output Voltage

Figure 8. Ripple Rejection vs Frequency
8 Detailed Description

8.1 Overview

The LM317 device is an adjustable three-terminal positive-voltage regulator capable of supplying up to 1.5 A over an output-voltage range of 1.25 V to 37 V. It requires only two external resistors to set the output voltage. The device features a typical line regulation of 0.01% and typical load regulation of 0.1%. It includes current limiting, thermal overload protection, and safe operating area protection. Overload protection remains functional even if the ADJUST terminal is disconnected.

The LM317 device is versatile in its applications, including uses in programmable output regulation and local on-card regulation. Or, by connecting a fixed resistor between the ADJUST and OUTPUT terminals, the LM317 device can function as a precision current regulator. An optional output capacitor can be added to improve transient response. The ADJUST terminal can be bypassed to achieve very high ripple-rejection ratios, which are difficult to achieve with standard three-terminal regulators.

8.2 Functional Block Diagram

8.3 Feature Description

8.3.1 NPN Darlington Output Drive

NPN Darlington output topology provides naturally low output impedance and an output capacitor is optional. 3-V headroom is recommended \((V_i - V_o)\) to support maximum current and lowest temperature.

8.3.2 Overload Block

Over-current and over-temperature shutdown protects the device against overload or damage from operating in excessive heat.

8.3.3 Programmable Feedback

Op amp with 1.25-V offset input at the ADJUST terminal provides easy output voltage or current (not both) programming. For current regulation applications, a single resistor whose resistance value is \(1.25 V/I_o\) and power rating is greater than \((1.25 V)^2/R\) should be used. For voltage regulation applications, two resistors set the output voltage.
8.4 Device Functional Modes

8.4.1 Normal Operation
The device OUTPUT pin will source current necessary to make OUTPUT pin 1.25 V greater than ADJUST terminal to provide output regulation.

8.4.2 Operation With Low Input Voltage
The device requires up to 3-V headroom ($V_i - V_o$) to operate in regulation. The device may drop out and OUTPUT voltage will be INPUT voltage minus drop out voltage with less headroom.

8.4.3 Operation at Light Loads
The device passes its bias current to the OUTPUT pin. The load or feedback must consume this minimum current for regulation or the output may be too high. See the Electrical Characteristics table for the minimum load current needed to maintain regulation.

8.4.4 Operation In Self Protection
When an overload occurs the device shuts down Darlington NPN output stage or reduces the output current to prevent device damage. The device will automatically reset from the overload. The output may be reduced or alternate between on and off until the overload is removed.
9 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information
The flexibility of the LM317 allows it to be configured to take on many different functions in DC power applications.

9.2 Typical Application

![Diagram of Adjustable Voltage Regulator](attachment:diagram.png)

**Figure 9. Adjustable Voltage Regulator**

9.2.1 Design Requirements
- R1 and R2 are required to set the output voltage.
- \( C_{ADJ} \) is recommended to improve ripple rejection. It prevents amplification of the ripple as the output voltage is adjusted higher.
- \( C_i \) is recommended, particularly if the regulator is not in close proximity to the power-supply filter capacitors. A 0.1-µF or 1-µF ceramic or tantalum capacitor provides sufficient bypassing for most applications, especially when adjustment and output capacitors are used.
- \( C_O \) improves transient response, but is not needed for stability.
- Protection diode D2 is recommended if \( C_{ADJ} \) is used. The diode provides a low-impedance discharge path to prevent the capacitor from discharging into the output of the regulator.
- Protection diode D1 is recommended if \( C_O \) is used. The diode provides a low-impedance discharge path to prevent the capacitor from discharging into the output of the regulator.

9.2.2 Detailed Design Procedure
\( V_O \) is calculated as shown in Equation 1. \( I_{ADJ} \) is typically 50 µA and negligible in most applications.

\[
V_O = V_{REF} \left(1 + \frac{R_2}{R_1}\right) + \left(I_{ADJ} \times R_2\right) \tag{1}
\]
Typical Application (continued)

9.2.3 Application Curves

9.3 System Examples

9.3.1 0-V to 30-V Regulator Circuit

Here, the voltage is determined by

$$V_{OUT} = V_{REF} \left(1 + \frac{R_2 + R_3}{R_1}\right) - 10 \text{V}$$

Figure 12. 0-V to 30-V Regulator Circuit
9.3.2 Adjustable Regulator Circuit With Improved Ripple Rejection

C2 helps to stabilize the voltage at the adjustment pin, which helps reject noise. Diode D1 exists to discharge C2 in case the output is shorted to ground.

![Adjustable Regulator Circuit with Improved Ripple Rejection](image)

9.3.3 Precision Current-Limiter Circuit

This application limits the output current to the $I_{\text{LIMIT}}$ in the diagram.

![Precision Current-Limiter Circuit](image)

9.3.4 Tracking Preregulator Circuit

This application keeps a constant voltage across the second LM317 in the circuit.

![Tracking Preregulator Circuit](image)
System Examples (continued)

9.3.5 1.25-V to 20-V Regulator Circuit With Minimum Program Current

Because the value of $V_{\text{REF}}$ is constant, the value of $R_1$ determines the amount of current that flows through $R_1$ and $R_2$. The size of $R_2$ determines the IR drop from ADJUSTMENT to GND. Higher values of $R_2$ translate to higher $V_{\text{OUT}}$.

$$V_{\text{OUT}} = V_{\text{REF}} \left( 1 + \frac{R_2}{R_1} \right) - 10 \text{ V}$$  \hspace{1cm} (2)

$$(R_1 + R_2)_{\text{min}} = V_{\text{Oreg(min)}}$$ \hspace{1cm} (3)

![Figure 16. 1.25-V to 20-V Regulator Circuit With Minimum Program Current](image)

9.3.6 Battery-Charger Circuit

The series resistor limits the current output of the LM317, minimizing damage to the battery cell.

$$V_{\text{OUT}} = 1.25 \text{ V} \times \left( 1 + \frac{R_2}{R_1} \right)$$  \hspace{1cm} (4)

$$I_{\text{OUT(short)}} = \frac{1.25 \text{ V}}{R_S}$$  \hspace{1cm} (5)

Output Impedance $= R_S \times \left( 1 + \frac{R_2}{R_1} \right)$  \hspace{1cm} (6)

![Figure 17. Battery-Charger Circuit](image)
System Examples (continued)

9.3.7 50-mA Constant-Current Battery-Charger Circuit

The current limit operation mode can be used to trickle charge a battery at a fixed current. $I_{\text{CHG}} = 1.25 \, \text{V} ÷ 24 \, \Omega$. $V_I$ should be greater than $V_{\text{BAT}} + 4.25 \, \text{V}$. (1.25 V [$V_{\text{REF}}$] + 3 V [headroom])

![Diagram of 50-mA Constant-Current Battery-Charger Circuit](image)

Figure 18. 50-mA Constant-Current Battery-Charger Circuit

9.3.8 Slow Turn-On 15-V Regulator Circuit

The capacitor C1, in combination with the PNP transistor, helps the circuit to slowly start supplying voltage. In the beginning, the capacitor is not charged. Therefore output voltage starts at $V_{\text{C1}} + V_{\text{BE}} + 1.25 \, \text{V} = 0 \, \text{V} + 0.65 \, \text{V} + 1.25 \, \text{V} = 1.9 \, \text{V}$. As the capacitor voltage rises, $V_{\text{OUT}}$ rises at the same rate. When the output voltage reaches the value determined by R1 and R2, the PNP will be turned off.

![Diagram of Slow Turn-On 15-V Regulator Circuit](image)

Figure 19. Slow Turn-On 15-V Regulator Circuit

9.3.9 AC Voltage-Regulator Circuit

These two LM317s can regulate both the positive and negative swings of a sinusoidal AC input.

![Diagram of AC Voltage-Regulator Circuit](image)

Figure 20. AC Voltage-Regulator Circuit
System Examples (continued)

9.3.10  Current-Limited 6-V Charger Circuit

As the charge current increases, the voltage at the bottom resistor increases until the NPN starts sinking current from the adjustment pin. The voltage at the adjustment pin drops, and consequently the output voltage decreases until the NPN stops conducting.

![Figure 21. Current-Limited 6-V Charger Circuit](image)

9.3.11  Adjustable 4-A Regulator Circuit

This application keeps the output current at 4 A while having the ability to adjust the output voltage using the adjustable (1.5 kΩ in schematic) resistor.

![Figure 22. Adjustable 4-A Regulator Circuit](image)
System Examples (continued)

9.3.12 High-Current Adjustable Regulator Circuit

The NPNs at the top of the schematic allow higher currents at $V_{OUT}$ than the LM317 can provide, while still keeping the output voltage at levels determined by the adjustment pin resistor divider of the LM317.

![Schematic Diagram](image)

Figure 23. High-Current Adjustable Regulator Circuit
10 Power Supply Recommendations

The LM317 is designed to operate from an input voltage supply range between 1.25 V to 37 V greater than the output voltage. If the device is more than six inches from the input filter capacitors, an input bypass capacitor, 0.1 μF or greater, of any type is needed for stability.

11 Layout

11.1 Layout Guidelines

- TI recommends that the input terminal be bypassed to ground with a bypass capacitor.
- The optimum placement is closest to the input terminal of the device and the system GND. Take care to minimize the loop area formed by the bypass-capacitor connection, the input terminal, and the system GND.
- For operation at full rated load, TI recommends to use wide trace lengths to eliminate I × R drop and heat dissipation.

11.2 Layout Example

![Layout Example Diagram]

Figure 24. Layout Example
12 Device and Documentation Support

12.1 Receiving Notification of Documentation Updates
To receive notification of documentation updates, navigate to the device product folder on ti.com. In the upper right corner, click on Alert me to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

12.2 Support Resources
TI E2E™ support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

Linked content is provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

12.3 Trademarks
E2E is a trademark of Texas Instruments.
All other trademarks are the property of their respective owners.

12.4 Electrostatic Discharge Caution
This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

12.5 Glossary
SLYZ022 — Ti Glossary.
This glossary lists and explains terms, acronyms, and definitions.

13 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
### PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead finish/Ball material (6)</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM317DCY</td>
<td>ACTIVE</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>80</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>Level-2-260C-1 YEAR</td>
<td>0 to 125</td>
<td>L3</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317DCYG3</td>
<td>ACTIVE</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>80</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>Level-2-260C-1 YEAR</td>
<td>0 to 125</td>
<td>L3</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317DCYR</td>
<td>ACTIVE</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>Level-2-260C-1 YEAR</td>
<td>0 to 125</td>
<td>L3</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317DCYRG3</td>
<td>ACTIVE</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>Level-2-260C-1 YEAR</td>
<td>0 to 125</td>
<td>L3</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317KCS</td>
<td>ACTIVE</td>
<td>TO-220</td>
<td>KCS</td>
<td>3</td>
<td>50</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 125</td>
<td>LM317</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317KCSE3</td>
<td>ACTIVE</td>
<td>TO-220</td>
<td>KCS</td>
<td>3</td>
<td>50</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 125</td>
<td>LM317</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317KCT</td>
<td>ACTIVE</td>
<td>TO-220</td>
<td>KCT</td>
<td>3</td>
<td>50</td>
<td>RoHS &amp; Non-Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 125</td>
<td>LM317</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317KTTR</td>
<td>ACTIVE</td>
<td>DDPAK/TO-263</td>
<td>KTT</td>
<td>3</td>
<td>500</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>Level-3-245C-168 HR</td>
<td>0 to 125</td>
<td>LM317</td>
<td>Samples</td>
</tr>
<tr>
<td>LM317KTTRG3</td>
<td>ACTIVE</td>
<td>DDPAK/TO-263</td>
<td>KTT</td>
<td>3</td>
<td>500</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>Level-3-245C-168 HR</td>
<td>0 to 125</td>
<td>LM317</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE:** Product device recommended for new designs.
- **LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
- **RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
- **Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
**TAPE AND REEL INFORMATION**

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM317DCYR</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>7.05</td>
<td>7.4</td>
<td>1.9</td>
<td>8.0</td>
<td>12.0</td>
<td>Q3</td>
</tr>
<tr>
<td>LM317DCYR</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.55</td>
<td>7.25</td>
<td>1.9</td>
<td>8.0</td>
<td>12.0</td>
<td>Q3</td>
</tr>
<tr>
<td>LM317DCYR</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>7.0</td>
<td>7.42</td>
<td>2.0</td>
<td>8.0</td>
<td>12.0</td>
<td>Q3</td>
</tr>
<tr>
<td>LM317KTTR</td>
<td>DDPACK/TO-263</td>
<td>KTT</td>
<td>3</td>
<td>500</td>
<td>330.0</td>
<td>24.4</td>
<td>10.8</td>
<td>16.1</td>
<td>4.9</td>
<td>16.0</td>
<td>24.0</td>
<td>Q2</td>
</tr>
</tbody>
</table>

*All dimensions are nominal.*

A0: Dimension designed to accommodate the component width
B0: Dimension designed to accommodate the component length
K0: Dimension designed to accommodate the component thickness
W: Overall width of the carrier tape
P1: Pitch between successive cavity centers
### TAPE AND REEL BOX DIMENSIONS

*All dimensions are nominal

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM317DCYR</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>340.0</td>
<td>340.0</td>
<td>38.0</td>
</tr>
<tr>
<td>LM317DCYR</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>336.0</td>
<td>336.0</td>
<td>48.0</td>
</tr>
<tr>
<td>LM317DCYR</td>
<td>SOT-223</td>
<td>DCY</td>
<td>4</td>
<td>2500</td>
<td>350.0</td>
<td>334.0</td>
<td>47.0</td>
</tr>
<tr>
<td>LM317KTTR</td>
<td>DDPACK/TO-263</td>
<td>KTT</td>
<td>3</td>
<td>500</td>
<td>350.0</td>
<td>334.0</td>
<td>47.0</td>
</tr>
</tbody>
</table>
NOTES:  
A. All linear dimensions are in millimeters (inches).  
B. This drawing is subject to change without notice.  
C. Body dimensions do not include mold flash or protrusion.  
D. Falls within JEDEC TO-261 Variation AA.
NOTES:

A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC-7351 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil recommendations. Refer to IPC 7525 for stencil design considerations.
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion. Mold flash or protrusion not to exceed 0.005 (0,13) per side.
⚠️ Falls within JEDEC TO-263 variation AA, except minimum lead thickness and minimum exposed pad length.
NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Publication IPC–SM–782 is recommended for alternate designs.
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release.
   Customers should contact their board assembly site for stencil design recommendations. Refer to IPC–7525.
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.
F. This package is designed to be soldered to a thermal pad on the board. Refer to the Product Datasheet for specific thermal information, via requirements, and recommended thermal pad size. For thermal pad sizes larger than shown a solder mask defined pad is recommended in order to maintain the solderable pad geometry while increasing copper area.
NOTES:

1. Dimensions are in millimeters. Any dimension in brackets or parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Lead dimensions are not controlled within this area.
4. Reference JEDEC registration TO-220.
EXAMPLE BOARD LAYOUT

KCT0003A

TO-220 - 20.55 mm max height

LAND PATTERN EXAMPLE
NON-SOLDER MASK DEFINED
SCALE: 15X

TO-220
EXAMPLE BOARD LAYOUT

KCS0003B
TO-220 - 19.65 mm max height

LAND PATTERN EXAMPLE
NON-SOLDER MASK DEFINED
SCALE:15X

TEXAS INSTRUMENTS
www.ti.com
IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (https://www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2021, Texas Instruments Incorporated