1 Features
- Operating Current Range
  - LM285-1.2: 10 μA to 20 mA
  - LM385-1.2: 15 μA to 20 mA
  - LM385B-1.2: 15 μA to 20 mA
- 1% and 2% Initial Voltage Tolerance
- Reference Impedance
  - LM385-1.2: 1 Ω MAX at 25°C
  - All devices: 1.5 Ω MAX over Full Temperature Range
- Very Low Power Consumption
- Interchangeable with Industry Standard LM285-1.2 and LM385-1.2

2 Applications
- Portable Meter References
- Portable Test Instruments
- Battery-Operated Systems
- Current-Loop Instrumentation
- Panel Meters

3 Description
These micropower, two-terminal, band-gap voltage references operate over a 10-μA to 20-mA current range and feature exceptionally low dynamic impedance and good temperature stability. On-chip trimming provides tight voltage tolerance. The band-gap reference for these devices has low noise and long-term stability.

The design makes these devices exceptionally tolerant of capacitive loading and, thus, easier to use in most reference applications. The wide dynamic operating temperature range accommodates varying current supplies, with excellent regulation.

The extremely low power drain of this series makes them useful for micropower circuitry. These voltage references can be used to make portable meters, regulators, or general-purpose analog circuitry, with battery life approaching shelf life. The wide operating current range allows them to replace older references with tighter-tolerance parts.

4 Simplified Schematic

---

An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.
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5 Revision History

Changes from Revision I (December 2005) to Revision J  Page

• Added Applications, Device Information table, Pin Functions table, ESD Ratings table, Thermal Information table, Typical Characteristics, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section. ................................................. 1

• Deleted Ordering Information table. ................................................................. 1
6 Pin Configuration and Functions

### Pin Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>LP</td>
<td>TYPE</td>
</tr>
<tr>
<td>NAME</td>
<td>D, PS or PW</td>
</tr>
</tbody>
</table>

**Shunt Current/Voltage input**

**Common pin, normally connected to ground**

**No internal connection**

---

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Submit Documentation Feedback

Product Folder Links: LM285-1.2, LM385-1.2, LM385B-1.2
7 Specifications

7.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)\(^{(1)}\)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>I(_{R}) Reverse Current</td>
<td>30</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>I(_{F}) Forward Current</td>
<td>10</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>T(_{J}) Operating virtual junction temperature</td>
<td>150</td>
<td>°C</td>
<td></td>
</tr>
<tr>
<td>T(_{stg}) Storage temperature</td>
<td>-65</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

\(^{(1)}\) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

7.2 ESD Ratings

<table>
<thead>
<tr>
<th></th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>V(_{(ESD)}) Electrostatic discharge</td>
<td>±2000</td>
<td>V</td>
</tr>
</tbody>
</table>

Human body model (HBM), per ANSI/ESDA/JEDEC JS-001, all pins\(^{(1)}\)

Charged device model (CDM), per JEDEC specification JESD22-C101, all pins\(^{(2)}\)

\(^{(1)}\) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

\(^{(2)}\) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

7.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>I(_{ZZ}) Reference current</td>
<td>0.01</td>
<td>20</td>
<td>mA</td>
</tr>
<tr>
<td>T(_{A}) Operating free-air temperature</td>
<td>LM285-1.2</td>
<td>-40</td>
<td>85</td>
</tr>
<tr>
<td></td>
<td>LM385-1.2, LM385B-1.2</td>
<td>0</td>
<td>70</td>
</tr>
</tbody>
</table>

7.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC(^{(1)})</th>
<th>LM(_{x85-1.2})</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>D</td>
</tr>
<tr>
<td></td>
<td>8 PINS</td>
</tr>
<tr>
<td>R(_{JA}) Junction-to-ambient thermal resistance</td>
<td>97</td>
</tr>
</tbody>
</table>

\(^{(1)}\) For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, SPRA953.
# 7.5 Electrical Characteristics

over operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>TA (1)</th>
<th>LM285-1.2</th>
<th>LM385-1.2</th>
<th>LM385B-1.2</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>MIN</td>
<td>TYP</td>
<td>MAX</td>
<td>MIN</td>
</tr>
<tr>
<td>VZ</td>
<td>IZ = I(min) to 20 mA (2)</td>
<td>25°C</td>
<td>1.223</td>
<td>1.235</td>
<td>1.247</td>
<td>1.21</td>
</tr>
<tr>
<td>aVZ</td>
<td>IZ = I(min) to 20 mA (2)</td>
<td>Full Range</td>
<td>±20</td>
<td>±20</td>
<td>±20</td>
<td>ppm°/°C</td>
</tr>
<tr>
<td>ΔVZ</td>
<td>IZ = I(min) to 1 mA (2)</td>
<td>25°C</td>
<td>1</td>
<td>1</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td></td>
<td>IZ = I(min) to 20 mA</td>
<td>25°C</td>
<td>12</td>
<td>20</td>
<td>20</td>
<td>15</td>
</tr>
<tr>
<td></td>
<td>Full Range</td>
<td></td>
<td>30</td>
<td>30</td>
<td>30</td>
<td>15</td>
</tr>
<tr>
<td>ΔVZ/Δt</td>
<td>IZ = 100 µA</td>
<td>25°C</td>
<td>±20</td>
<td>±20</td>
<td>±20</td>
<td>ppm/k hr</td>
</tr>
<tr>
<td>I(min)</td>
<td>IZ = 100 µA</td>
<td>Full Range</td>
<td>8</td>
<td>10</td>
<td>8</td>
<td>15</td>
</tr>
<tr>
<td>ZZ</td>
<td>IZ = 100 µA, f = 25 Hz</td>
<td>25°C</td>
<td>0.2</td>
<td>0.6</td>
<td>0.4</td>
<td>0.4</td>
</tr>
<tr>
<td></td>
<td>Full Range</td>
<td></td>
<td>1.5</td>
<td>1.5</td>
<td>1.5</td>
<td>1.5</td>
</tr>
<tr>
<td>VN</td>
<td>IZ = 100 µA, f = 10 Hz to 10 kHz</td>
<td>25°C</td>
<td>60</td>
<td>60</td>
<td>60</td>
<td>60</td>
</tr>
</tbody>
</table>

(1) Full range is –40°C to 85°C for the LM285-1.2 and 0°C to 70°C for the LM385-1.2 and LM385B-1.2.
(2) I(min) = 10 µA for the LM285-1.2 and 15 µA for the LM385-1.2 and LM385B-1.2
(3) The average temperature coefficient of reference voltage is defined as the total change in reference voltage divided by the specified temperature range.

## 7.6 Typical Characteristics

![Figure 1. Reverse Current vs Reverse Voltage](image1)

![Figure 2. Reference Voltage Change vs Reverse Current](image2)
Typical Characteristics (continued)

Figure 3. Forward Voltage vs Forward Current

Figure 4. Reference Voltage vs Free-Air Temperature

Figure 5. Reference Impedance vs Reference Current

Figure 6. Noise Voltage vs Frequency

Figure 7. Output Noise Voltage vs Cutoff Frequency
8 Detailed Description

8.1 Overview
The LM285-1.2, LM385-1.2, and LM385-1.2 devices are micropower, two-terminal, band-gap voltage references which operate over a 10-μA to 20-mA current range. On-chip trimming provides tight voltage tolerance. The band-gap reference for these devices has low noise and long-term stability.

The design makes these devices exceptionally tolerant of capacitive loading and, thus, easier to use in most reference applications. The wide dynamic operating temperature range accommodates varying current supplies, with excellent regulation.

The extremely low power drain of this series makes them useful for micropower circuitry. These voltage references can be used to make portable meters, regulators, or general-purpose analog circuitry, with battery life approaching shelf life.

8.2 Functional Block Diagram

8.3 Feature Description
A band gap voltage reference controls high gain amplifier and shunt pass element to maintain a nearly constant voltage between cathode and anode. Regulation occurs after a minimum current is provided to power the voltage divider and amplifier. Internal frequency compensation provides a stable loop for all capacitor loads. Floating shunt design is useful for both positive and negative regulation applications.

8.4 Device Functional Modes
LM285-1.2, LM385-1.2, and LM385-1.2 devices will operate in one mode, which is as a fixed voltage reference that cannot be adjusted.

In order for a proper Reverse Voltage to be developed, current must be sourced into the cathode of LM285. The minimum current needed for proper regulation is denoted in Electrical Characteristics as $I_{Z,min}$. 
9 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information

The LM285-1.2, LM385-1.2, and LM385-1.2 devices create a voltage reference for to be used for a variety of applications including amplifiers, power supplies, and current-sensing circuits. The following application shows how to use these devices to establish a voltage reference.

9.2 Typical Application

![Figure 8. Generating Reference Voltage with a Resistive Current Source](image)

9.2.1 Design Requirements

The key design requirement when using this device as a voltage reference is to supply the LM385 with a minimum Cathode Current ($I_{Z}$), as indicated in Electrical Characteristics.

9.2.2 Detailed Design Procedure

In order to generate a constant and stable reference voltage, a current greater than $I_{Z(MIN)}$ must be sourced into the cathode of this device. This can be accomplished using a current regulating device such as LM334 or a simple resistor. For a resistor, its value should be equal to or greater than $(V_{supply} - V_{reference}) / I_{Z(MIN)}$.
Typical Application (continued)

9.2.3 Application Curves

Figure 9. Transient Response
9.3 System Examples

9.3.1 Thermocouple Cold-Junction Compensator

![Thermocouple Cold-Junction Compensator Diagram]

† Adjust for 11.15 mV at 25°C across 953 Ω
‡ Adjust for 12.17 mV at 25°C across 412 Ω

Figure 10. Thermocouple Cold-Junction Compensator

9.3.2 Generating Reference Voltage with a Constant Current Source

![Generating Reference Voltage with a Constant Current Source Diagram]

Figure 11. Generating Reference Voltage with a Constant Current Source Device
10 Power Supply Recommendations

In order to not exceed the maximum cathode current, be sure that the supply voltage is current limited.

For applications shunting high currents (30 mA max), pay attention to the cathode and anode trace lengths, adjusting the width of the traces to have the proper current density.

11 Layout

11.1 Layout Guidelines

Figure 12 shows an example of a PCB layout of LMx85x-1.2. Some key V_ref noise considerations are:

- Connect a low-ESR, 0.1-μF (C_L) ceramic bypass capacitor on the cathode pin node.
- Decouple other active devices in the system per the device specifications.
- Using a solid ground plane helps distribute heat and reduces electromagnetic interference (EMI) noise pickup.
- Place the external components as close to the device as possible. This configuration prevents parasitic errors (such as the Seebeck effect) from occurring.
- Do not run sensitive analog traces in parallel with digital traces. Avoid crossing digital and analog traces if possible and only make perpendicular crossings when absolutely necessary.

11.2 Layout Example

![Layout Diagram](image)

Figure 12. Layout Diagram
12 Device and Documentation Support

12.1 Related Links
The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

<table>
<thead>
<tr>
<th>PARTS</th>
<th>PRODUCT FOLDER</th>
<th>SAMPLE &amp; BUY</th>
<th>TECHNICAL DOCUMENTS</th>
<th>TOOLS &amp; SOFTWARE</th>
<th>SUPPORT &amp; COMMUNITY</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM285-1.2</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
</tr>
<tr>
<td>LM385-1.2</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
</tr>
<tr>
<td>LM385B-1.2</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
</tr>
</tbody>
</table>

12.2 Trademarks
All trademarks are the property of their respective owners.

12.3 Electrostatic Discharge Caution

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

12.4 Glossary
SLYZ022 — Ti Glossary.
This glossary lists and explains terms, acronyms, and definitions.

13 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead finish/ Ball material</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM285D-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>75</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM285DG4-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>75</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM285DR-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM285DRE4-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM285DRG4-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM285LP-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>1000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM285LPE3-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>1000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>-40 to 85</td>
<td>285-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385BD-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>75</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385B12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385BDG4-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>75</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385B12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385BD-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>385B12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385BLPE3-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>385B12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385BPW-1-2</td>
<td>ACTIVE</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>150</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385B12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385BPWR-1-2</td>
<td>ACTIVE</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385B12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385D-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>75</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385DG4-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>75</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>Orderable Device</td>
<td>Status</td>
<td>Package Type</td>
<td>Package Drawing</td>
<td>Pins</td>
<td>Package Qty</td>
<td>Eco Plan</td>
<td>Lead finish/ Ball material</td>
<td>MSL Peak Temp</td>
<td>Op Temp (°C)</td>
<td>Device Marking</td>
<td>Samples</td>
</tr>
<tr>
<td>-----------------</td>
<td>---------</td>
<td>--------------</td>
<td>-----------------</td>
<td>------</td>
<td>-------------</td>
<td>---------</td>
<td>-----------------------------</td>
<td>---------------</td>
<td>--------------</td>
<td>----------------</td>
<td>---------</td>
</tr>
<tr>
<td>LM385DR-1-2</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385LP-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>1000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385LPR-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385LPRE3-1-2</td>
<td>ACTIVE</td>
<td>TO-92</td>
<td>LP</td>
<td>3</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>SN</td>
<td>N / A for Pkg Type</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385PW-1-2</td>
<td>ACTIVE</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>150</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385PWE4-1-2</td>
<td>ACTIVE</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>150</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385PWR-1-2</td>
<td>ACTIVE</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
<tr>
<td>LM385PWRE4-1-2</td>
<td>ACTIVE</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>RoHS &amp; Green</td>
<td>NIPDAU</td>
<td>Level-1-260C-UNLIM</td>
<td>0 to 70</td>
<td>385-12</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
## TAPE AND REEL INFORMATION

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM285DR-1-2</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.4</td>
<td>5.2</td>
<td>2.1</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
<tr>
<td>LM385BDR-1-2</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.4</td>
<td>5.2</td>
<td>2.1</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
<tr>
<td>LM385BPWR-1-2</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>330.0</td>
<td>12.4</td>
<td>7.0</td>
<td>3.6</td>
<td>1.6</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
<tr>
<td>LM385DR-1-2</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.4</td>
<td>5.2</td>
<td>2.1</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
<tr>
<td>LM385PWR-1-2</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>330.0</td>
<td>12.4</td>
<td>7.0</td>
<td>3.6</td>
<td>1.6</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
</tbody>
</table>

*All dimensions are nominal.*

**TAPE DIMENSIONS**
- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- K0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P1: Pitch between successive cavity centers

**QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE**
- Quadrant assignments are based on the direction of feed.
<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM285DR-1-2</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>340.5</td>
<td>336.1</td>
<td>25.0</td>
</tr>
<tr>
<td>LM385BDR-1-2</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>340.5</td>
<td>336.1</td>
<td>25.0</td>
</tr>
<tr>
<td>LM385BPWR-1-2</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>367.0</td>
<td>367.0</td>
<td>35.0</td>
</tr>
<tr>
<td>LM385DR-1-2</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>340.5</td>
<td>336.1</td>
<td>25.0</td>
</tr>
<tr>
<td>LM385PWR-1-2</td>
<td>TSSOP</td>
<td>PW</td>
<td>8</td>
<td>2000</td>
<td>367.0</td>
<td>367.0</td>
<td>35.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
## TUBE

![TUBE Diagram](image)

*T all dimensions are nominal

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Name</th>
<th>Package Type</th>
<th>Pins</th>
<th>SPQ</th>
<th>L (mm)</th>
<th>W (mm)</th>
<th>T (µm)</th>
<th>B (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM285D-1-2</td>
<td>D</td>
<td>SOIC</td>
<td>8</td>
<td>75</td>
<td>507</td>
<td>8</td>
<td>3940</td>
<td>4.32</td>
</tr>
<tr>
<td>LM285DG4-1-2</td>
<td>D</td>
<td>SOIC</td>
<td>8</td>
<td>75</td>
<td>507</td>
<td>8</td>
<td>3940</td>
<td>4.32</td>
</tr>
<tr>
<td>LM385BD-1-2</td>
<td>D</td>
<td>SOIC</td>
<td>8</td>
<td>75</td>
<td>507</td>
<td>8</td>
<td>3940</td>
<td>4.32</td>
</tr>
<tr>
<td>LM385BDG4-1-2</td>
<td>D</td>
<td>SOIC</td>
<td>8</td>
<td>75</td>
<td>507</td>
<td>8</td>
<td>3940</td>
<td>4.32</td>
</tr>
<tr>
<td>LM385BPW-1-2</td>
<td>PW</td>
<td>TSSOP</td>
<td>8</td>
<td>150</td>
<td>530</td>
<td>10.2</td>
<td>3600</td>
<td>3.5</td>
</tr>
<tr>
<td>LM385D-1-2</td>
<td>D</td>
<td>SOIC</td>
<td>8</td>
<td>75</td>
<td>507</td>
<td>8</td>
<td>3940</td>
<td>4.32</td>
</tr>
<tr>
<td>LM385DG4-1-2</td>
<td>D</td>
<td>SOIC</td>
<td>8</td>
<td>75</td>
<td>507</td>
<td>8</td>
<td>3940</td>
<td>4.32</td>
</tr>
<tr>
<td>LM385PW-1-2</td>
<td>PW</td>
<td>TSSOP</td>
<td>8</td>
<td>150</td>
<td>530</td>
<td>10.2</td>
<td>3600</td>
<td>3.5</td>
</tr>
<tr>
<td>LM385PWE4-1-2</td>
<td>PW</td>
<td>TSSOP</td>
<td>8</td>
<td>150</td>
<td>530</td>
<td>10.2</td>
<td>3600</td>
<td>3.5</td>
</tr>
</tbody>
</table>
NOTES:

1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.
4. This dimension does not include interlead flash.
5. Reference JEDEC registration MS-012, variation AA.
NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.
5. Reference JEDEC registration MO-153, variation AA.
NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.
Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. Lead dimensions are not controlled within this area.
4. Reference JEDEC TO-226, variation AA.
5. Shipping method:
   a. Straight lead option available in bulk pack only.
   b. Formed lead option available in tape and reel or ammo pack.
   c. Specific products can be offered in limited combinations of shipping medium and lead options.
   d. Consult product folder for more information on available options.
EXAMPLE BOARD LAYOUT

LAND PATTERN EXAMPLE
STRAIGHT LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X

LAND PATTERN EXAMPLE
FORMED LEAD OPTION
NON-SOLDER MASK DEFINED
SCALE:15X

TO-92 - 5.34 mm max height

LP0003A

4215214/B  04/2017
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