The LM4562 is part of the ultra-low distortion, low-noise, high-slew-rate operational amplifier series optimized and fully specified for high-performance, high-fidelity applications. The LM4562 audio operational amplifiers deliver superior audio signal amplification for outstanding audio performance. The LM4562 combines extremely low voltage noise density (2.7nV/√Hz) with vanishingly low THD+N (0.00003%) to easily satisfy the most demanding audio applications. To ensure that the most challenging loads are driven without compromise, the LM4562 has a high slew rate of ±20V/μs and an output current capability of ±26mA. Further, dynamic range is maximized by an output stage that drives 2kΩ loads to within 1V of either power supply voltage and to within 1.4V when driving 600Ω loads.

The LM4562's outstanding CMRR (120dB), PSRR (120dB), and VOS (0.1mV) give the amplifier excellent operational amplifier DC performance.

The LM4562 has a wide supply range of ±2.5V to ±17V. Over this supply range the LM4562's input circuitry maintains excellent common-mode and power supply rejection, as well as maintaining its low input bias current. The LM4562 is unity gain stable. This Audio Operational Amplifier achieves outstanding AC performance while driving complex loads with values as high as 100pF.

The LM4562 is available in an 8-lead narrow body SOIC, an 8-lead PDIP, and an 8-lead TO-99.

### Typical Application

A. 1% metal film resistors, 5% polypropylene capacitors

Passively Equalized RIAA Phono Preamplifier

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Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of the Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

**ABSOLUTE MAXIMUM RATINGS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Specification</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Supply Voltage ($V_S = V^+ - V^-$)</td>
<td>36V</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-65°C to 150°C</td>
</tr>
<tr>
<td>Input Voltage</td>
<td>$(V^-) - 0.7V$ to $(V^+) + 0.7V$</td>
</tr>
<tr>
<td>Output Short Circuit</td>
<td>Continuous</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>Internally Limited</td>
</tr>
<tr>
<td>ESD Susceptibility (5)</td>
<td>2000V</td>
</tr>
<tr>
<td>ESD Susceptibility (6)</td>
<td>Pins 1, 4, 7 and 8: 200V Pins 2, 3, 5 and 6: 100V</td>
</tr>
<tr>
<td>Junction Temperature</td>
<td>150°C</td>
</tr>
<tr>
<td>Thermal Resistance</td>
<td></td>
</tr>
<tr>
<td>$\theta_{JA}$ (D)</td>
<td>145°C/W</td>
</tr>
<tr>
<td>$\theta_{JA}$ (P)</td>
<td>102°C/W</td>
</tr>
<tr>
<td>$\theta_{JA}$ (LMC)</td>
<td>150°C/W</td>
</tr>
<tr>
<td>$\theta_{JC}$ (LMC)</td>
<td>35°C/W</td>
</tr>
<tr>
<td>Temperature Range ($T_{MIN} \leq T_A \leq T_{MAX}$)</td>
<td>$-40°C \leq T_A \leq 85°C$</td>
</tr>
<tr>
<td>Supply Voltage Range</td>
<td>$\pm 2.5V \leq V_S \leq \pm 17V$</td>
</tr>
</tbody>
</table>

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.
(2) Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The ensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.
(3) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.
(4) Amplifier output connected to GND, any number of amplifiers within a package.
(5) Human body model, 100pF discharged through a 1.5kΩ resistor.
(6) Machine Model ESD test is covered by specification EIAJ IC-121-1981. A 200pF cap is charged to the specified voltage and then discharged directly into the IC with no external series resistor (resistance of discharge path must be under 50Ω).
ELECTRICAL CHARACTERISTICS FOR THE LM4562

The specifications apply for $V_S = \pm 15V$, $R_L = 2k\Omega$, $f_{IN} = 1kHz$, $T_A = 25^\circ C$, unless otherwise specified.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>LM4562</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td>Typical(3)</td>
<td>Limit(4)</td>
</tr>
<tr>
<td>THD+N</td>
<td>Total Harmonic Distortion - Noise</td>
<td>$A_V = 1$, $V_{OUT} = 3V_{rms}$</td>
<td>0.00003</td>
<td>0.00009 % (max)</td>
</tr>
<tr>
<td></td>
<td>$R_L = 2k\Omega$</td>
<td>$R_L = 600\Omega$</td>
<td>0.00003</td>
<td>0.00009 % (max)</td>
</tr>
<tr>
<td>IMD</td>
<td>Intermodulation Distortion</td>
<td>$A_V = 1$, $V_{OUT} = 3V_{rms}$ Two-tone, 60Hz &amp; 7kHz 4:1</td>
<td>0.00005</td>
<td>%</td>
</tr>
<tr>
<td>GBWP</td>
<td>Gain Bandwidth Product</td>
<td>$V_{OUT} = 1V$, $V_{IN} = 3V_{rms}$</td>
<td>55</td>
<td>45 MHz (min)</td>
</tr>
<tr>
<td>SR</td>
<td>Slew Rate</td>
<td>$V_{OUT} = 1V$, $V_{IN} = 3V_{rms}$ referenced to output magnitude at $f = 1kHz$</td>
<td>±20</td>
<td>±15 V/µs (min)</td>
</tr>
<tr>
<td>FPBW</td>
<td>Full Power Bandwidth</td>
<td>$f_{IN} = 1kHz$</td>
<td>10</td>
<td>MHz</td>
</tr>
<tr>
<td>$t_s$</td>
<td>Settling time</td>
<td>$A_V = -1$, 10V step, $C_L = 100pF$ 0.1% error range</td>
<td>1.2</td>
<td>µs</td>
</tr>
<tr>
<td>$e_n$</td>
<td>Equivalent Input Noise Voltage</td>
<td>$f_{BW} = 20Hz$ to 20kHz</td>
<td>0.34</td>
<td>0.65 µV_{RMS} (max)</td>
</tr>
<tr>
<td></td>
<td>Equivalent Input Noise Density</td>
<td>$f = 1kHz$</td>
<td>2.7</td>
<td>6.4 nV/√Hz (max)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$f = 10Hz$</td>
<td>4.7</td>
<td>8.6 nV/√Hz (max)</td>
</tr>
<tr>
<td>$I_n$</td>
<td>Current Noise Density</td>
<td>$f = 1kHz$</td>
<td>1.6</td>
<td>3.1 pA/√Hz (max)</td>
</tr>
<tr>
<td>$V_{OS}$</td>
<td>Offset Voltage</td>
<td>$V_{IN} = 1V$, $V_{OUT} = 3V_{rms}$</td>
<td>±0.1</td>
<td>±0.7 mV (max)</td>
</tr>
<tr>
<td>$\Delta V_{OS}/\Delta T_{Temp}$</td>
<td>Average Input Offset Voltage Drift vs Temperature</td>
<td>$-40^\circ C \leq T_A \leq 85^\circ C$</td>
<td>0.2</td>
<td>µV/°C</td>
</tr>
<tr>
<td>$\Delta V_{S}$</td>
<td>Average Input Offset Voltage Shift vs Power Supply Voltage</td>
<td></td>
<td>120</td>
<td>110 dB (min)</td>
</tr>
<tr>
<td>ISOCH-CH</td>
<td>Channel-to-Channel Isolation</td>
<td>$V_{OS}$ is measured at two supply voltages, ±5V and ±15V. $PSRR =</td>
<td>20\log(\Delta V_{OS}/\Delta V_S)</td>
<td>$.</td>
</tr>
<tr>
<td>$I_B$</td>
<td>Input Bias Current</td>
<td>$V_{CM} = 0V$</td>
<td>10</td>
<td>72 nA (max)</td>
</tr>
<tr>
<td>$\Delta I_{OS}/\Delta T_{Temp}$</td>
<td>Input Bias Current Drift vs Temperature</td>
<td>$-40^\circ C \leq T_A \leq 85^\circ C$</td>
<td>0.1</td>
<td>nA/°C</td>
</tr>
<tr>
<td>$I_{OS}$</td>
<td>Input Offset Current</td>
<td>$V_{CM} = 0V$</td>
<td>11</td>
<td>65 nA (max)</td>
</tr>
<tr>
<td>$V_{IN-CM}$</td>
<td>Common-Mode Input Voltage Range</td>
<td></td>
<td>+14.1</td>
<td>–13.9 (V+) – 2.0 (V–) V (min)</td>
</tr>
<tr>
<td>CMRR</td>
<td>Common-Mode Rejection</td>
<td>$-10V &lt; V_{CM} &lt; 10V$</td>
<td>120</td>
<td>110 dB (min)</td>
</tr>
<tr>
<td>$Z_{IN}$</td>
<td>Differential Input Impedance</td>
<td></td>
<td>30</td>
<td>kΩ</td>
</tr>
<tr>
<td></td>
<td>Common Mode Input Impedance</td>
<td>$-10V &lt; V_{CM} &lt; 10V$</td>
<td>1000</td>
<td>MΩ</td>
</tr>
<tr>
<td>$A_{VOL}$</td>
<td>Open Loop Voltage Gain</td>
<td>$-10V &lt; V_{OUT} &lt; 10V$, $R_L = 600\Omega$</td>
<td>140</td>
<td>125 dB (min)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$-10V &lt; V_{OUT} &lt; 10V$, $R_L = 2k\Omega$</td>
<td>140</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$-10V &lt; V_{OUT} &lt; 10V$, $R_L = 10k\Omega$</td>
<td>140</td>
<td></td>
</tr>
<tr>
<td>$V_{OUT_{MAX}}$</td>
<td>Maximum Output Voltage Swing</td>
<td>$R_L = 600\Omega$</td>
<td>±13.6</td>
<td>±12.5 V (min)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 2k\Omega$</td>
<td>±14.0</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$R_L = 10k\Omega$</td>
<td>±14.1</td>
<td></td>
</tr>
<tr>
<td>$I_{OUT}$</td>
<td>Output Current</td>
<td>$R_L = 600\Omega$, $V_S = \pm 17V$</td>
<td>±26</td>
<td>±23 mA (min)</td>
</tr>
<tr>
<td>$I_{OUT-CC}$</td>
<td>Instantaneous Short Circuit Current</td>
<td></td>
<td>+53</td>
<td>–42 mA</td>
</tr>
<tr>
<td>$R_{OUT}$</td>
<td>Output Impedance</td>
<td>$f_{IN} = 10kHz$</td>
<td>0.01</td>
<td>Ω</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Closed-Loop</td>
<td>13</td>
<td></td>
</tr>
</tbody>
</table>

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.
(2) Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. The ensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.
(3) Typical specifications are specified at +25°C and represent the most likely parametric norm.
(4) Tested limits are specified to AOQL (Average Outgoing Quality Level).
(5) PSRR is measured as follows: $V_{OS}$ is measured at two supply voltages, ±5V and ±15V. $PSRR = |20\log(\Delta V_{OS}/\Delta V_S)|$. 

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The specifications apply for $V_S = \pm 15\, \text{V}$, $R_L = 2\, \text{k}\Omega$, $f_{IN} = 1\, \text{kHz}$, $T_A = 25^\circ\text{C}$, unless otherwise specified.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Typical$^{(3)}$</th>
<th>Limit$^{(4)}$</th>
<th>Units (Limits)</th>
</tr>
</thead>
<tbody>
<tr>
<td>$C_{LOAD}$</td>
<td>Capacitive Load Drive Overshoot</td>
<td>100pF</td>
<td>16</td>
<td></td>
<td>%</td>
</tr>
<tr>
<td>$I_S$</td>
<td>Total Quiescent Current</td>
<td>$I_{OUT} = 0, \text{mA}$</td>
<td>10</td>
<td>12</td>
<td>mA (max)</td>
</tr>
</tbody>
</table>
TYPICAL PERFORMANCE CHARACTERISTICS

THD+N vs Output Voltage
V\textsubscript{CC} = 15V, V\textsubscript{EE} = –15V
R\textsubscript{L} = 2k\Omega

THD+N vs Output Voltage
V\textsubscript{CC} = 12V, V\textsubscript{EE} = –12V
R\textsubscript{L} = 2k\Omega

THD+N vs Output Voltage
V\textsubscript{CC} = 17V, V\textsubscript{EE} = –17V
R\textsubscript{L} = 2k\Omega

THD+N vs Output Voltage
V\textsubscript{CC} = 2.5V, V\textsubscript{EE} = –2.5V
R\textsubscript{L} = 2k\Omega

THD+N vs Output Voltage
V\textsubscript{CC} = 15V, V\textsubscript{EE} = –15V
R\textsubscript{L} = 600\Omega

THD+N vs Output Voltage
V\textsubscript{CC} = 12V, V\textsubscript{EE} = –12V
R\textsubscript{L} = 600\Omega

Figure 3.

Figure 4.

Figure 5.

Figure 6.

Figure 7.

Figure 8.
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

THD+N vs Output Voltage

---

Figure 9.

THD+N vs Output Voltage

---

Figure 10.

THD+N vs Output Voltage

---

Figure 11.

THD+N vs Output Voltage

---

Figure 12.

THD+N vs Output Voltage

---

Figure 13.

THD+N vs Output Voltage

---

Figure 14.
Figure 15. THD+N vs Frequency

$V_{CC} = 15V$, $V_{EE} = -15V$, $V_{OUT} = 3V_{RMS}$

Figure 16. THD+N vs Frequency

$V_{CC} = 12V$, $V_{EE} = -12V$, $V_{OUT} = 3V_{RMS}$

Figure 17. THD+N vs Frequency

$V_{CC} = 17V$, $V_{EE} = -17V$, $V_{OUT} = 3V_{RMS}$

Figure 18. THD+N vs Frequency

$V_{CC} = 15V$, $V_{EE} = -15V$, $V_{OUT} = 3V_{RMS}$

$R_L = 2k\Omega$

Figure 19. THD+N vs Frequency

$V_{CC} = 12V$, $V_{EE} = -12V$, $V_{OUT} = 3V_{RMS}$

$R_L = 600\Omega$

Figure 20. THD+N vs Frequency

$V_{CC} = 17V$, $V_{EE} = -17V$, $V_{OUT} = 3V_{RMS}$

$R_L = 600\Omega$
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

THD+N vs Frequency
$V_{CC} = 15V, V_{EE} = -15V, V_{OUT} = 3V_{RMS}$
$R_L = 10\Omega$

Figure 21.

THD+N vs Frequency
$V_{CC} = 12V, V_{EE} = -12V, V_{OUT} = 3V_{RMS}$
$R_L = 10\Omega$

Figure 22.

IMD vs Output Voltage
$V_{CC} = 17V, V_{EE} = -17V, V_{OUT} = 3V_{RMS}$
$R_L = 10\Omega$

Figure 23.

IMD vs Output Voltage
$V_{CC} = 15V, V_{EE} = -15V$
$R_L = 2\Omega$

Figure 24.

IMD vs Output Voltage
$V_{CC} = 12V, V_{EE} = -12V$
$R_L = 2\Omega$

Figure 25.

IMD vs Output Voltage
$V_{CC} = 2.5V, V_{EE} = -2.5V$
$R_L = 2\Omega$

Figure 26.
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

IMD vs Output Voltage

**Figure 27.**

IMD vs Output Voltage

**Figure 28.**

IMD vs Output Voltage

**Figure 29.**

IMD vs Output Voltage

**Figure 30.**

IMD vs Output Voltage

**Figure 31.**

IMD vs Output Voltage

**Figure 32.**
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**IMD vs Output Voltage**
- **$V_{CC} = 12V$, $V_{EE} = –12V$, $R_L = 10k\Omega$**
- **$V_{CC} = 17V$, $V_{EE} = –17V$, $R_L = 10k\Omega$**

**Voltage Noise Density vs Frequency**
- **$V_S = 30V$, $V_{CM} = 15V$**
- **2.7 nV/\sqrt{Hz}**

**Current Noise Density vs Frequency**
- **$V_S = 30V$, $V_{CM} = 15V$**
- **1.6 pA/\sqrt{Hz}**

Product Folder Links: LM4562
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Crosstalk vs Frequency

**Figure 39.**

*V<sub>CC</sub> = 15V, V<sub>EE</sub> = −15V, V<sub>OUT</sub> = 10V<sub>RMS</sub>,
A<sub>V</sub> = 0dB, R<sub>L</sub> = 2kΩ*

**Figure 40.**

*V<sub>CC</sub> = 12V, V<sub>EE</sub> = −12V, V<sub>OUT</sub> = 3V<sub>RMS</sub>,
A<sub>V</sub> = 0dB, R<sub>L</sub> = 2kΩ*

**Figure 41.**

*V<sub>CC</sub> = 12V, V<sub>EE</sub> = −12V, V<sub>OUT</sub> = 10V<sub>RMS</sub>,
A<sub>V</sub> = 0dB, R<sub>L</sub> = 2kΩ*

**Figure 42.**

*V<sub>CC</sub> = 17V, V<sub>EE</sub> = −17V, V<sub>OUT</sub> = 3V<sub>RMS</sub>,
A<sub>V</sub> = 0dB, R<sub>L</sub> = 2kΩ*

**Figure 43.**

*V<sub>CC</sub> = 17V, V<sub>EE</sub> = −17V, V<sub>OUT</sub> = 10V<sub>RMS</sub>,
A<sub>V</sub> = 0dB, R<sub>L</sub> = 2kΩ*

**Figure 44.**

*V<sub>CC</sub> = 2.5V, V<sub>EE</sub> = −2.5V, V<sub>OUT</sub> = 1V<sub>RMS</sub>,
A<sub>V</sub> = 0dB, R<sub>L</sub> = 2kΩ*
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Crosstalk vs Frequency

\( V_{CC} = 15V, V_{EE} = -15V, V_{OUT} = 3V_{RMS} \)
\( A_V = 0dB, R_L = 600\Omega \)

Figure 45.

\( V_{CC} = 12V, V_{EE} = -12V, V_{OUT} = 3V_{RMS} \)
\( A_V = 0dB, R_L = 600\Omega \)

Figure 47.

\( V_{CC} = 17V, V_{EE} = -17V, V_{OUT} = 3V_{RMS} \)
\( A_V = 0dB, R_L = 600\Omega \)

Figure 49.

Crosstalk vs Frequency

\( V_{CC} = 15V, V_{EE} = -15V, V_{OUT} = 10V_{RMS} \)
\( A_V = 0dB, R_L = 600\Omega \)

Figure 46.

\( V_{CC} = 12V, V_{EE} = -12V, V_{OUT} = 10V_{RMS} \)
\( A_V = 0dB, R_L = 600\Omega \)

Figure 48.

\( V_{CC} = 17V, V_{EE} = -17V, V_{OUT} = 10V_{RMS} \)
\( A_V = 0dB, R_L = 600\Omega \)

Figure 50.
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Crosstalk vs Frequency

Figure 51.

- V\text{CC} = 2.5V, V\text{EE} = –2.5V, V\text{OUT} = 1V\text{RMS}
- V\text{CC} = 2.5V, V\text{EE} = –2.5V, V\text{OUT} = 1V\text{RMS}

Figure 52.

- V\text{CC} = 15V, V\text{EE} = –15V, V\text{OUT} = 3V\text{RMS}
- V\text{CC} = 15V, V\text{EE} = –15V, V\text{OUT} = 3V\text{RMS}

Figure 53.

- V\text{CC} = 12V, V\text{EE} = –12V, V\text{OUT} = 10V\text{RMS}
- V\text{CC} = 12V, V\text{EE} = –12V, V\text{OUT} = 10V\text{RMS}

Figure 54.

- V\text{CC} = 17V, V\text{EE} = –17V, V\text{OUT} = 3V\text{RMS}
- V\text{CC} = 17V, V\text{EE} = –17V, V\text{OUT} = 3V\text{RMS}

Figure 55.

- A\text{V} = 0dB, R\text{L} = 600\Omega
- A\text{V} = 0dB, R\text{L} = 10k\Omega

Figure 56.

- A\text{V} = 0dB, R\text{L} = 600\Omega
- A\text{V} = 0dB, R\text{L} = 10k\Omega

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Product Folder Links: LM4562
### TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**Crosstalk vs Frequency**

- **$V_{CC} = 17V$, $V_{EE} = -17V$, $V_{OUT} = 10V_{RMS}$**
- $A_V = 0dB$, $R_L = 10k\Omega$

![Crosstalk vs Frequency Graph](image)

**PSRR+ vs Frequency**

- **$V_{CC} = 15V$, $V_{EE} = -15V$**
- $R_L = 10k\Omega$, $f = 200kHz$, $V_{RIPPLE} = 200mVpp$

![PSRR+ vs Frequency Graph](image)

**PSRR- vs Frequency**

- **$V_{CC} = 15V$, $V_{EE} = -15V$**
- $R_L = 2k\Omega$, $f = 200kHz$, $V_{RIPPLE} = 200mVpp$

![PSRR- vs Frequency Graph](image)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**PSRR+ vs Frequency**

$V_{CC} = 15V, V_{EE} = -15V$

$R_L = 600\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

![Figure 63.](image)

**PSRR– vs Frequency**

$V_{CC} = 15V, V_{EE} = -15V$

$R_L = 600\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

![Figure 64.](image)

$V_{CC} = 12V, V_{EE} = -12V$

$R_L = 10k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

![Figure 65.](image)

$V_{CC} = 12V, V_{EE} = -12V$

$R_L = 10k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

![Figure 66.](image)

$V_{CC} = 12V, V_{EE} = -12V$

$R_L = 2k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

![Figure 67.](image)

$V_{CC} = 12V, V_{EE} = -12V$

$R_L = 2k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

![Figure 68.](image)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

PSRR+ vs Frequency
$V_{CC} = 12V, V_{EE} = -12V$
$R_L = 600\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

PSRR− vs Frequency
$V_{CC} = 12V, V_{EE} = -12V$
$R_L = 600\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

Figure 69.

PSRR+ vs Frequency
$V_{CC} = 17V, V_{EE} = -17V$
$R_L = 10k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

PSRR− vs Frequency
$V_{CC} = 17V, V_{EE} = -17V$
$R_L = 10k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

Figure 70.

PSRR+ vs Frequency
$V_{CC} = 17V, V_{EE} = -17V$
$R_L = 2k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

PSRR− vs Frequency
$V_{CC} = 17V, V_{EE} = -17V$
$R_L = 2k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

Figure 71.

Figure 72.

Figure 73.

Figure 74.
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**Figure 75.**

**PSRR+ vs Frequency**

\[ V_{CC} = 17V, V_{EE} = -17V \]

\[ R_L = 600\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp \]

**Figure 76.**

**PSRR– vs Frequency**

\[ V_{CC} = 17V, V_{EE} = -17V \]

\[ R_L = 600\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp \]

**Figure 77.**

**PSRR+ vs Frequency**

\[ V_{CC} = 2.5V, V_{EE} = -2.5V \]

\[ R_L = 10k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp \]

**Figure 78.**

**PSRR– vs Frequency**

\[ V_{CC} = 2.5V, V_{EE} = -2.5V \]

\[ R_L = 10k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp \]

**Figure 79.**

**PSRR+ vs Frequency**

\[ V_{CC} = 2.5V, V_{EE} = -2.5V \]

\[ R_L = 2k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp \]

**Figure 80.**

**PSRR– vs Frequency**

\[ V_{CC} = 2.5V, V_{EE} = -2.5V \]

\[ R_L = 2k\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp \]
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

PSRR+ vs Frequency
$V_{CC} = 2.5V, V_{EE} = -2.5V$
$R_L = 600\,\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

Figure 81.

PSRR– vs Frequency
$V_{CC} = 2.5V, V_{EE} = -2.5V$
$R_L = 600\,\Omega, f = 200kHz, V_{RIPPLE} = 200mVpp$

Figure 82.

CMRR vs Frequency
$V_{CC} = 15V, V_{EE} = -15V$

$V_{CC} = 12V, V_{EE} = -12V$
$R_L = 2k\Omega$

Figure 83.

Figure 84.

CMRR vs Frequency
$V_{CC} = 17V, V_{EE} = -17V$
$R_L = 2k\Omega$

Figure 85.

Figure 86.
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

CMRR vs Frequency

V_{CC} = 15V, V_{EE} = –15V
R_{L} = 600Ω

Figure 87.

CMRR vs Frequency

V_{CC} = 12V, V_{EE} = –12V
R_{L} = 600Ω

Figure 88.

CMRR vs Frequency

V_{CC} = 17V, V_{EE} = –17V
R_{L} = 600Ω

Figure 89.

CMRR vs Frequency

V_{CC} = 2.5V, V_{EE} = –2.5V
R_{L} = 600Ω

Figure 90.

CMRR vs Frequency

V_{CC} = 15V, V_{EE} = –15V
R_{L} = 10kΩ

Figure 91.

CMRR vs Frequency

V_{CC} = 12V, V_{EE} = –12V
R_{L} = 10kΩ

Figure 92.
### CMRR vs Frequency

**VCC = 17V, VEE = –17V**  
**RL = 10kΩ**

<table>
<thead>
<tr>
<th>Frequency (Hz)</th>
<th>CMRR (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>0</td>
</tr>
<tr>
<td>100</td>
<td>-20</td>
</tr>
<tr>
<td>1k</td>
<td>-40</td>
</tr>
<tr>
<td>10k</td>
<td>-60</td>
</tr>
<tr>
<td>20k</td>
<td>-80</td>
</tr>
<tr>
<td>100k</td>
<td>-100</td>
</tr>
</tbody>
</table>

**VCC = 2.5V, VEE = –2.5V**  
**RL = 10kΩ**

<table>
<thead>
<tr>
<th>Frequency (Hz)</th>
<th>CMRR (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>0</td>
</tr>
<tr>
<td>100</td>
<td>-20</td>
</tr>
<tr>
<td>1k</td>
<td>-40</td>
</tr>
<tr>
<td>10k</td>
<td>-60</td>
</tr>
<tr>
<td>20k</td>
<td>-80</td>
</tr>
<tr>
<td>100k</td>
<td>-100</td>
</tr>
</tbody>
</table>

**Output Voltage vs Load Resistance**

**VDD = 15V, VEE = –15V**  
**THD+N = 1%**

<table>
<thead>
<tr>
<th>Load Resistance (Ω)</th>
<th>Output (Vrms)</th>
</tr>
</thead>
<tbody>
<tr>
<td>500</td>
<td>9.0</td>
</tr>
<tr>
<td>600</td>
<td>9.5</td>
</tr>
<tr>
<td>800</td>
<td>10.0</td>
</tr>
<tr>
<td>2k</td>
<td>10.5</td>
</tr>
<tr>
<td>5k</td>
<td>11.0</td>
</tr>
<tr>
<td>10k</td>
<td>11.5</td>
</tr>
</tbody>
</table>

**VDD = 12V, VEE = –12V**  
**THD+N = 1%**

<table>
<thead>
<tr>
<th>Load Resistance (Ω)</th>
<th>Output (Vrms)</th>
</tr>
</thead>
<tbody>
<tr>
<td>500</td>
<td>7.0</td>
</tr>
<tr>
<td>600</td>
<td>7.5</td>
</tr>
<tr>
<td>800</td>
<td>8.0</td>
</tr>
<tr>
<td>2k</td>
<td>8.5</td>
</tr>
<tr>
<td>5k</td>
<td>9.0</td>
</tr>
<tr>
<td>10k</td>
<td>9.5</td>
</tr>
</tbody>
</table>

**VDD = 2.5V, VEE = –2.5V**  
**THD+N = 1%**

<table>
<thead>
<tr>
<th>Load Resistance (Ω)</th>
<th>Output (Vrms)</th>
</tr>
</thead>
<tbody>
<tr>
<td>500</td>
<td>0.0</td>
</tr>
<tr>
<td>600</td>
<td>0.25</td>
</tr>
<tr>
<td>800</td>
<td>0.50</td>
</tr>
<tr>
<td>2k</td>
<td>0.75</td>
</tr>
<tr>
<td>5k</td>
<td>1.00</td>
</tr>
<tr>
<td>10k</td>
<td>1.25</td>
</tr>
</tbody>
</table>
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Output Voltage vs Supply Voltage
- $R_L = 2\, \Omega$, THD+N = 1%
- $R_L = 600\, \Omega$, THD+N = 1%

Supply Current vs Supply Voltage
- $R_L = 10\, \Omega$, THD+N = 1%
- $R_L = 2\, \Omega$
- $R_L = 600\, \Omega$
- $R_L = 10\, \Omega$

Figure 99.
Figure 100.
Figure 101.
Figure 102.
Figure 103.
Figure 104.
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Full Power Bandwidth vs Frequency

FREQUENCY (Hz)

Gain Phase vs Frequency

FREQUENCY (Hz)

Small-Signal Transient Response

Small-Signal Transient Response

Figure 105.

Figure 106.

Figure 107.

Figure 108.
DISTORTION MEASUREMENTS

The vanishingly low residual distortion produced by LM4562 is below the capabilities of all commercially available equipment. This makes distortion measurements just slightly more difficult than simply connecting a distortion meter to the amplifier’s inputs and outputs. The solution, however, is quite simple: an additional resistor. Adding this resistor extends the resolution of the distortion measurement equipment.

The LM4562’s low residual distortion is an input referred internal error. As shown in Figure 109, adding the 10Ω resistor connected between the amplifier’s inverting and non-inverting inputs changes the amplifier’s noise gain. The result is that the error signal (distortion) is amplified by a factor of 101. Although the amplifier’s closed-loop gain is unaltered, the feedback available to correct distortion errors is reduced by 101, which means that measurement resolution increases by 101. To ensure minimum effects on distortion measurements, keep the value of R1 low as shown in Figure 109.

This technique is verified by duplicating the measurements with high closed loop gain and/or making the measurements at high frequencies. Doing so produces distortion components that are within the measurement equipment’s capabilities. This datasheet’s THD+N and IMD values were generated using the above described circuit connected to an Audio Precision System Two Cascade.

The LM4562 is a high-speed op amp with excellent phase margin and stability. Capacitive loads up to 100pF will cause little change in the phase characteristics of the amplifiers and are therefore allowable.

Capacitive loads greater than 100pF must be isolated from the output. The most straightforward way to do this is to put a resistor in series with the output. This resistor will also prevent excess power dissipation if the output is accidentally shorted.
A. Complete shielding is required to prevent induced pick up from external sources. Always check with oscilloscope for power line noise.

**Figure 110. Noise Measurement Circuit**

- Total Gain: 115 dB @ \( f = 1 \) kHz
- Input Referred Noise Voltage: \( e_n = V_0/560,000 \) (V)

---

**Figure 111. RIAA Preamp Voltage Gain, RIAA Deviation vs Frequency**

**Figure 112. Flat Amp Voltage Gain vs Frequency**
Evaluation Module Schematic

Figure 113. Inverting Amplifiers

Typical Applications

Figure 114. NAB Preamp

\[ A_v = 34.5 \]
\[ F = 1 \text{ kHz} \]
\[ E_n = 0.38 \text{ µV} \]
\[ A \text{ Weighted} \]
Figure 115. NAB Preamp Voltage Gain vs Frequency

\[ V_O = V_1 - V_2 \]

Figure 116. Balanced to Single-Ended Converter

\[ V_O = V_1 + V_2 - V_3 - V_4 \]

Figure 117. Adder/Subtractor

\[ f_o = \frac{1}{2\pi RC} \]

Figure 118. Sine Wave Oscillator
$R_1 = \frac{\sqrt{2}}{2\omega_0 C}$

$R_2 = 2\times R_1$

Illustration is $f_0 = 1$ kHz

**Figure 119. Second-Order High-Pass Filter (Butterworth)**

$C_1 = \frac{\sqrt{2}}{\omega_0 R}$

$C_2 = \frac{C_1}{2}$

Illustration is $f_0 = 1$ kHz

**Figure 120. Second-Order Low-Pass Filter (Butterworth)**
\[ f_0 = \frac{1}{2\pi C_1 R_1}, \quad Q = \frac{1}{2} \left( 1 + \frac{R_2}{R_0} \cdot \frac{R_2}{R_G} \right), \quad A_{BP} = Q_{ALP} = Q_{ALH} = \frac{R_2}{R_G} \]

Illustration is \( f_0 = 1 \text{ kHz}, \quad Q = 10, \quad A_{BP} = 1 \)

**Figure 121. State Variable Filter**

**Figure 122. AC/DC Converter**

**Figure 123. 2-Channel Panning Circuit (Pan Pot)**
The equations started above are simplifications, providing guidance of general –3dB point values, when the potentiometers are at their null position.

Illustration is:

\[ f_L = \frac{1}{2\pi R_2 C_1} \quad f_{LB} = \frac{1}{2\pi R_1 C_1} \]

\[ f_H = \frac{1}{2\pi R_5 C_2} \quad f_{HB} = \frac{1}{2\pi (R_1 + R_5 + 2R_3) C_2} \]

The equations started above are simplifications, providing guidance of general –3dB point values, when the potentiometers are at their null position.

Illustration is:

\[ f_L = 32 \text{ Hz}, f_{LB} = 320 \text{ Hz} \]

\[ f_H = 11 \text{ kHz}, f_{HB} = 1.1 \text{ kHz} \]
$A_v = 35 \text{ dB}$

$E_n = 0.33 \, \mu V \ S/N = 90 \, \text{dB}$

$f = 1 \, \text{kHz}$

A Weighted

A Weighted, $V_{IN} = 10 \, \text{mV}$

@f = 1 kHz

**Figure 126. RIAA Preamp**

If $R_2 = R_5$, $R_3 = R_6$, $R_4 = R_7$

$V_0 = \left(1 + \frac{2R_2}{R_1}\right) \frac{R_4}{R_3} (V_2 - V_1)$

Illustration is:

$V_0 = 101(V_2 - V_1)$

**Figure 127. Balanced Input Mic Amp**
Figure 128. 10-Band Graphic Equalizer

Table 1. \( C_1 \), \( C_2 \), \( R_1 \), and \( R_2 \) Values for Figure 128\(^{(1)}\)

<table>
<thead>
<tr>
<th>( f_0 ) (Hz)</th>
<th>( C_1 )</th>
<th>( C_2 )</th>
<th>( R_1 )</th>
<th>( R_2 )</th>
</tr>
</thead>
<tbody>
<tr>
<td>32</td>
<td>0.12( \mu )F</td>
<td>4.7( \mu )F</td>
<td>75k( \Omega )</td>
<td>500( \Omega )</td>
</tr>
<tr>
<td>64</td>
<td>0.056( \mu )F</td>
<td>3.3( \mu )F</td>
<td>68k( \Omega )</td>
<td>510( \Omega )</td>
</tr>
<tr>
<td>125</td>
<td>0.033( \mu )F</td>
<td>1.5( \mu )F</td>
<td>62k( \Omega )</td>
<td>510( \Omega )</td>
</tr>
<tr>
<td>250</td>
<td>0.015( \mu )F</td>
<td>0.82( \mu )F</td>
<td>68k( \Omega )</td>
<td>470( \Omega )</td>
</tr>
<tr>
<td>500</td>
<td>8200pF</td>
<td>0.39( \mu )F</td>
<td>62k( \Omega )</td>
<td>470( \Omega )</td>
</tr>
<tr>
<td>1k</td>
<td>3900pF</td>
<td>0.22( \mu )F</td>
<td>68k( \Omega )</td>
<td>470( \Omega )</td>
</tr>
<tr>
<td>2k</td>
<td>2000pF</td>
<td>0.1( \mu )F</td>
<td>68k( \Omega )</td>
<td>470( \Omega )</td>
</tr>
<tr>
<td>4k</td>
<td>1100pF</td>
<td>0.056( \mu )F</td>
<td>62k( \Omega )</td>
<td>470( \Omega )</td>
</tr>
<tr>
<td>8k</td>
<td>510pF</td>
<td>0.022( \mu )F</td>
<td>68k( \Omega )</td>
<td>510( \Omega )</td>
</tr>
<tr>
<td>16k</td>
<td>330pF</td>
<td>0.012( \mu )F</td>
<td>51k( \Omega )</td>
<td>510( \Omega )</td>
</tr>
</tbody>
</table>

\(^{(1)}\) At volume of change = ±12 dB \( Q = 1.7 \)
## REVISION HISTORY

Changes from Revision J (April 2013) to Revision K

<table>
<thead>
<tr>
<th>Changes</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>Added EVM schematic</td>
<td>25</td>
</tr>
</tbody>
</table>
## REVISION HISTORY

<table>
<thead>
<tr>
<th>Rev</th>
<th>Date</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>08/16/06</td>
<td>Initial release.</td>
</tr>
<tr>
<td>1.1</td>
<td>08/22/06</td>
<td>Updated the Instantaneous Short Circuit Current specification.</td>
</tr>
<tr>
<td>1.2</td>
<td>09/12/06</td>
<td>Updated the three ±15V CMRR Typical Performance Curves.</td>
</tr>
<tr>
<td>1.3</td>
<td>09/26/06</td>
<td>Updated interstage filter capacitor values on page 1 Typical Application schematic.</td>
</tr>
<tr>
<td>1.4</td>
<td>05/03/07</td>
<td>Added the “general note” under the EC table.</td>
</tr>
<tr>
<td>1.5</td>
<td>10/17/07</td>
<td>Replaced all the PSRR curves.</td>
</tr>
<tr>
<td>1.6</td>
<td>01/26/10</td>
<td>Edited the equations on page 28 (under Tone Control).</td>
</tr>
<tr>
<td>J</td>
<td>04/04/13</td>
<td>Changed layout of National Data Sheet to TI format</td>
</tr>
</tbody>
</table>
# Packaging Information

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM4562MA/NOPB</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>95</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>L4562MA</td>
<td>Samples</td>
</tr>
<tr>
<td>LM4562MAX/NOPB</td>
<td>ACTIVE</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>L4562MA</td>
<td>Samples</td>
</tr>
<tr>
<td>LM4562NA/NOPB</td>
<td>ACTIVE</td>
<td>PDIP</td>
<td>P</td>
<td>8</td>
<td>40</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SN</td>
<td>Level-1-NA-UNLIM</td>
<td>-40 to 85</td>
<td>LM4562NA</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt**: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green**: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
### TAPE AND REEL INFORMATION

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM4562MAX/NOPB</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>330.0</td>
<td>12.4</td>
<td>6.5</td>
<td>5.4</td>
<td>2.0</td>
<td>8.0</td>
<td>12.0</td>
<td>Q1</td>
</tr>
</tbody>
</table>

*All dimensions are nominal.*

- **A0**: Dimension designed to accommodate the component width
- **B0**: Dimension designed to accommodate the component length
- **K0**: Dimension designed to accommodate the component thickness
- **W**: Overall width of the carrier tape
- **P1**: Pitch between successive cavity centers
**TAPE AND REEL BOX DIMENSIONS**

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM4562MAX/NOPB</td>
<td>SOIC</td>
<td>D</td>
<td>8</td>
<td>2500</td>
<td>367.0</td>
<td>367.0</td>
<td>35.0</td>
</tr>
</tbody>
</table>

*All dimensions are nominal*
NOTES:

1. Linear dimensions are in inches [millimeters]. Dimensions in parenthesis are for reference only. Controlling dimensions are in inches. Dimensioning and tolerancing per ASME Y14.5M.

2. This drawing is subject to change without notice.

3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 [0.15] per side.

4. This dimension does not include interlead flash.

5. Reference JEDEC registration MS-012, variation AA.
6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate
design recommendations.
9. Board assembly site may have different recommendations for stencil design.
P (R-PDIP-T8)  PLASTIC DUAL-IN-LINE PACKAGE

NOTES:
A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Falls within JEDEC MS-001 variation BA.
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