Micro-Module Integrated Bluetooth® 1.2 Baseband Controller and Radio

Check for Samples: LMX5452

FEATURES
- Compliant with the Bluetooth 1.2 Core Specification
- -80 dBm Receiver Sensitivity (Typical)
- Class 2 Operation
- Low Power Consumption:
  - Advanced Power Management Features
  - Clocking Option 12/13 MHz with PLL Bypass Mode for Power Reduction
- High integration:
  - Implemented in 0.18 μm CMOS Technology
  - RF Includes On-chip Antenna Filter and Switch
  - On-chip Firmware with Complete HCI
- Embedded ROM and Patch RAM Memory
- Up to Seven Asynchronous Connection Less (ACL) Links
- Support for Two Simultaneous Voice or Synchronous Connection Oriented (SCO) Links
- Accepts 10-20 MHz External Clock or Crystal Network
- Fractional-N Sigma/Delta Modulator
- Operating Voltage Range 2.5–3.6V
- I/O Voltage Range 1.6–3.6V
- 60-pad micro-module NFBGA package (6.1 mm × 9.1 mm × 1.2 mm)

APPLICATIONS
- Mobile Handsets
- USB Dongles
- Stereo Headsets
- Personal Digital Assistants
- Personal Computers
- Automotive Telematics

INTERFACES
- Full-duplex UART Supporting Transfer Rates up to 921.6 kbps Including Baud Rate Detection for HCI
- Full Speed (12 Mbps) USB 2.0 for HCI
- ACCESS.bus and SPI/Microwire for Interfacing with External Non-volatile Memory
- Advanced Audio Interface (AAI) for Interfacing with External 8-kHz PCM Codec
- Up to 3 GPIO port Pins (OP4/PG4, PG6, PG7) Controllable by HCI Commands
- JTAG Based Serial On-chip Debug Interface
- Single Rx/Tx-pad Radio Interface

DESCRIPTION
The LMX5452 is a highly integrated Bluetooth 1.2 compliant solution. The integrated baseband controller and 2.4 GHz radio combine to form a complete, small form-factor (6.1 mm × 9.1 mm × 1.2 mm) Bluetooth node.

The baseband controller has a standard Host Controller Interface (HCI). Based on the Texas Instruments CompactRISC™ 16-bit processor, the LMX5452 is optimized to handle the audio, data, and link management processing requirements of a Bluetooth node.

The on-chip memory, ROM, and Patch RAM provide lowest cost and minimize design risk with the flexibility of firmware upgrades.

The firmware supplied in the on-chip ROM supports a complete Bluetooth Link Manager and HCI with communication through a UART or USB interface. This firmware features point-to-point and point-to-multipoint link management, supporting data rates up to 723 kbps.

The radio employs an integrated antenna filter and switch to minimize the number of external components.

The radio has a heterodyne receiver architecture with a low intermediate frequency (IF), which enables the IF filters to be integrated on-chip. The transmitter uses direct IQ-modulation with Gaussian-filtered bit-stream data, a voltage-controlled oscillator (VCO) buffer, and a power amplifier.
DESCRIPTION (CONTINUED)
The LMX5452 module is lead free and RoHS (Restriction of Hazardous Substances) compliant. For more information on those quality standards, please visit our quality and reliability website at http://focus.ti.com/quality/docs/qualityhome.tsp

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Functional Block Diagram

Connection Diagram

Figure 1. NFBGA, Plastic, Laminate, 9x6x1.2mm, 60 Ball, 0.8mm Pitch Package
REVISION HISTORY

Changes from Original (March 2013) to Revision A

• Changed layout of National Data Sheet to TI format ................................................................. 2
### PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LMX5452SMX/NOPB</td>
<td>ACTIVE</td>
<td>NFBGA</td>
<td>NZB</td>
<td>60</td>
<td>2500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>SNAGCU</td>
<td>Level-4-260C-72 HR</td>
<td>-40 to 85</td>
<td>5452SM</td>
<td>Samples</td>
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</table>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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### TAPE AND REEL INFORMATION

#### TAPE DIMENSIONS

<table>
<thead>
<tr>
<th>A0</th>
<th>Dimension designed to accommodate the component width</th>
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</thead>
<tbody>
<tr>
<td>B0</td>
<td>Dimension designed to accommodate the component length</td>
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<tr>
<td>K0</td>
<td>Dimension designed to accommodate the component thickness</td>
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<tr>
<td>W</td>
<td>Overall width of the carrier tape</td>
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<tr>
<td>P1</td>
<td>Pitch between successive cavity centers</td>
</tr>
</tbody>
</table>

#### REEL DIMENSIONS

- **Reel Diameter**: Nominal
- **Reel Width (W1)**: Nominal

#### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

- Sprocket Holes
- User Direction of Feed
- Pocket Quadrants

*All dimensions are nominal.

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
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### TAPE AND REEL BOX DIMENSIONS

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*All dimensions are nominal*
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