SNx4HC165 8-Bit Parallel-Load Shift Registers

1 Features

- Wide Operating Voltage Range of 2 V to 6 V
- Outputs Can Drive Up to 10 LSTTL Loads
- Low Power Consumption, 80-μA Maximum $I_{CC}$
- Typical $t_{pd} = 13$ ns
- ±4-mA Output Drive at 5 V
- Low Input Current of 1 μA Maximum
- Complementary Outputs
- Direct Overriding Load (Data) Inputs
- Gated Clock Inputs
- Parallel-to-Serial Data Conversion
- On Products Compliant to MIL-PRF-38535, All Parameters Are Tested Unless Otherwise Noted. On All Other Products, Production Processing Does Not Necessarily Include Testing of All Parameters.

2 Applications

- Programable Logic Controllers
- Appliances
- Video Display Systems
- Output Expander
- Keyboards

3 Description

The SNx4HC165 devices are 8-bit parallel-load shift registers that, when clocked, shift the data toward a serial ($Q_H$) output. Parallel-in access to each stage is provided by eight individual direct data (A–H) inputs that are enabled by a low level at the shift/load (SH/LD) input. The SNx4HC165 devices also feature a clock-inhibit (CLK INH) function and a complementary serial ($\overline{Q}_H$) output.

Clocking is accomplished by a low-to-high transition of the clock (CLK) input while SH/LD is held high and CLK INH is held low. The functions of CLK and CLK INH are interchangeable. Because a low CLK and a low-to-high transition of CLK INH also accomplish clocking, CLK INH must be changed to the high level only while CLK is high. Parallel loading is inhibited when SH/LD is held high. While SH/LD is low, the parallel inputs to the register are enabled independently of the levels of the CLK, CLK INH, or serial (SER) inputs.

Device Information(1)

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SN74HC165D</td>
<td>SOIC (16)</td>
<td>10.00 mm × 6.20 mm</td>
</tr>
<tr>
<td>SN74HC165DB</td>
<td>SSOP (16)</td>
<td>8.20 mm × 6.50 mm</td>
</tr>
<tr>
<td>SN74HC165N</td>
<td>PDIP (16)</td>
<td>6.60 mm × 18.92 mm</td>
</tr>
<tr>
<td>SN74HC165NS</td>
<td>SO (16)</td>
<td>8.20 mm × 9.90 mm</td>
</tr>
<tr>
<td>SN74HC165PW</td>
<td>TSSOP (16)</td>
<td>6.60 mm × 5.10 mm</td>
</tr>
<tr>
<td>SN54HC165FK</td>
<td>LCCC (20)</td>
<td>9.09 mm × 9.09 mm</td>
</tr>
<tr>
<td>SN54HC165J</td>
<td>CDIP (16)</td>
<td>21.34 mm × 7.52 mm</td>
</tr>
<tr>
<td>SN54HC165W</td>
<td>CFP (16)</td>
<td>9.40 mm × 7.75 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Logic Diagram Positive Logic

Pin numbers shown are for the D, DB, J, N, NS, PW and W packages.
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4 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision G (August 2013) to Revision H Page

• Added Applications section, Device Information table, Pin Configuration and Functions section, ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section ................................................................. 1
• Added Military Disclaimer to Features list ................................................................. 1
• Added ESD warning ................................................................. 16

Changes from Revision F (December 2010) to Revision G Page

• Updated document to new TI data sheet format - no specification changes ................................................................. 1
• Removed Ordering Information table ................................................................. 1
• Added Handling Ratings table ................................................................. 4
• Extended maximum temperature operating range from 85°C to 125°C ................................................................. 4
## 5 Pin Configuration and Functions

**D, DB, N, NS, J, W, or PW Package**

16-Pin SOIC, SSOP, PDIP, SO, CDIP, CFP, or TSSOP

### Top View

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>DB</td>
<td>I</td>
<td>Parallel Input</td>
</tr>
<tr>
<td>CB</td>
<td>I</td>
<td>Parallel Input</td>
</tr>
<tr>
<td>NC</td>
<td>—</td>
<td>Ground Pin</td>
</tr>
<tr>
<td>SH/LD</td>
<td>I</td>
<td>Clock input</td>
</tr>
<tr>
<td>QH</td>
<td>O</td>
<td>Serial Output</td>
</tr>
<tr>
<td>QH</td>
<td>O</td>
<td>Complementary Serial Output</td>
</tr>
<tr>
<td>SER</td>
<td>I</td>
<td>Serial Input</td>
</tr>
<tr>
<td>VCC</td>
<td>—</td>
<td>Power Pin</td>
</tr>
<tr>
<td>VCC</td>
<td>—</td>
<td>Power Pin</td>
</tr>
</tbody>
</table>

### FK Package

20-Pin LCCC

### Top View

- **PIN**
  - **NAME**
    - **D, DB, N, NS, PW, J or W**
    - **FK**
    - **I/O**
    - **DESCRIPTION**
  - **A**
    - 11
    - 14
    - I
    - Parallel Input
  - **B**
    - 12
    - 15
    - I
    - Parallel Input
  - **C**
    - 13
    - 17
    - I
    - Parallel Input
  - **CLK**
    - 2
    - 3
    - I
    - Clock input
  - **CLK INH**
    - 15
    - 19
    - I
    - Clock Inhibit, when High No change in output
  - **D**
    - 14
    - 18
    - I
    - Parallel Input
  - **E**
    - 3
    - 4
    - I
    - Parallel Input
  - **F**
    - 4
    - 5
    - I
    - Parallel Input
  - **G**
    - 5
    - 7
    - I
    - Parallel Input
  - **GND**
    - 8
    - 10
    - —
    - Ground Pin
  - **H**
    - 6
    - 8
    - I
    - Parallel Input
  - **NC**
    - —
    - 6
    - —
    - Not Connected
    - 11
    - 16
  - **QH**
    - 9
    - 12
    - O
    - Serial Output
  - **QH**
    - 7
    - 9
    - O
    - Complementary Serial Output
  - **SER**
    - 10
    - 13
    - I
    - Serial Input
  - **SH/LD**
    - 1
    - 2
    - I
    - Shift or Load input, When High Data, shifted. When Low data is loaded from parallel inputs
  - **VCC**
    - 16
    - 20
    - —
    - Power Pin

(1) NC – No internal connection

---

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Product Folder Links: SN54HC165 SN74HC165
6 Specifications

6.1 Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{CC}$</td>
<td>$-0.5$</td>
<td>$7$</td>
<td>V</td>
</tr>
<tr>
<td>$I_{IK}$</td>
<td>$-20$</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>$I_{OK}$</td>
<td>$-20$</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>$I_{O}$</td>
<td>$-25$</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>$T_{stg}$</td>
<td>$-65$</td>
<td>$150$</td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

6.2 ESD Ratings

<table>
<thead>
<tr>
<th>Parameter</th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{(ESD)}$</td>
<td>±1500 V</td>
<td></td>
</tr>
</tbody>
</table>

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{CC}$</td>
<td>$2$</td>
<td>$5$</td>
<td>$6$</td>
<td>V</td>
</tr>
<tr>
<td>$V_{IH}$</td>
<td>$1.5$</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{IL}$</td>
<td>$0$</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{I}$</td>
<td>$0$</td>
<td>$V_{CC}$</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$V_{O}$</td>
<td>$0$</td>
<td>$V_{CC}$</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$\Delta t/\Delta v^{(2)}$</td>
<td>$1000$</td>
<td>ns/V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$T_{A}$</td>
<td>$-55$</td>
<td>$125$</td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>

(1) All unused inputs of the device must be held at $V_{CC}$ or GND to ensure proper device operation. Refer to the TI application report, Implications of Slow or Floating CMOS Inputs, literature number SCBA004.

(2) If this device is used in the threshold region (from $V_{IL_{max}} = 0.5$ V to $V_{IH_{min}} = 1.5$ V), there is a potential to go into the wrong state from induced grounding, causing double clocking. Operating with the inputs at $t_{i} = 1000$ ns and $V_{CC} = 2$ V does not damage the device; however, functionally, the CLK inputs are not ensured while in the shift, count, or toggle operating modes.

6.4 Thermal Information

<table>
<thead>
<tr>
<th>Parameter</th>
<th>SN74HC165</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R_{JA}$</td>
<td>Junction-to-ambient thermal resistance</td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see the Semiconductor and IC Package Thermal Metrics application report, SPRA853.
### 6.5 Electrical Characteristics, $T_A = 25^\circ\text{C}$

Over recommended operating free-air temperature range for both the SN74HC165 and SN54HC165 (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>$V_{CC}$</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OH}$</td>
<td>$V_I = V_{IH}$ or $V_{IL}$</td>
<td>$I_{OH} = -20 \mu A$</td>
<td>2 V</td>
<td>1.9</td>
<td>1.998</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td>4.4</td>
<td>4.499</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td>5.9</td>
<td>5.999</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OH} = -4 \ mA$</td>
<td>4.5 V</td>
<td>3.98</td>
<td>4.3</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OH} = -5.2 \ mA$</td>
<td>6 V</td>
<td>5.48</td>
<td>5.8</td>
<td></td>
</tr>
<tr>
<td>$V_{OL}$</td>
<td>$V_I = V_{IH}$ or $V_{IL}$</td>
<td>$I_{OL} = 20 \mu A$</td>
<td>2 V</td>
<td>0.002</td>
<td>0.1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td>0.001</td>
<td>0.1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td>0.001</td>
<td>0.1</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OL} = 4 \ mA$</td>
<td>4.5 V</td>
<td>0.17</td>
<td>0.26</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OL} = 5.2 \ mA$</td>
<td>6 V</td>
<td>0.15</td>
<td>0.26</td>
<td></td>
</tr>
<tr>
<td>$I_{I}$</td>
<td>$V_I = V_{CC}$ or 0</td>
<td></td>
<td>6 V</td>
<td>±0.1</td>
<td>±100</td>
<td>nA</td>
</tr>
<tr>
<td>$I_{OC}$</td>
<td>$V_I = V_{CC}$ or 0, $I_O = 0$</td>
<td></td>
<td>6 V</td>
<td>8</td>
<td></td>
<td>µA</td>
</tr>
<tr>
<td>$C_i$</td>
<td></td>
<td>2 V to 6 V</td>
<td>3</td>
<td>10</td>
<td>pF</td>
<td></td>
</tr>
</tbody>
</table>

### 6.6 Electrical Characteristics, SN54HC165

Over recommended operating free-air temperature range, $T_A = -55^\circ\text{C}$ to 125°C (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>$V_{CC}$</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OH}$</td>
<td>$V_I = V_{IH}$ or $V_{IL}$</td>
<td>$I_{OH} = -20 \mu A$</td>
<td>2 V</td>
<td>1.9</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td>4.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td>5.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OH} = -4 \ mA$</td>
<td>4.5 V</td>
<td>3.7</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OH} = -5.2 \ mA$</td>
<td>6 V</td>
<td>5.2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{OL}$</td>
<td>$V_I = V_{IH}$ or $V_{IL}$</td>
<td>$I_{OL} = 20 \mu A$</td>
<td>2 V</td>
<td>0.1</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td>0.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td>0.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OL} = 4 \ mA$</td>
<td>4.5 V</td>
<td>0.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OL} = 5.2 \ mA$</td>
<td>6 V</td>
<td>0.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{I}$</td>
<td>$V_I = V_{CC}$ or 0</td>
<td></td>
<td>6 V</td>
<td>±1000</td>
<td></td>
<td>nA</td>
</tr>
<tr>
<td>$I_{OC}$</td>
<td>$V_I = V_{CC}$ or 0, $I_O = 0$</td>
<td></td>
<td>6 V</td>
<td>160</td>
<td></td>
<td>µA</td>
</tr>
<tr>
<td>$C_i$</td>
<td></td>
<td>2 V to 6 V</td>
<td></td>
<td>10</td>
<td></td>
<td>pF</td>
</tr>
</tbody>
</table>

### 6.7 Electrical Characteristics, SN74HC165

Over recommended operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>$V_{CC}$</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{OH}$</td>
<td>$V_I = V_{IH}$ or $V_{IL}$</td>
<td>$I_{OH} = -20 \mu A$</td>
<td>2 V</td>
<td>1.9</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td>4.4</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td>5.9</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OH} = -4 \ mA$</td>
<td>4.5 V</td>
<td>3.84</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OH} = -5.2 \ mA$</td>
<td>6 V</td>
<td>5.34</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$V_{OL}$</td>
<td>$V_I = V_{IH}$ or $V_{IL}$</td>
<td>$I_{OL} = 20 \mu A$</td>
<td>2 V</td>
<td>0.1</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td>0.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td>0.1</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OL} = 4 \ mA$</td>
<td>4.5 V</td>
<td>0.33</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>$I_{OL} = 5.2 \ mA$</td>
<td>6 V</td>
<td>0.33</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

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Product Folder Links: SN54HC165 SN74HC165
Electrical Characteristics, SN74HC165 (continued)

over recommended operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>( V_{CC} )</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( I_I )</td>
<td>( V_I = V_{CC} ) or 0, ( T_A = -40°C ) to 125°C</td>
<td>( V_{CC} )</td>
<td>6</td>
<td>±1000</td>
<td>nA</td>
<td></td>
</tr>
<tr>
<td>( I_{CC} )</td>
<td>( V_I = V_{CC} ) or 0, ( I_O = 0 ), ( T_A = -40°C ) to 85°C</td>
<td>6</td>
<td>80</td>
<td>160</td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>( C_i )</td>
<td>Recommended ( T_A = -40°C ) to 125°C</td>
<td>2 V to 6 V</td>
<td>10</td>
<td>pF</td>
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</tr>
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6.8 Switching Characteristics, \( T_A = 25°C \)

over recommended operating free-air temperature range for both the SN74HC165 and SN54HC165, \( C_L = 50 \text{ pF} \) (unless otherwise noted) (see Figure 2)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>FROM (INPUT)</th>
<th>TO (OUTPUT)</th>
<th>( V_{CC} )</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( f_{max} )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>2 V</td>
<td>6</td>
<td>13</td>
<td>MHz</td>
<td></td>
<td></td>
</tr>
<tr>
<td>( t_{pd} )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>4.5 V</td>
<td>31</td>
<td>50</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( SH/\overline{DD} )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>6 V</td>
<td>36</td>
<td>62</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( CLK )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>4.5 V</td>
<td>20</td>
<td>30</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( H )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>6 V</td>
<td>16</td>
<td>26</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( t_t )</td>
<td>Any</td>
<td>2 V</td>
<td>38</td>
<td>75</td>
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<tr>
<td>( Any )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>4.5 V</td>
<td>8</td>
<td>15</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>( Any )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>6 V</td>
<td>6</td>
<td>13</td>
<td></td>
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<td></td>
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6.9 Switching Characteristics, SN54HC165

over recommended operating free-air temperature range, \( T_A = -55°C \) to 125°C, \( C_L = 50 \text{ pF} \) (unless otherwise noted) (see Figure 2)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>FROM (INPUT)</th>
<th>TO (OUTPUT)</th>
<th>( V_{CC} )</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>( f_{max} )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>2 V</td>
<td>4.2</td>
<td>MHz</td>
<td></td>
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</tr>
<tr>
<td>( t_{pd} )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>4.5 V</td>
<td>21</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( SH/\overline{DD} )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>6 V</td>
<td>25</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>( CLK )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>2 V</td>
<td>225</td>
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</tr>
<tr>
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<td>( Q_H ) or ( Q_H )</td>
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<td>45</td>
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<td>Any</td>
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<td></td>
</tr>
<tr>
<td>( Any )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>4.5 V</td>
<td>22</td>
<td></td>
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<td></td>
</tr>
<tr>
<td>( Any )</td>
<td>( Q_H ) or ( Q_H )</td>
<td>6 V</td>
<td>19</td>
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### 6.10 Switching Characteristics, SN74HC165

over recommended operating free-air temperature range, $C_L = 50 \, \text{pF}$ (unless otherwise noted) (see Figure 2)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>FROM (INPUT)</th>
<th>TO (OUTPUT)</th>
<th>$V_{CC}$</th>
<th>TEMPERATURE</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
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</thead>
<tbody>
<tr>
<td>$f_{\text{max}}$</td>
<td>2 V</td>
<td>$T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$</td>
<td></td>
<td></td>
<td>5</td>
<td></td>
<td>MHz</td>
</tr>
<tr>
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<td></td>
<td>$T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$</td>
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<tr>
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<td>4.5 V</td>
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<tr>
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<td>6 V</td>
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<td>$T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$</td>
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<td>$t_{\text{pd}}$</td>
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<td>190</td>
<td>225</td>
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<td>4.5 V</td>
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<td>38</td>
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<td></td>
<td></td>
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<tr>
<td></td>
<td>6 V</td>
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<td>32</td>
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<td>$Q_H$ or $\overline{Q}_H$</td>
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<td>190</td>
<td>225</td>
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<td>4.5 V</td>
<td>$T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$</td>
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<td>$T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$</td>
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<td></td>
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</tr>
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<td>6 V</td>
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<td>32</td>
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<td>95</td>
<td>110</td>
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<td>4.5 V</td>
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<td></td>
<td>19</td>
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<td>$T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$</td>
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<td></td>
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<tr>
<td></td>
<td>6 V</td>
<td>$T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$</td>
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<td>16</td>
<td></td>
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### 6.11 Timing Requirements, $T_A = 25^\circ\text{C}$

over recommended operating free-air temperature range for both the SN74HC165 and SN54HC165 (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>FROM (INPUT)</th>
<th>TO (OUTPUT)</th>
<th>$V_{CC}$</th>
<th>TEMPERATURE</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
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</thead>
<tbody>
<tr>
<td>$f_{\text{clock}}$</td>
<td>Clock frequency</td>
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<td>6</td>
<td></td>
<td>MHz</td>
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<td></td>
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<td>4.5 V</td>
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<td>6 V</td>
<td></td>
<td>36</td>
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<td></td>
</tr>
<tr>
<td>$t_{\text{w}}$</td>
<td>Pulse duration</td>
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<td>80</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
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<td>4.5 V</td>
<td></td>
<td>16</td>
<td></td>
<td></td>
</tr>
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<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td></td>
<td>14</td>
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<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>CLK high or low</td>
<td>2 V</td>
<td></td>
<td>80</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>4.5 V</td>
<td></td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>6 V</td>
<td></td>
<td>14</td>
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</tbody>
</table>
### Timing Requirements, $T_A = 25°C$ (continued)

over recommended operating free-air temperature range for both the SN74HC165 and SN54HC165 (unless otherwise noted)

<table>
<thead>
<tr>
<th>$t_{su}$</th>
<th>Set-up time</th>
<th>$V_{CC}$</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>SH/LE high before CLK↑</td>
<td>2 V</td>
<td>80</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>16</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>14</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>SER before CLK↑</td>
<td>2 V</td>
<td>40</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>7</td>
<td></td>
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</tr>
<tr>
<td></td>
<td>CLK INH low before CLK↑</td>
<td>2 V</td>
<td>100</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>20</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>17</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>CLK INH high before CLK↑</td>
<td>2 V</td>
<td>40</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>8</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>7</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Data before SH/LE↓</td>
<td>2 V</td>
<td>100</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>20</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>17</td>
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<table>
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<th>Hold time</th>
<th>$V_{CC}$</th>
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<th>MAX</th>
<th>UNIT</th>
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<tbody>
<tr>
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<td>SER data after CLK↑</td>
<td>2 V</td>
<td>5</td>
<td></td>
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</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>5</td>
<td></td>
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</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>5</td>
<td></td>
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</tr>
<tr>
<td></td>
<td>PAR data after SH/LE↓</td>
<td>2 V</td>
<td>5</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>5</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>5</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### 6.12 Timing Requirements, SN54HC165

over recommended operating free-air temperature range, $T_A = –55°C$ to $125°C$ (unless otherwise noted)

<table>
<thead>
<tr>
<th>$t_{clock}$</th>
<th>Clock frequency</th>
<th>$V_{CC}$</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
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<tbody>
<tr>
<td></td>
<td>$f_{clock}$</td>
<td>2 V</td>
<td>4.2</td>
<td></td>
<td>MHz</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>21</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>25</td>
<td></td>
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</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>$t_w$</th>
<th>Pulse duration</th>
<th>$V_{CC}$</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
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</thead>
<tbody>
<tr>
<td></td>
<td>SH/L low</td>
<td>2 V</td>
<td>120</td>
<td></td>
<td>ns</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5 V</td>
<td>24</td>
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</tr>
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<td>6 V</td>
<td>20</td>
<td></td>
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</tr>
<tr>
<td></td>
<td>CLK high or low</td>
<td>2 V</td>
<td>120</td>
<td></td>
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</tr>
<tr>
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<td></td>
<td>4.5 V</td>
<td>24</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>6 V</td>
<td>20</td>
<td></td>
<td></td>
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</table>
Timing Requirements, SN54HC165 (continued)

over recommended operating free-air temperature range, $T_A = \text{–55°C to 125°C}$ (unless otherwise noted)

<table>
<thead>
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<th>$V_{CC}$</th>
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<th>MAX</th>
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</thead>
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<td>$t_{su}$ Set-up time</td>
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</tr>
<tr>
<td>SH/LD high before CLK†</td>
<td>2 V</td>
<td>120</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4.5 V</td>
<td>24</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>6 V</td>
<td>20</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SER before CLK†</td>
<td>2 V</td>
<td>60</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>4.5 V</td>
<td>12</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>6 V</td>
<td>10</td>
<td></td>
<td></td>
</tr>
<tr>
<td>CLK INH low before CLK†</td>
<td>2 V</td>
<td>150</td>
<td></td>
<td>ns</td>
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</tr>
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<td>6 V</td>
<td>25</td>
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<tr>
<td>CLK INH high before CLK†</td>
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<td></td>
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<td>6 V</td>
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</tr>
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</tr>
<tr>
<td>$t_h$ Hold time</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SER data after CLK†</td>
<td>2 V</td>
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<td></td>
<td>4.5 V</td>
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<tr>
<td>PAR data after SH/LD↓</td>
<td>2 V</td>
<td>5</td>
<td></td>
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<td></td>
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<td>6 V</td>
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</table>

6.13 Timing Requirements, SN74HC165

over recommended operating free-air temperature range (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>$V_{CC}$</th>
<th>TEMPERATURE</th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
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<tbody>
<tr>
<td>$f_{clock}$ Clock frequency</td>
<td></td>
<td>$T_A = -40^\circ\text{C to 85^\circ\text{C}}$</td>
<td>5</td>
<td></td>
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</tr>
<tr>
<td></td>
<td>2 V</td>
<td>$T_A = -40^\circ\text{C to 125^\circ\text{C}}$</td>
<td>4.2</td>
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<td>$T_A = -40^\circ\text{C to 125^\circ\text{C}}$</td>
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<tr>
<td>$t_w$ Pulse duration</td>
<td>2 V</td>
<td>$T_A = -40^\circ\text{C to 85^\circ\text{C}}$</td>
<td>100</td>
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<td></td>
<td>$T_A = -40^\circ\text{C to 125^\circ\text{C}}$</td>
<td>120</td>
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<td></td>
</tr>
<tr>
<td></td>
<td>4.5 V</td>
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<td></td>
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<tr>
<td>$f_{clock}$ Clock frequency</td>
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<td>2 V</td>
<td>$T_A = -40^\circ\text{C to 125^\circ\text{C}}$</td>
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<td></td>
<td>$T_A = -40^\circ\text{C to 125^\circ\text{C}}$</td>
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## Timing Requirements, SN74HC165 (continued)

Over recommended operating free-air temperature range (unless otherwise noted)

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<th>PARAMETER</th>
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<th>MAX</th>
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<td>4.5 V</td>
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<td>2 V</td>
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<td></td>
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<tr>
<td></td>
<td>6 V</td>
<td>$T_A = -40°C$ to $85°C$</td>
<td>21</td>
<td></td>
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<td></td>
<td>$T_A = -40°C$ to $125°C$</td>
<td>26</td>
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</tr>
<tr>
<td>$t_h$</td>
<td>2 V</td>
<td>$T_A = -40°C$ to $125°C$</td>
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<td></td>
</tr>
<tr>
<td></td>
<td>4.5 V</td>
<td>$T_A = -40°C$ to $125°C$</td>
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</tr>
<tr>
<td></td>
<td>6 V</td>
<td>$T_A = -40°C$ to $125°C$</td>
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<td></td>
<td>4.5 V</td>
<td>$T_A = -40°C$ to $125°C$</td>
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</tr>
<tr>
<td></td>
<td>6 V</td>
<td>$T_A = -40°C$ to $125°C$</td>
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</table>

### 6.14 Operating Characteristics

$T_A = 25°C$

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>TYP</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$C_{pd}$</td>
<td>Power dissipation capacitance</td>
<td>No load</td>
<td>75 pF</td>
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</tbody>
</table>
6.15 Typical Characteristics

![Propagation Delay vs Supply Voltage at TA = 25°C](image)

**Figure 1.** Propagation Delay vs Supply Voltage at TA = 25°C

7 Parameter Measurement Information

![Load Circuit and Voltage Waveforms](image)

**Figure 2.** Load Circuit and Voltage Waveforms

NOTES:
A. CL includes probe and test-fixture capacitance.
B. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics: PRR ≤ 1 MHz, ZO = 50 Ω, tr = 6 ns, tf = 6 ns.
C. For clock inputs, fmax is measured when the input duty cycle is 50%.
D. The outputs are measured one at a time with one input transition per measurement.
E. tPLH and tPHL are the same as tpd.
8 Detailed Description

8.1 Overview

The SNx4HC165 is an 8-bit Parallel load shift register with 1 serial input and 8 parallel load input. The device loads all the 8 bits simultaneously through parallel load input when SH/LD is low. This will also ignore any input at CLK or CLK INH.

The device shifts the data when CLK toggles. The data is shifted on rising edge of the clock. Clock Inhibit (CLK INH) inhibits the clock function resulting in no change of the output. If SH/LD is low clock inputs are ignored. To realize the shift function, SH/LD should be high.

CLK and CLK INH functions are interchangeable. If CLK is low then change a clock signal at CLK INH pin causes a shift of data to QH. If CLK INH is Low clock signal on CLK pin shifts the data out to QH.

8.2 Functional Block Diagram

Pin numbers shown are for the D, DB, J, N, NS, PW, and W packages.

Figure 3. Logic Diagram Positive Logic

Figure 4. Typical Shift, Load, and Inhibit Sequence
8.3 Feature Description

The SNx4HC165 has a wide operating voltage range of 2 V to 6 V, outputs that can drive up to 10 LSTTL loads and Low Power Consumption, 80-μA maximum I. It is typically tpd = 13 ns and has ±4-mA output drive at 5 V with low input current of 1-μA maximum. The device features the direct overloading load of data input, meaning parallel data is loaded irrespective of clock signals.

8.4 Device Functional Table

Table 1 lists the functional modes of the SNx4HC165.

Table 1. Function Table

<table>
<thead>
<tr>
<th>INPUTS</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>SH/LD</td>
<td>CLK</td>
</tr>
<tr>
<td>L</td>
<td>X</td>
</tr>
<tr>
<td>H</td>
<td>H</td>
</tr>
<tr>
<td>H</td>
<td>X</td>
</tr>
<tr>
<td>H</td>
<td>L</td>
</tr>
<tr>
<td>H</td>
<td>↑</td>
</tr>
</tbody>
</table>

(1) Shift: Content of each internal register shifts towards serial output QH. Data at SER is shifted into the first register.
9 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

9.1 Application Information
The SNx4HC165 is an 8-bit shift register that can be used as a serializer in order to reduce the number of connection needed when transmitting signals between boards or to the device. SNx4HC165 can be used to expand inputs for processors with limited GPIOs for examples basic keyboard interface to the controller. SNx4HC165 allows inputs to be load into the shift registers and clock is used to shift data to the processor. Multiple SNx4HC165 can be cascaded together to allow more digital inputs to be interfaced with single processor by connecting output of the cascaded shift register $Q_h$ to serial input SER of the SNx4HC165 and so on. Note this application does not allow the communication to be bi-direction in nature as data can only be read by the processor not written back.

9.2 Typical Application

![Figure 5. Typical Application Diagram for SN74HC165](image)

9.2.1 Design Requirements
Ensure that the incoming clock rising edge meets the criteria in Recommended Operating Conditions.

9.2.2 Detailed Design Procedure
Ensure that input and output voltages do not exceed ratings in Absolute Maximum Ratings.

Input voltage threshold information for each device can be found in the Electrical Characteristics tables in the Specifications section.

Detailed timing requirements for each device can be found in Timing Requirements tables in the Specifications section.
9.2.3 Application Curve

![Figure 6. Propagation Delay vs Supply Voltage at $T_A = 25^\circ C$](image)

**10 Power Supply Recommendations**

The power supply can be any voltage between the minimum and maximum supply voltage rating located in the Recommended Operating Conditions table.

Each $V_{CC}$ pin must have a good bypass capacitor in order to prevent power disturbance. For devices with a single supply, a 0.1-μF capacitor is recommended and if there are multiple $V_{CC}$ pins then a 0.01-μF or 0.022-μF capacitor is recommended for each power pin. It is ok to parallel multiple bypass caps to reject different frequencies of noise. 0.1-μF and 1-μF capacitors are commonly used in parallel. The bypass capacitor should be installed as close to the power pin as possible for best results.

**11 Layout**

11.1 Layout Guidelines

Reflections and matching are closely related to loop antenna theory, but different enough to warrant their own discussion. When a PCB trace turns a corner at a 90° angle, a reflection can occur. This is primarily due to the change of width of the trace. At the apex of the turn, the trace width is increased to 1.414 times its width. This upsets the transmission line characteristics, especially the distributed capacitance and self–inductance of the trace — resulting in the reflection. It is a given that not all PCB traces can be straight, and so they will have to turn corners. Figure 7 shows progressively better techniques of rounding corners. Only the last example maintains constant trace width and minimizes reflections.

11.2 Layout Example

![Figure 7. Trace Example](image)
12 Device and Documentation Support

12.1 Documentation Support

12.1.1 Related Documentation

Implications of Slow or Floating CMOS Inputs, SCBA004.

12.2 Related Links

The table below lists quick access links. Categories include technical documents, support and community resources, tools and software, and quick access to sample or buy.

<table>
<thead>
<tr>
<th>PARTS</th>
<th>PRODUCT FOLDER</th>
<th>SAMPLE &amp; BUY</th>
<th>TECHNICAL DOCUMENTS</th>
<th>TOOLS &amp; SOFTWARE</th>
<th>SUPPORT &amp; COMMUNITY</th>
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<td>SN54HC165</td>
<td>Click here</td>
<td>Click here</td>
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<td>Click here</td>
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<tr>
<td>SN74HC165</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
<td>Click here</td>
</tr>
</tbody>
</table>

12.3 Community Resources

The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

TI E2E™ Online Community **TI's Engineer-to-Engineer (E2E) Community.** Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** **TI's Design Support** Quickly find helpful E2E forums along with design support tools and contact information for technical support.

12.4 Trademarks

E2E is a trademark of Texas Instruments. All other trademarks are the property of their respective owners.

12.5 Electrostatic Discharge Caution

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

12.6 Glossary

SLYZ022 — **TI Glossary.**

This glossary lists and explains terms, acronyms and definitions.

13 Mechanical, Packaging, and Orderable Information

The following pages include mechanical packaging and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser based versions of this data sheet, refer to the left hand navigation.
<table>
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<th>Status</th>
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<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead finish/ Ball material</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
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<td>-55 to 125</td>
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(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
- **PREVIEW**: Device has been announced but is not in production. Samples may or may not be available.
- **OBSOLETE**: TI has discontinued the production of the device.

(2) **RoHS**: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, “RoHS” products are suitable for use in specified lead-free processes. TI may reference these types of products as “Pb-Free”.
- **RoHS Exempt**: TI defines “RoHS Exempt” to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
- **Green**: TI defines “Green” to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.**: The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

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Addendum-Page 2
Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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In no event shall TI’s liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

OTHER QUALIFIED VERSIONS OF SN54HC165, SN74HC165:
- Catalog: SN74HC165
- Automotive: SN74HC165-Q1, SN74HC165-Q1
- Military: SN54HC165

NOTE: Qualified Version Definitions:
- Catalog - TI's standard catalog product
- Automotive - Q100 devices qualified for high-reliability automotive applications targeting zero defects
- Enhanced Product - Supports Defense, Aerospace and Medical Applications
- Military - QML certified for Military and Defense Applications
TAPE AND REEL INFORMATION

### TAPE DIMENSIONS

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<th>Dimension designed to accommodate the component width</th>
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<td>K0</td>
<td>Dimension designed to accommodate the component thickness</td>
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<td>W</td>
<td>Overall width of the carrier tape</td>
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<td>Pitch between successive cavity centers</td>
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### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

- **Q1**: Upper left
- **Q2**: Upper right
- **Q3**: Lower right
- **Q4**: Lower left

- **Sprocket Holes**: For feeding the tape
- **Pocket Quadrants**: Indicate the orientation of the components

- **User Direction of Feed**: The direction in which the tape is fed

*All dimensions are nominal.*

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<td>853.0</td>
<td>449.0</td>
<td>35.0</td>
</tr>
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<td>SN74HC165PWT</td>
<td>TSSOP</td>
<td>PW</td>
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<td>250</td>
<td>853.0</td>
<td>449.0</td>
<td>35.0</td>
</tr>
</tbody>
</table>
NOTES:
A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. This package can be hermetically sealed with a metal lid.
D. Falls within JEDEC MS-004
NOTES:
A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0.15) each side.
D. Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0.43) each side.
E. Reference JEDEC MS-012 variation AC.
NOTES:  
A. All linear dimensions are in millimeters.  
B. This drawing is subject to change without notice.  
C. Publication IPC-7351 is recommended for alternate designs.  
D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.  
E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.
5. Reference JEDEC registration MO-153.
NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
NOTES: (continued)

8. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
9. Board assembly site may have different recommendations for stencil design.
MECHANICAL DATA

NS (R-PDSO-G**)
PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN

NOTES:
A. All linear dimensions are in millimeters.
B. This drawing is subject to change without notice.
C. Body dimensions do not include mold flash or protrusion, not to exceed 0.15.

<table>
<thead>
<tr>
<th>DIM</th>
<th>PINS **</th>
<th>14</th>
<th>16</th>
<th>20</th>
<th>24</th>
</tr>
</thead>
<tbody>
<tr>
<td>A MAX</td>
<td></td>
<td>10.50</td>
<td>10.50</td>
<td>12.90</td>
<td>15.30</td>
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<td>A MIN</td>
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<td>9.90</td>
<td>12.30</td>
<td>14.70</td>
</tr>
</tbody>
</table>

4040062/C 03/03
NOTES:
A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. This package can be hermetically sealed with a ceramic lid using glass frit.
D. Index point is provided on cap for terminal identification only.
E. Falls within MIL STD 1835 GDFP2–F16
**J (R-GDIP-T**)**

14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE

<table>
<thead>
<tr>
<th>PIN</th>
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<th>18</th>
<th>20</th>
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</thead>
<tbody>
<tr>
<td>A</td>
<td>0.300 (7,62)</td>
<td>0.300 (7,62)</td>
<td>0.300 (7,62)</td>
<td>0.300 (7,62)</td>
</tr>
<tr>
<td>A</td>
<td>0.065 (1,65)</td>
<td>0.065 (1,65)</td>
<td>0.065 (1,65)</td>
<td>0.065 (1,65)</td>
</tr>
<tr>
<td>A</td>
<td>0.045 (1,14)</td>
<td>0.045 (1,14)</td>
<td>0.045 (1,14)</td>
<td>0.045 (1,14)</td>
</tr>
<tr>
<td>B</td>
<td>0.785 (19,94)</td>
<td>0.840 (21,34)</td>
<td>0.960 (24,38)</td>
<td>1.060 (26,92)</td>
</tr>
<tr>
<td>B</td>
<td>——</td>
<td>——</td>
<td>——</td>
<td>——</td>
</tr>
<tr>
<td>C</td>
<td>0.300 (7,62)</td>
<td>0.300 (7,62)</td>
<td>0.310 (7,87)</td>
<td>0.300 (7,62)</td>
</tr>
<tr>
<td>C</td>
<td>0.245 (6,22)</td>
<td>0.245 (6,22)</td>
<td>0.220 (5,59)</td>
<td>0.245 (6,22)</td>
</tr>
<tr>
<td>C</td>
<td>0.005 (0,13) MIN</td>
<td>0.005 (0,13) MIN</td>
<td>0.005 (0,13) MIN</td>
<td>0.005 (0,13) MIN</td>
</tr>
<tr>
<td>C</td>
<td>0.060 (1,52) MAX</td>
<td>0.060 (1,52) MAX</td>
<td>0.060 (1,52) MAX</td>
<td>0.060 (1,52) MAX</td>
</tr>
<tr>
<td>C</td>
<td>0.015 (0,38)</td>
<td>0.015 (0,38)</td>
<td>0.015 (0,38)</td>
<td>0.015 (0,38)</td>
</tr>
<tr>
<td>C</td>
<td>0.200 (5,08) MAX</td>
<td>0.200 (5,08) MAX</td>
<td>0.200 (5,08) MAX</td>
<td>0.200 (5,08) MAX</td>
</tr>
<tr>
<td>C</td>
<td>0.130 (3,30) MIN</td>
<td>0.130 (3,30) MIN</td>
<td>0.130 (3,30) MIN</td>
<td>0.130 (3,30) MIN</td>
</tr>
<tr>
<td>C</td>
<td>0.014 (0,36)</td>
<td>0.014 (0,36)</td>
<td>0.014 (0,36)</td>
<td>0.014 (0,36)</td>
</tr>
<tr>
<td>C</td>
<td>0.008 (0,20)</td>
<td>0.008 (0,20)</td>
<td>0.008 (0,20)</td>
<td>0.008 (0,20)</td>
</tr>
</tbody>
</table>

**NOTES:**

A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. This package is hermetically sealed with a ceramic lid using glass frit.
D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.
DB (R-PDSO-G**)  
PLASTIC SMALL-OUTLINE  

28 PINS SHOWN

<table>
<thead>
<tr>
<th>PINS **</th>
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<th>20</th>
<th>24</th>
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<th>30</th>
<th>38</th>
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</thead>
<tbody>
<tr>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>A MAX</td>
<td>6.50</td>
<td>6.50</td>
<td>7.50</td>
<td>8.50</td>
<td>10.50</td>
<td>10.50</td>
<td>12.90</td>
</tr>
<tr>
<td>A MIN</td>
<td>5.90</td>
<td>5.90</td>
<td>6.90</td>
<td>7.90</td>
<td>9.90</td>
<td>9.90</td>
<td>12.30</td>
</tr>
</tbody>
</table>

NOTES:  
A. All linear dimensions are in millimeters.  
B. This drawing is subject to change without notice.  
C. Body dimensions do not include mold flash or protrusion not to exceed 0.15.  
D. Falls within JEDEC MO-150
N (R—PDIP—T**)  

PLASTIC DUAL—IN—LINE PACKAGE

16 PINS SHOWN

<table>
<thead>
<tr>
<th>PINS **</th>
<th>14</th>
<th>16</th>
<th>18</th>
<th>20</th>
</tr>
</thead>
<tbody>
<tr>
<td>A MAX</td>
<td>0.775 (19.69)</td>
<td>0.775 (19.69)</td>
<td>0.920 (23.37)</td>
<td>1.060 (26.92)</td>
</tr>
<tr>
<td>A MIN</td>
<td>0.745 (18.92)</td>
<td>0.745 (18.92)</td>
<td>0.850 (21.59)</td>
<td>0.940 (23.88)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>MS—001 VARIATION</th>
<th>AA</th>
<th>BB</th>
<th>AC</th>
<th>AD</th>
</tr>
</thead>
</table>

NOTES:  
A. All linear dimensions are in inches (millimeters).  
B. This drawing is subject to change without notice.  
C. Falls within JEDEC MS—001, except 18 and 20 pin minimum body length (Dim A).  
D. The 20 pin end lead shoulder width is a vendor option, either half or full width.
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