TPS60150 5-V, 140-mA Charge-Pump

1 Features
- 2.7-V to 5.5-V Input Voltage Range
- Fixed Output Voltage of 5 V
- Maximum Output Current: 140 mA
- 1.5-MHz Switching Frequency
- Typical 90-μA Quiescent Current at No Load Condition (Skip Mode)
- X2 Charge Pump
- Hardware Enable and Disable Function
- Built-in Soft Start
- Built-in Undervoltage Lockout Protection
- Thermal and Overcurrent Protection
- Available in a 2-mm × 2-mm 6-Pin SON Package with 0.8-mm Height

2 Applications
- USB On-the-Go (OTG)
- HDMI
- Portable Communication Devices
- PCMCIA Cards
- Mobile Phones, Smart Phones
- Handheld Meters

3 Description
The TPS60150 device is a switched capacitor voltage converter that produces a regulated, low noise, and low-ripple output voltage of 5 V from an unregulated input voltage.

The 5-V output can supply a minimum of 140-mA current.

The TPS60150 device operates in skip mode when the load current falls less than 8 mA under typical condition. In skip mode operation, quiescent current is reduced to 90 μA.

Only 3 external capacitors are needed to generate the output voltage, therefore saving PCB space.

Inrush current is limited by the soft-start function during power on and power transient states.

The TPS60150 device operates over a free-air temperature range of –40°C to 85°C. The device is available with a small 2-mm × 2-mm 6-pin SON package (QFN).

Device Information

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>PACKAGE</th>
<th>BODY SIZE (NOM)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS60150</td>
<td>WSON (6)</td>
<td>2.00 mm × 2.00 mm</td>
</tr>
</tbody>
</table>

(1) For all available packages, see the orderable addendum at the end of the data sheet.

Typical Application Schematic

Efficiency vs Input Voltage

An IMPORTANT NOTICE at the end of this data sheet addresses availability, warranty, changes, use in safety-critical applications, intellectual property matters and other important disclaimers. PRODUCTION DATA.
Table of Contents

1 Features .................................................................. 1
2 Applications ........................................................... 1
3 Description ............................................................. 1
4 Revision History ....................................................... 2
5 Pin Configuration and Functions ................................. 3
6 Specifications .......................................................... 4
   6.1 Absolute Maximum Ratings .................................. 4
   6.2 ESD Ratings ........................................................ 4
   6.3 Recommended Operating Conditions...................... 4
   6.4 Thermal Information ............................................ 4
   6.5 Electrical Characteristics ..................................... 5
   6.6 Typical Characteristics ......................................... 6
7 Detailed Description ................................................... 7
   7.1 Overview ........................................................... 7
   7.2 Functional Block Diagram .................................... 8
7.3 Feature Description ................................................ 8
7.4 Device Functional Modes ......................................... 9
8 Application and Implementation ................................... 11
   8.1 Application Information ...................................... 11
   8.2 Typical Application ............................................. 11
9 Power Supply Recommendations ................................. 16
10 Layout ................................................................ 16
   10.1 Layout Guidelines ............................................. 16
   10.2 Layout Example ............................................... 16
11 Device and Documentation Support ............................. 17
   11.1 Community Resources ..................................... 17
   11.2 Trademarks ...................................................... 17
   11.3 Electrostatic Discharge Caution ......................... 17
   11.4 Glossary ......................................................... 17
12 Mechanical, Packaging, and Orderable Information ......... 17

4 Revision History
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision B (February 2011) to Revision C Page

• Added Pin Configuration and Functions section, ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section .......................... 1

Changes from Revision A (April 2009) to Revision B Page

• Added the Thermal Table and deleted the Dissipation Rating Table .......................................................................................... 4

Submit Documentation Feedback
Copyright © 2008–2015, Texas Instruments Incorporated

Product Folder Links: TPS60150
5 Pin Configuration and Functions

<table>
<thead>
<tr>
<th>PIN</th>
<th>I/O</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>NAME</td>
<td>NO.</td>
<td></td>
</tr>
<tr>
<td>CP+</td>
<td>4</td>
<td>— Connect to the flying capacitor</td>
</tr>
<tr>
<td>CP–</td>
<td>5</td>
<td>— Connect to the flying capacitor</td>
</tr>
<tr>
<td>ENA</td>
<td>6</td>
<td>IN Hardware enable/disable pin (High = Enable)</td>
</tr>
<tr>
<td>GND</td>
<td>1</td>
<td>— Ground</td>
</tr>
<tr>
<td>VIN</td>
<td>2</td>
<td>IN Supply voltage input</td>
</tr>
<tr>
<td>VOUT</td>
<td>3</td>
<td>OUT Output, connect to the output capacitor</td>
</tr>
</tbody>
</table>

Copyright © 2008–2015, Texas Instruments Incorporated
6 Specifications

6.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) (1)

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>V\text{IN}</td>
<td>–0.3</td>
<td>7</td>
<td>V</td>
</tr>
<tr>
<td>T\text{A}</td>
<td>–40</td>
<td>85</td>
<td>°C</td>
</tr>
<tr>
<td>T\text{J}</td>
<td>–55</td>
<td>150</td>
<td>°C</td>
</tr>
<tr>
<td>T\text{stg}</td>
<td>–55</td>
<td>150</td>
<td>°C</td>
</tr>
</tbody>
</table>

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

6.2 ESD Ratings

<table>
<thead>
<tr>
<th></th>
<th>VALUE</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>V\text{(ESD)}</td>
<td>±2000</td>
<td>V</td>
</tr>
</tbody>
</table>

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.
(2) The human body model (HBM) is a 100-pF capacitor discharged through a 1.5-kΩ resistor into each pin. The testing is done according JEDECs EIA/JESD22-A114.
(3) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6.3 Recommended Operating Conditions

<table>
<thead>
<tr>
<th></th>
<th>MIN</th>
<th>NOM</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>V\text{IN}</td>
<td>2.7</td>
<td>5.5</td>
<td>5.5</td>
<td>V</td>
</tr>
<tr>
<td>T\text{A}</td>
<td>–40</td>
<td>85</td>
<td>85</td>
<td>°C</td>
</tr>
<tr>
<td>T\text{J}</td>
<td>–40</td>
<td>125</td>
<td>125</td>
<td>°C</td>
</tr>
<tr>
<td>C\text{in}</td>
<td>2.2</td>
<td></td>
<td>2.2</td>
<td>µF</td>
</tr>
<tr>
<td>C\text{o}</td>
<td>2.2</td>
<td></td>
<td>2.2</td>
<td>µF</td>
</tr>
<tr>
<td>C\text{f}</td>
<td>1</td>
<td></td>
<td>1</td>
<td>µF</td>
</tr>
</tbody>
</table>

6.4 Thermal Information

<table>
<thead>
<tr>
<th>THERMAL METRIC</th>
<th>TPS60150</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>R\text{JA}</td>
<td>69.1</td>
<td>°C/W</td>
</tr>
<tr>
<td>R\text{JC(top)}</td>
<td>79.8</td>
<td>°C/W</td>
</tr>
<tr>
<td>R\text{JB}</td>
<td>38.6</td>
<td>°C/W</td>
</tr>
<tr>
<td>ψ\text{JT}</td>
<td>1.2</td>
<td>°C/W</td>
</tr>
<tr>
<td>ψ\text{JB}</td>
<td>38.4</td>
<td>°C/W</td>
</tr>
<tr>
<td>R\text{JC(bot)}</td>
<td>9.2</td>
<td>°C/W</td>
</tr>
</tbody>
</table>

(1) For more information about traditional and new thermal metrics, see the IC Package Thermal Metrics application report, SPRA953.
6.5 Electrical Characteristics

$V_{IN} = 3.6 \text{ V}, \ T_A = -40^\circ \text{C} \text{ to } 85^\circ \text{C}$, typical values are at $T_A = 25^\circ \text{C}$, $C_1 = C_3 = 2.2 \ \mu \text{F}$, $C_2 = 1 \ \mu \text{F}$ (unless otherwise noted)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>TEST CONDITIONS</th>
<th>MIN</th>
<th>TYP</th>
<th>MAX</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{IN}$</td>
<td>Input voltage range</td>
<td>2.7</td>
<td>5.5</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$V_{UVLO}$</td>
<td>Undervoltage lockout threshold</td>
<td>1.9</td>
<td>2.1</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$I_Q$</td>
<td>Operating quiescent current</td>
<td>$I_{OUT} = 140 \ \text{mA}$, Enable = $V_{IN}$</td>
<td>4.7</td>
<td>mA</td>
<td></td>
</tr>
<tr>
<td>$I_{Qskip}$</td>
<td>Skip mode operating quiescent current</td>
<td>$I_{OUT} = 0 \ \text{mA}$, Enable = $V_{IN}$ (no switching)</td>
<td>80</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td></td>
<td>$I_{OUT} = 0 \ \text{mA}$, Enable = $V_{IN}$ (minimum switching)</td>
<td>90</td>
<td>μA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$I_{SD}$</td>
<td>Shut down current</td>
<td>$2.7 \ \text{V} \leq V_{IN} \leq 5.5 \ \text{V}$, Enable = 0 V</td>
<td>1</td>
<td>μA</td>
<td></td>
</tr>
<tr>
<td>$V_{OUT}$</td>
<td>Output voltage$^{(1)}$</td>
<td>$I_{OUT} \leq 50 \ \text{mA}$, $2.7 \ \text{V} \leq V_{IN} \leq 5.5 \ \text{V}$</td>
<td>4.8</td>
<td>5</td>
<td>5.2</td>
</tr>
<tr>
<td>$V_{OUT(skip)}$</td>
<td>Skip mode output voltage</td>
<td>$I_{OUT} = 0 \ \text{mA}$, $2.7 \ \text{V} \leq V_{IN} \leq 5.5 \ \text{V}$</td>
<td>$V_{OUT} + 0.1$</td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>$F_{SW}$</td>
<td>Switching frequency</td>
<td>1.5</td>
<td>MHz</td>
<td></td>
<td></td>
</tr>
<tr>
<td>$SS_{TIME}$</td>
<td>Soft-start time</td>
<td>From the rising edge of enable to 90% output</td>
<td>150</td>
<td>μs</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>OUTPUT CURRENT</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{OUT,nom}$</td>
</tr>
<tr>
<td></td>
</tr>
<tr>
<td>$I_{OUT_short}$</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>RIPPLE VOLTAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>$VR$</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>ENABLE CONTROL</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{HI}$</td>
</tr>
<tr>
<td>$V_{LI}$</td>
</tr>
<tr>
<td>$I_{HI}$</td>
</tr>
<tr>
<td>$I_{LI}$</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>THERMAL SHUTDOWN</th>
</tr>
</thead>
<tbody>
<tr>
<td>$T_{SD}$</td>
</tr>
<tr>
<td>$T_{RC}$</td>
</tr>
</tbody>
</table>

$^{(1)}$ When in skip mode, output voltage can exceed $V_{OUT}$ spec because $V_{OUT(skip)} = V_{OUT} + 0.1$.

$^{(2)}$ The TPS60150 device has internal protection circuit to protect IC when $V_{OUT}$ shorted to GND.
6.6 Typical Characteristics

Figure 1. Quiescent Current vs Input Voltage

Figure 2. Maximum Output Current vs Input Voltage at Temperature
7 Detailed Description

7.1 Overview

The TPS60150 regulated charge pump provides a regulated output voltage for various input voltages. The TPS60150 device regulates the voltage across the flying capacitor to 2.5 V and controls the voltage drop of Q1 and Q2 while a conversion clock with 50% duty cycle drives the FETs.

![Figure 3. Charging Mode](image)

During the first half cycle, Q2 and Q3 transistors are turned on and flying capacitor, $C_F$, will be charged to 2.5 V ideally.

![Figure 4. Discharging Mode](image)

During the second half cycle, Q1 and Q4 transistors are turned on. Capacitor $C_F$ will then be discharged to output.

Use Equation 1 to calculate the output voltage.

$$V_{OUT} = V_{IN} - V_{Q1} + V(C_F) - VQ4 = V_{IN} - VQ1 + 2.5 \text{ V} - VQ4 = 5 \text{ V}$$

(Ideal)

The output voltage is regulated by output feedback and an internally compensated voltage control loop.
7.3 Feature Description

7.3.1 Enable
An enable pin on the regulator is used to place the device into an energy-saving shutdown mode. In this mode, the output is disconnected from the input, and the input quiescent current is reduced to 10 μA maximum.

7.3.2 Undervoltage Lockout
When the input voltage drops, the undervoltage lockout prevents misoperation by switching off the device. The converter starts operation again when the input voltage exceeds the threshold, provided the enable pin is high.

7.3.3 Thermal Shutdown Protection
The regulator has thermal shutdown circuitry that protects it from damage caused by overload conditions. The thermal protection circuitry disables the output when the junction temperature reached approximately 160°C, allowing the device to cool. When the junction temperature cools to approximately 140°C, the output circuitry is automatically reenabled. Continuously running the regulator into thermal shutdown can degrade reliability. The regulator also provides current limit to protect itself and the load.
7.4 Device Functional Modes

7.4.1 Soft Start
An internal soft start limits the inrush current when the device is being enabled.

7.4.2 Normal Mode and Skip Mode Operation
The TPS60150 device has skip mode operation as shown in Figure 5. The TPS60150 device enters skip mode if the output voltage reaches 5 V +0.1 V and the load current is less than 8 mA (typical). In skip mode, the TPS60150 device disables the oscillator and decreases the prebias current of the output stage to reduce the power consumption. Once the output voltage dips less than the threshold voltage of 5 V +0.1 V, the TPS60150 device begins switching to increase output voltage until the output reaches 5 V +0.1 V. When the output voltage dips less than 5 V, the TPS60150 device returns to normal pulse width modulation (PWM) mode; thereby reenabling the oscillator and increasing the prebias current of the output stage to supply output current.

The skip threshold voltage and current depend on input voltage and output current conditions.

Figure 5. Normal Mode and Skip Mode Operation

7.4.3 Short Circuit Protection
The TPS60150 device has internal short circuit protection to protect the IC when the output is shorted to ground. To avoid damage when output is shorted to ground, the short circuit protection circuitry senses output voltage and clamps the maximum output current to 80 mA (typical).
Device Functional Modes (continued)

Figure 6. Maximum Output Current Capability and Short Circuit Protection
8 Application and Implementation

NOTE
Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI’s customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

8.1 Application Information
Most of today’s battery-powered portable electronics allow and/or require data transfer with a PC. One of the fastest data transfer protocols is through USB On-the-Go (OTG). As Figure 7 shows, the USB OTG circuitry in the portable device requires a 5-V power rail and up to 140 mA of current. The TPS60150 device may be used to provide a 5-V power rail in a battery powered system.

8.2 Typical Application

8.2.1 USB On the Go Circuitry

8.2.1.1 Design Requirements
The design guideline provides a component selection to operate the device within the Recommended Operating Conditions.

8.2.1.2 Detailed Design Procedure

8.2.1.2.1 Capacitor Selection
For minimum output voltage ripple, the output capacitor ($C_{OUT}$) should be a surface-mount ceramic capacitor. Tantalum capacitors generally have a higher effective series resistance (ESR) and may contribute to higher output voltage ripple. Leaded capacitors also increase ripple due to the higher inductance of the package itself. To achieve the best operation with low input voltage and high load current, the input and flying capacitors ($C_{IN}$ and $C_F$, respectively) should also be surface-mount ceramic types.
Typical Application (continued)

Generally, $C_{FLY}$ can be calculated using Equation 2.

\[ Q_{\text{charging}} = c \times v = C_{FLY} \times \Delta V_{CFLY}, \]

\[ Q_{\text{discharging}} = i_{\text{discharge}} \times t = 2 \times I_{\text{LOAD(MAX)}} \times \left( \frac{T}{2} \right), \text{ half duty.} \]

Both equation should be same,

\[ \therefore 2 \times I_{\text{LOAD(MAX)}} \times \left( \frac{T}{2} \right) = C_{FLY} \times \Delta V_{CFLY} \]

If $I_{\text{LOAD}} = 140 \text{ mA}$, $f = 1.5 \text{ MHZ}$, and $\Delta V_{CFLY} = 100 \text{ mV}$, the minimum value of the flying capacitor should be $1 \mu F$.

Output capacitance, $C_{OUT}$, is also strongly related to output ripple voltage and loop stability,

\[ V_{OUT(ripples)} = \frac{I_{\text{LOAD(MAX)}}}{(2 \times f \times C_{OUT})} + 2I_{\text{LOAD(MAX)}} \times ESR_{COUT} \]

The minimum output capacitance for all output levels is $2.2 \mu F$ due to control stability. Larger ceramic capacitors or low ESR capacitors can be used to lower the output ripple voltage.

Table 1. Suggested Capacitors (Input, Output, and Flying Capacitor)

<table>
<thead>
<tr>
<th>VALUE</th>
<th>DIELECTRIC MATERIAL</th>
<th>PACKAGE SIZE</th>
<th>RATED VOLTAGE</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.7 $\mu F$</td>
<td>X5R or X7R</td>
<td>0603</td>
<td>10 V</td>
</tr>
<tr>
<td>2.2 $\mu F$</td>
<td>X5R or X7R</td>
<td>0603</td>
<td>10 V</td>
</tr>
</tbody>
</table>

The efficiency of the charge pump regulator varies with the output voltage, the applied input voltage and the load current.

Use Equation 5 and Equation 6 to calculate the approximate efficiency in normal operating mode is given by:

\[ \text{Efficiency(%) = } \frac{PD_{\text{out}}}{PD_{\text{in}}} \times 100 = \frac{V_{OUT} \times I_{OUT}}{V_{IN} \times I_{IN}} \times 100 \]  
\[ \therefore I_{IN} = 2 \times I_{OUT} + I_{Q} \]  

(5)

\[ \text{Efficiency(%) = } \frac{V_{OUT}}{2 \times V_{IN}} \times 100 \quad (I_{IN} = 2 \times I_{OUT}) \quad \text{Quiescent current was neglected.} \]  

(6)
8.2.1.3 Application Curves

Figure 9. Output Voltage vs Output Current

Figure 10. Output Voltage vs Input Voltage

Figure 11. Efficiency vs Input Voltage

Figure 12. Load Transient Response

Figure 13. Load Transient Response

Figure 14. Output Ripple Voltage (Skip Mode)
Figure 15. Output Ripple Voltage (Skip Mode)
$V_{IN} = 3.6\, \text{V}, I_O = 0\, \text{mA}$

Figure 16. Output Ripple Voltage (Normal Mode)
$V_{IN} = 2.7\, \text{V}, I_O = 50\, \text{mA}$

Figure 17. Output Ripple (Normal Mode)
$V_{IN} = 3.6\, \text{V}, I_O = 100\, \text{mA}$

Figure 18. Power On
$V_{IN} = 2.7\, \text{V}, I_O = 50\, \text{mA}$

Figure 19. Power On
$V_{IN} = 3.6\, \text{V}, I_O = 100\, \text{mA}$

Figure 20. Enable / Disable
$V_{IN} = 2.7\, \text{V}, I_O = 50\, \text{mA}$
8.2.2 System Example

Low-cost portable electronics with small LCD displays require a low-cost solution for providing the WLED backlight. As shown in Figure 23, the TPS60150 device can also be used to drive several WLEDs in parallel, with the help of ballast resistors.

---

**Figure 22. Thermal Shutdown Operation**

\[ V_{IN} = 5.5 \text{ V}, \; R_{LOAD} = 20 \Omega \]

**Figure 21. Enable / Disable**

\[ V_{IN} = 3.6 \text{ V}, \; I_O = 100 \text{ mA} \]

**Figure 23. Application Circuit for Driving White LEDs**
9 Power Supply Recommendations

The TPS60150 device has no special requirements for its input power supply. The input power supply’s output current must be rated according to the supply voltage, output voltage and output current of the TPS60150 device.

10 Layout

10.1 Layout Guidelines
Large transient currents flow in the VIN, VOUT, and GND traces. To minimize both input and output ripple, keep the capacitors as close as possible to the regulator using short, direct circuit traces.

10.2 Layout Example

Figure 24. Recommended PCB Layout
11 Device and Documentation Support

11.1 Community Resources
The following links connect to TI community resources. Linked contents are provided "AS IS" by the respective contributors. They do not constitute TI specifications and do not necessarily reflect TI's views; see TI's Terms of Use.

**TI E2E™ Online Community** *TI's Engineer-to-Engineer (E2E) Community.* Created to foster collaboration among engineers. At e2e.ti.com, you can ask questions, share knowledge, explore ideas and help solve problems with fellow engineers.

**Design Support** *TI's Design Support* Quickly find helpful E2E forums along with design support tools and contact information for technical support.

11.2 Trademarks
E2E is a trademark of Texas Instruments. All other trademarks are the property of their respective owners.

11.3 Electrostatic Discharge Caution
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

11.4 Glossary
**SLYZ022 — TI Glossary.**
This glossary lists and explains terms, acronyms, and definitions.

12 Mechanical, Packaging, and Orderable Information
The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.
### PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan</th>
<th>Lead finish/ Ball material</th>
<th>MSL Peak Temp</th>
<th>Op Temp (°C)</th>
<th>Device Marking</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS60150DRVR</td>
<td>ACTIVE</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>3000</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>NIPDAU</td>
<td>NIPDAUAG</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>CGO</td>
</tr>
<tr>
<td>TPS60150DRVT</td>
<td>ACTIVE</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>250</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>NIPDAU</td>
<td>NIPDAUAG</td>
<td>Level-1-260C-UNLIM</td>
<td>-40 to 85</td>
<td>CGO</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines “RoHS” to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, “RoHS” products are suitable for use in specified lead-free processes. TI may reference these types of products as “Pb-Free”.

**RoHS Exempt:** TI defines “RoHS Exempt” to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a “~” will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.
**TAPE AND REEL INFORMATION**

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Reel Diameter (mm)</th>
<th>Reel Width W1 (mm)</th>
<th>A0 (mm)</th>
<th>B0 (mm)</th>
<th>K0 (mm)</th>
<th>P1 (mm)</th>
<th>W (mm)</th>
<th>Pin1 Quadrant</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS60150DRVR</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>3000</td>
<td>178.0</td>
<td>8.4</td>
<td>2.25</td>
<td>2.25</td>
<td>1.0</td>
<td>4.0</td>
<td>8.0</td>
<td>Q2</td>
</tr>
<tr>
<td>TPS60150DRVR</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>3000</td>
<td>180.0</td>
<td>8.4</td>
<td>2.3</td>
<td>2.3</td>
<td>1.15</td>
<td>4.0</td>
<td>8.0</td>
<td>Q2</td>
</tr>
<tr>
<td>TPS60150DRVT</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>250</td>
<td>178.0</td>
<td>8.4</td>
<td>2.25</td>
<td>2.25</td>
<td>1.0</td>
<td>4.0</td>
<td>8.0</td>
<td>Q2</td>
</tr>
<tr>
<td>TPS60150DRVT</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>250</td>
<td>180.0</td>
<td>8.4</td>
<td>2.3</td>
<td>2.3</td>
<td>1.15</td>
<td>4.0</td>
<td>8.0</td>
<td>Q2</td>
</tr>
</tbody>
</table>

*All dimensions are nominal.*
### TAPE AND REEL BOX DIMENSIONS

*All dimensions are nominal*

<table>
<thead>
<tr>
<th>Device</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>SPQ</th>
<th>Length (mm)</th>
<th>Width (mm)</th>
<th>Height (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>TPS60150DRVR</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>3000</td>
<td>205.0</td>
<td>200.0</td>
<td>33.0</td>
</tr>
<tr>
<td>TPS60150DRVR</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>3000</td>
<td>182.0</td>
<td>182.0</td>
<td>20.0</td>
</tr>
<tr>
<td>TPS60150DRVT</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>250</td>
<td>205.0</td>
<td>200.0</td>
<td>33.0</td>
</tr>
<tr>
<td>TPS60150DRVT</td>
<td>WSON</td>
<td>DRV</td>
<td>6</td>
<td>250</td>
<td>182.0</td>
<td>182.0</td>
<td>20.0</td>
</tr>
</tbody>
</table>
Images above are just a representation of the package family, actual package may vary.
Refer to the product data sheet for package details.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

5. Vias are optional depending on application, refer to device data sheet. If some or all are implemented, recommended via locations are shown.
6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
NOTES:

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.
NOTES: (continued)

4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).

5. Vias are optional depending on application, refer to device data sheet. If some or all are implemented, recommended via locations are shown.
NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale (www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2020, Texas Instruments Incorporated