
TUSB1310 Errata

Problem:	<i>PLL lock circuit sometimes produces a false unlock.</i>
Work Around:	Use a 40-MHz crystal or 40-MHz oscillator to correct problem.
Severity:	High
Problem:	<i>When operating USB3 loopback BERT, the BCNT returns FFFF.</i>
Work Around:	None.
Severity:	Low
Problem:	<i>USB2 HS eye violates mask.</i>
Work Around:	None.
Severity:	Medium
Problem:	<i>RX CMD Byte does not return the correct VBUS State. This only affects legacy USB2 link controllers that do not use Power Present.</i>
Work Around:	Must use the Power Present input to detect the presence of VBUS (use a SS link controller). If a legacy controller is used, the user must implement the logic required to use Power Present.
Severity:	Low
Problem:	<i>RX_ELECIDLE low (it should be high) when not receiving a valid LFPS.</i>
Work Around:	None.
Severity:	Low
Problem:	<i>Corrupted SKIPS returned to USB3 Loopback Master.</i>
Work Around:	Implement external loopback in the FPGA.
Severity:	Low
Problem:	<i>No output on pipe interface under some conditions.</i>
Work Around:	Use external pull up on OUTPUT ENABLE, internal pull up resistor may not work over PVT (process, voltage, temperature).
Severity:	Low

Problem: *RX.DETECT fails in P3.*

Work Around: Use P2.

Severity: Low

Problem: *On the ULPI bus, line state is reported incorrectly at end of driving chirp K.*

Work Around: None.

Severity: Low

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