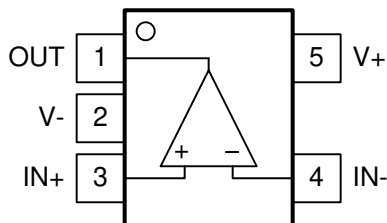


4 Pin Configuration and Functions

Pin Configuration: TLV1831-Q1 and TLV1841-Q1

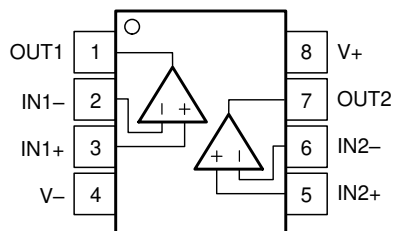


DBV, DCK Packages
SOT-23-5, SC-70-5
Top View
(Standard "north west" pinout)

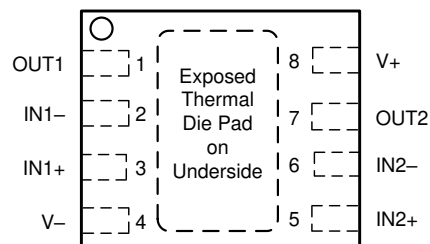
Table 4-1. Pin Functions: TLV1831-Q1 and TLV1841-Q1

PIN		I/O	DESCRIPTION
NAME	NO.		
OUT	1	O	Output
V-	2	-	Negative supply voltage
IN+	3	I	Non-inverting (+) input
IN-	4	I	Inverting (-) input
V+	5	-	Positive supply voltage

Pin Configurations: TLV1832-Q1 and TLV1842-Q1



DGG, PW Packages
8-Pin VSSOP, TSSOP
Top View



NOTE: Connect exposed thermal pad directly to V- pin.

DSG Package
8-Pad WSON With Exposed Thermal Pad,
Top View

Table 4-2. Pin Functions: TLV1832-Q1 and TLV1842-Q1

PIN		I/O	DESCRIPTION
NAME	NO.		
OUT1	1	O	Output pin of the comparator 1
IN1–	2	I	Inverting input pin of comparator 1
IN1+	3	I	Noninverting input pin of comparator 1
V–	4	—	Negative supply voltage
IN2+	5	I	Noninverting input pin of comparator 2
IN2–	6	I	Inverting input pin of comparator 2
OUT2	7	O	Output pin of the comparator 2
V+	8	—	Positive supply voltage

