







AMC60704 SLASF65 - DECEMBER 2022

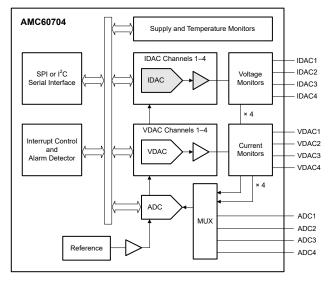
AMC60704 4-Channel Optical Monitor and Controller With Current and Voltage Output DACs and Multichannel ADC

1 Features

- Four 12-bit current output DACs (IDACs)
 - 200-mA full-scale output range
 - Low supply headroom: 300 mV at 200 mA
- Four 12-bit voltage output DACs (VDACs)
 - Selectable full-scale output ranges: -5 V, -2.5 V, +2.5 V and +5 V
 - High-current drive capability: ±50 mA
- Multichannel, 12-bit, 1-MSPS SAR ADC
 - Four external inputs: 2.5-V and 5-V ranges
 - Four IDAC voltage monitor channels
 - Four VDAC current monitor channels
 - Programmable sequencer
 - Programmable out-of-range alarms
- Internal 2.5-V reference
- Supply and temperature fault alarms
- SPI and I²C interfaces: 1.7-V to 3.6-V operation
 - SPI: 4-wire interface
 - I²C: Four target addresses
- Specified temperature range: -40°C to +125°C

2 Applications

- Optical module
- Intra-DC interconnect (metro)



Simplified Schematic

3 Description

The AMC60704 is a highly integrated, low-power analog monitor and controller optimized for electroabsorption modulated laser (EML) applications.

The AMC60704 includes four 12-bit current output digital-to-analog converters (IDACs) and four 12-bit voltage output DACs (VDACs) with programmable output ranges. The device also includes a 12-bit, 1-MSPS analog-to-digital converter (ADC) used for external and internal signal monitoring, supply and temperature alarm monitors, and a high-precision internal reference.

The AMC60704 VDACs support both positive and negative output-range operation and are capable or sourcing and sinking up to 50 mA, making them an excellent choice for biasing optical modulators. Additionally, the AMC60704 IDACs support a fullscale output range of 200 mA with very-low power dissipation. The IDACs eliminate the need for external components to bias laser diodes. In combination, the AMC60704 four VDACs and four IDACs enable accurate biasing of up to four EMLs.

The AMC60704 also includes four input pins that are multiplexed to the ADC and incorporate a lowlatency window comparator. These features make this device an excellent choice for received signal strength indicator (RSSI) and loss-of-signal (LOS) detection. The ADC is also capable of measuring the voltage at the IDAC pins, as well as the current being sourced or sinked by the VDACs, thus enabling these outputs to be monitored.

The AMC60704 low power, high integration, very small size, and wide operating temperature range make this device an excellent choice as an all-in-one control circuit for optical modules.

Package Information

PART NUMBER	PACKAGE ⁽¹⁾	BODY SIZE (NOM)			
AMC60704	YBH (DSBGA, 36)	2.56 mm × 2.56 mm			

For all available packages, see the package option addendum at the end of the data sheet.



4 Device and Documentation Support

4.1 Documentation Support

Note

TI is transitioning to use more inclusive terminology. Some language may be different than what you would expect to see for certain technology areas.

4.2 Receiving Notification of Documentation Updates

To receive notification of documentation updates, navigate to the device product folder on ti.com. Click on *Subscribe to updates* to register and receive a weekly digest of any product information that has changed. For change details, review the revision history included in any revised document.

4.3 Support Resources

TI E2E[™] support forums are an engineer's go-to source for fast, verified answers and design help — straight from the experts. Search existing answers or ask your own question to get the quick design help you need.

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4.4 Trademarks

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4.5 Electrostatic Discharge Caution



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

4.6 Glossary

TI Glossary

This glossary lists and explains terms, acronyms, and definitions.

5 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

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PACKAGING INFORMATION

Orderable part number	Status (1)	Material type	Package Pins	Package qty Carrier	RoHS (3)	Lead finish/ Ball material	MSL rating/ Peak reflow	Op temp (°C)	Part marking (6)
AMC60704YBHR	Active	Production	DSBGA (YBH) 36	3000 LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 105	AMC60704
AMC60704YBHR.A	Active	Production	DSBGA (YBH) 36	3000 LARGE T&R	Yes	SNAGCU	Level-1-260C-UNLIM	-40 to 105	AMC60704

⁽¹⁾ Status: For more details on status, see our product life cycle.

Multiple part markings will be inside parentheses. Only one part marking contained in parentheses and separated by a "~" will appear on a part. If a line is indented then it is a continuation of the previous line and the two combined represent the entire part marking for that device.

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⁽²⁾ Material type: When designated, preproduction parts are prototypes/experimental devices, and are not yet approved or released for full production. Testing and final process, including without limitation quality assurance, reliability performance testing, and/or process qualification, may not yet be complete, and this item is subject to further changes or possible discontinuation. If available for ordering, purchases will be subject to an additional waiver at checkout, and are intended for early internal evaluation purposes only. These items are sold without warranties of any kind.

⁽³⁾ RoHS values: Yes, No, RoHS Exempt. See the TI RoHS Statement for additional information and value definition.

⁽⁴⁾ Lead finish/Ball material: Parts may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

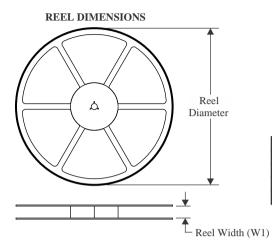
⁽⁵⁾ MSL rating/Peak reflow: The moisture sensitivity level ratings and peak solder (reflow) temperatures. In the event that a part has multiple moisture sensitivity ratings, only the lowest level per JEDEC standards is shown. Refer to the shipping label for the actual reflow temperature that will be used to mount the part to the printed circuit board.

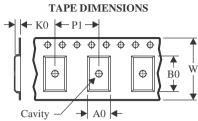
⁽⁶⁾ Part marking: There may be an additional marking, which relates to the logo, the lot trace code information, or the environmental category of the part.

PACKAGE MATERIALS INFORMATION

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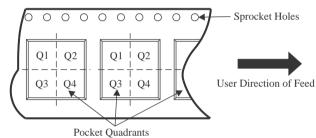
TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

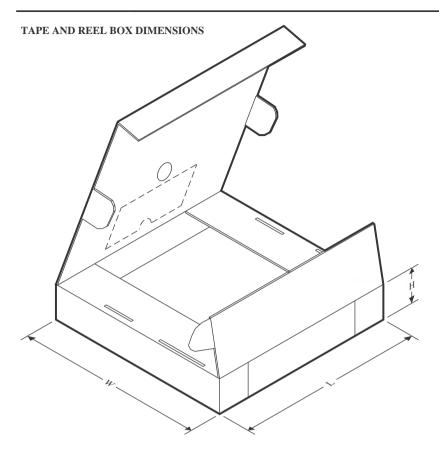
QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
AMC60704YBHR	DSBGA	YBH	36	3000	180.0	8.4	2.71	2.71	0.6	4.0	8.0	Q1

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*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)	
AMC60704YBHR	DSBGA	YBH	36	3000	182.0	182.0	20.0	

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