

Welcome!

Texas Instruments New Product Update

- This webinar will be recorded and available at www.ti.com/npu
- Phone lines are muted
- Please post questions in the chat or contact your TI sales contact or field applications engineer

INTRODUCING THE NEW ARM[®] CORTEX[®]-M0+ MCU PORTFOLIO

New Product Update

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- Product Marketing Engineer

Agenda

- Portfolio introduction and features
- L-series and G-series M0+ MCUs
- Getting started with development

Please feel free to use the “chat” function for any questions you have throughout this presentation.

MSPM0 MCUs | More options. Unlimited possibilities.

The most comprehensive portfolio of Arm® Cortex® M0+ microcontrollers that delivers the sensing and processing features you need



Cost optimized

- Leveraging TI's recent capacity and cost investments
- Industry's smallest packages enable the smallest PCB designs
- High performance integrated analog to reduce BOM cost



Scalable

- 32/80 MHz, 8-128 kB flash, 16-64 pins, and scalable analog
- Pin to pin compatible across wide range of memory & analog options
- 105° C, 125° C, and AEC-Q100 automotive options



Simple to use

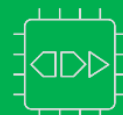
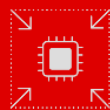
- Fast, fully graphical device configuration with code generation
- Code size optimized drivers (1/3rd the size of competitors)
- Plug-and-play subsystems, code examples, and reference designs

MSPM0 MCUs | Portfolio features

MSPM0 Mixed-Signal MCUs

Arm Cortex-M0+ CPU
32 or 80 MHz at less than 96 $\mu\text{A}/\text{MHz}$
Up to **174** CoreMark / **76** MIPS
Optional hardware **DIV/SQRT/TRIG/MAC**

Compute



Sense

Up to 4-Msps, 12-bit SAR ADCs
Zero-drift **chopper** op-amps with **PGA**
High-speed / ultra-low power comparators

8- to 128-kB flash memory (optional **ECC**)
2- to 32-kB SRAM (optional **ECC**)

Memory



Control

Advanced **control** timers
General purpose & **low power** timers
12-bit 1-Msps buffered DAC, 8-bit ref DAC

SOT, VSSOP, QFN, QFP
16 to 64 pins with **pin-to-pin** compatibility

Package



Connect

UART (LIN), I2C (FM+), SPI, and CAN-FD,
AES (128/256), TRNG, secure boot

1.62-3.6 V
<200- μs cold boot time

1- μA standby
with retention

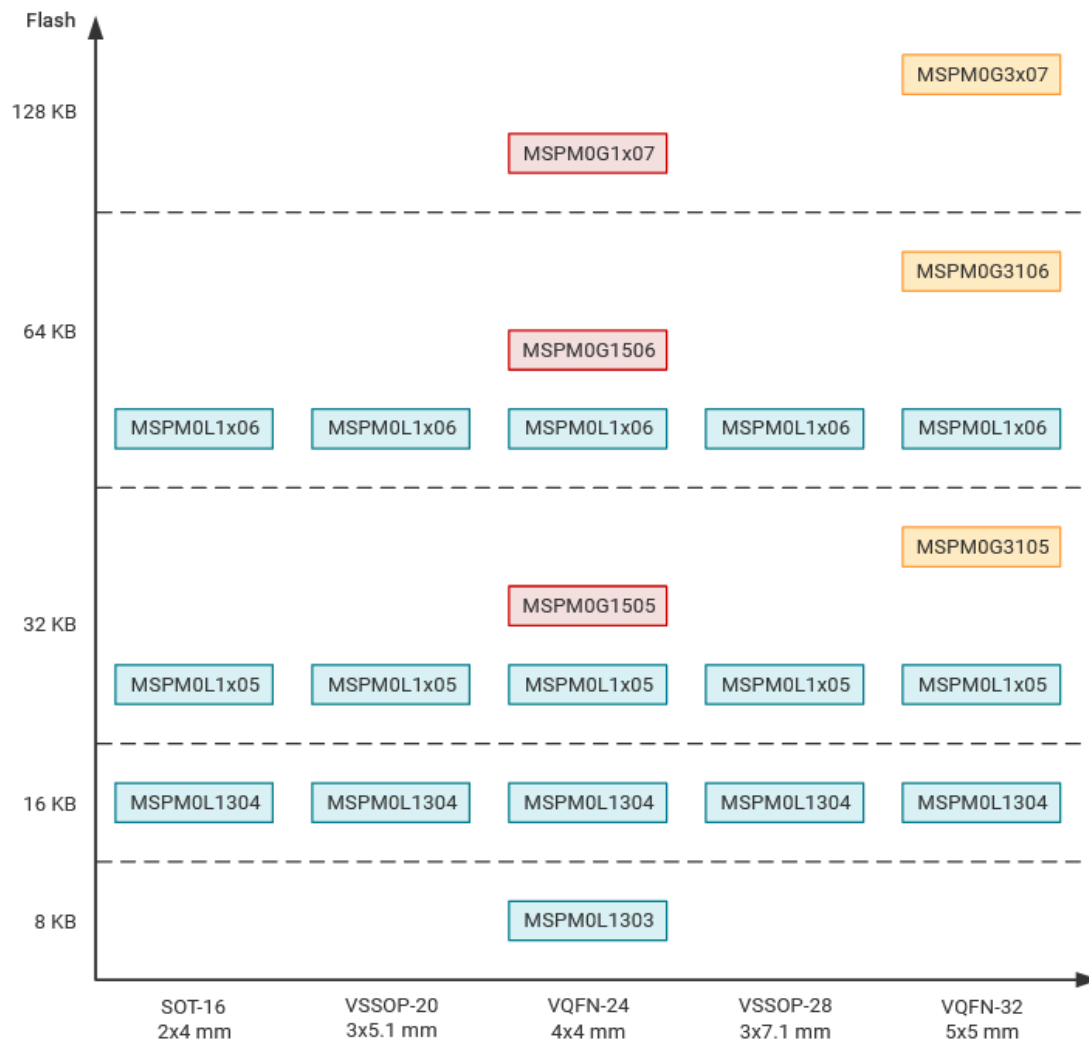
$\pm 1\%$ on-chip HF
oscillator

-40 to 125C
temperature range

AEC-Q100 grade 1
automotive

Easy-to-use platform with a common software development kit and pin-to-pin compatibility in common packages

MSPM0 MCUs | Broad scalability



32 MHz

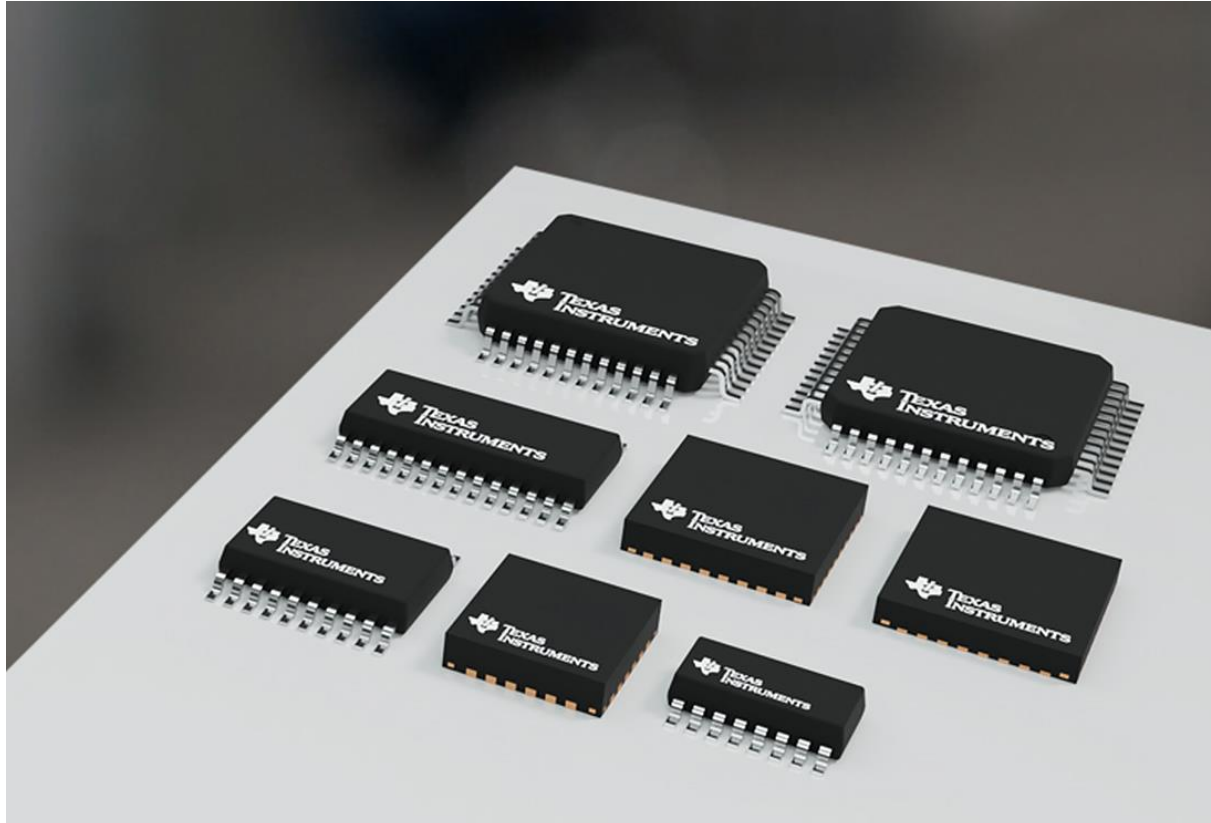
L-series MCUs

80 MHz

G-series MCUs

Pin-to-pin
Hardware and software
compatible

MSPM0 MCUs | Scalable small packages



8

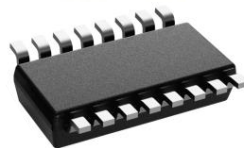
Pin to pin compatible
packages

2x pins, 1/2 space
16-pins SOT vs. 8-pins SOIC

Low cost

SOT, VSSOP packaging technologies

Competing MCUs
SOIC (8-pin)
29.4mm²



TI MSPM0 MCUs
SOT (16-pin)
13.4mm²

MSPM0 L-Series microcontrollers

MSPM0L13xx/110x

1.62 - 3.6V
-40 to 125 C

CPU Arm Cortex-M0+ 32 MHz NVIC / 3-ch DMA	Power & Clocking POR / BOR / SVS Internal LF 32kHz (3%) Internal HF 4-32MHz (1%)	Analog 12-bit ADC 1.45Msps (10-ch) Comparator w/ 8-bit DAC Zero-drift chopper op-amps (2) General purpose amp Internal ADC reference (1.5%) Temperature sensor
On-chip Memory 8, 16, 32 or 64 kB flash 2 or 4 kB SRAM	Communication UART w/ LIN (1) UART (1) SPI (1) I2C (2) w/ FastMode+	Timers Low power 16-bit 2 CC (4) Windowed watchdog
Data Integrity & Security CRC accelerator (16 and 32 bit)	IO Up to 28 GPIO Up to 2 low Ib OPA inputs	
Programming & Debug ARM SWD interface UART & I2C bootloader		

Leaded packages: SOT-16, VSSOP-20/28
No-lead packages: WQFN-16, VQFN-24/32

< \$0.39
starting at 1 kU

< 0.5 $\mu\text{V}/^\circ\text{C}$
Op-amp input offset drift

46
8-kB – 64-kB memory,
package, peripheral options

MSPM0 MCUs | Get more accuracy out of your sensors

7x

Reduction in input
offset voltage error

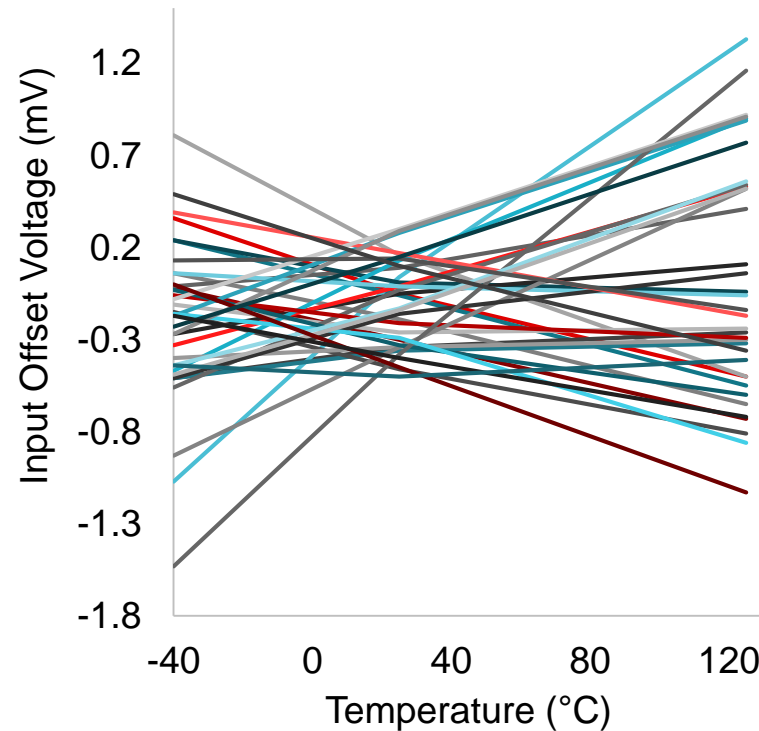
Zero

Crossover distortion

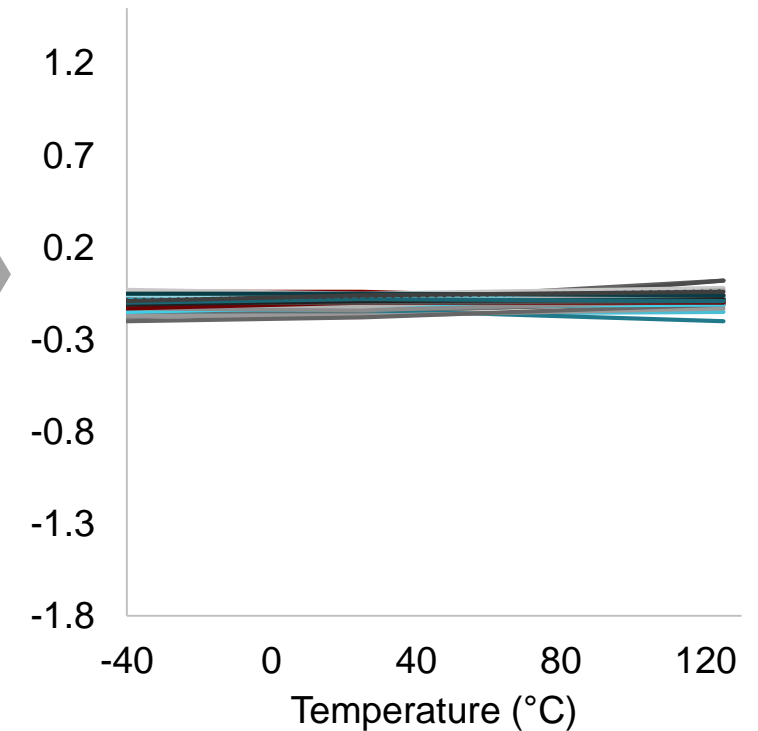
<150 pA

Available ultra-low input
bias current

Input Offset vs. Temperature
No Chopping
($\pm 2\text{mV}$ max)



Input Offset vs. Temperature
With Chopping
($\pm 300\mu\text{V}$ max)



MSPM0 G-Series microcontrollers

MSPM0G350x/310x/150x/110x			1.62 - 3.6V -40 to 125 C
CPU Arm Cortex-M0+ 80 MHz NVIC / MPU / 7-ch DMA	Power & Clocking POR / BOR / SVS External LF 32kHz XTAL External HF 4-48MHz XTAL Internal LF 32kHz (3%) Internal HF 4-32MHz (1%) PLL (up to 80 MHz)	Precision Analog 12-bit ADC 4Msps (9-ch) 12-bit ADC 4Msps (8-ch) Comparators w/ 8-bit DACs (3) 12-bit 1Msps buffered DAC (1) Zero-drift chopper op-amps (2) Internal reference (1.5%) General purpose amp (1) Temperature sensor	
Accelerators Math (DIV, SQRT, TRIG, MAC)	Communication UART w/ LIN (1) UART (3) SPI (2) I2C (2) w/ FastMode+ CAN-FD (1)	Timers Advanced control 16-bit 4 CC (1) Advanced control 16-bit 2 CC (1) General purpose 32-bit 2 CC (1) General purpose 16-bit 2 CC (2) Low power 16-bit 2 CC (2) Windowed watchdog (2) Real-time clock (1)	
On-chip Memory 32, 64, or 128 kB flash [ECC] 16 or 32 kB SRAM [ECC]	IO Up to 60 GPIO		
Data Integrity & Security CRC accelerator (16 and 32 bit) AES256 accelerator + TRNG			
Programming & Debug ARM SWD interface UART & I2C bootloader			
Leaded packages: VSSOP-20/28, LQFP-48/64 No-lead packages: VQFN-24/32/48			

Dual 4 Msps

12- bit ADCs with 14 bit oversampling

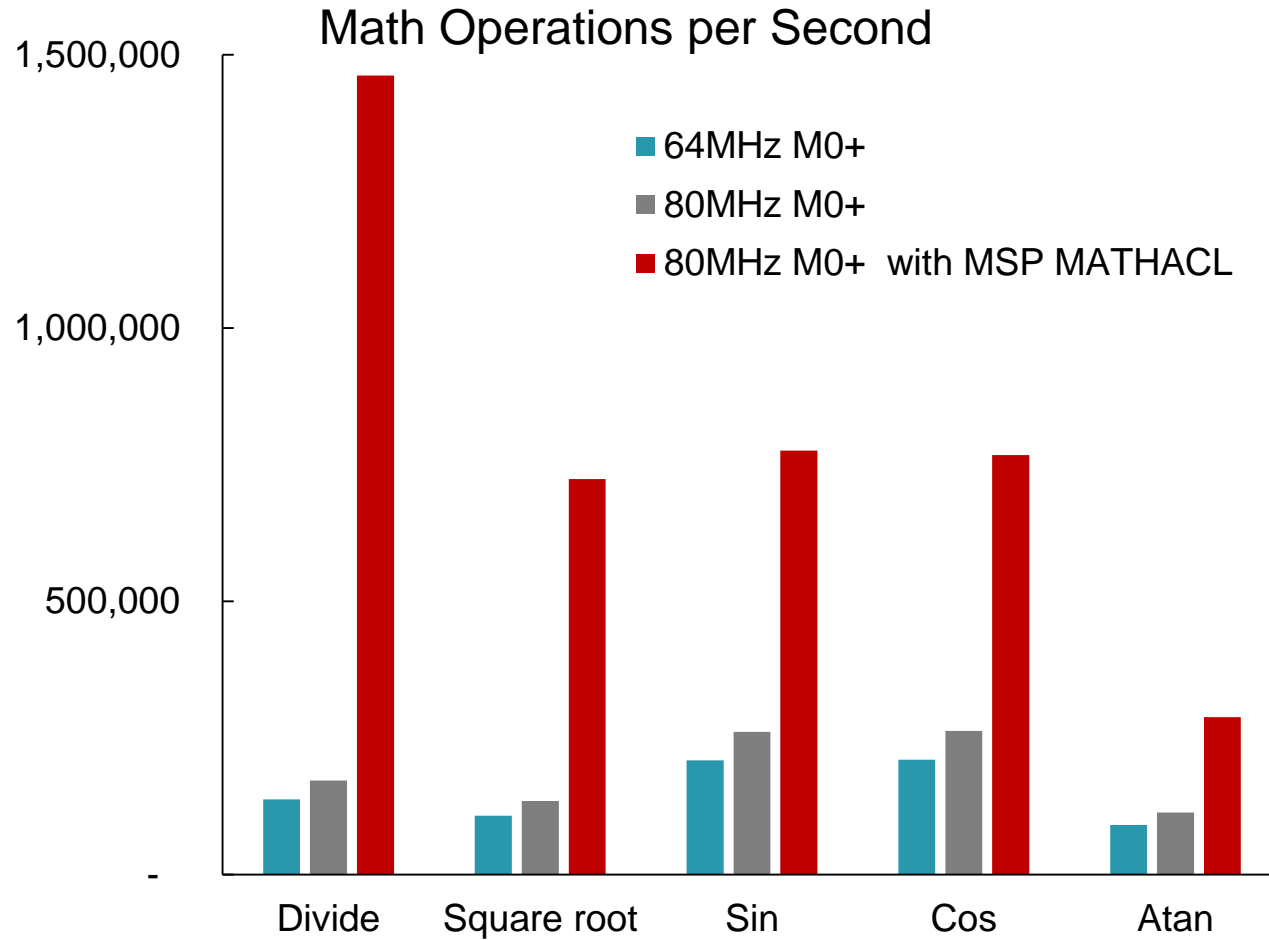
3X lower latency

In field oriented motor control loops

87

32 kB- 128 kB memory,
package, peripheral options

MSPM0 MCUs | Reduce your real-time loop latency



3x

Lower loop latency in
field-oriented motor control

6x

More square root operations per
second

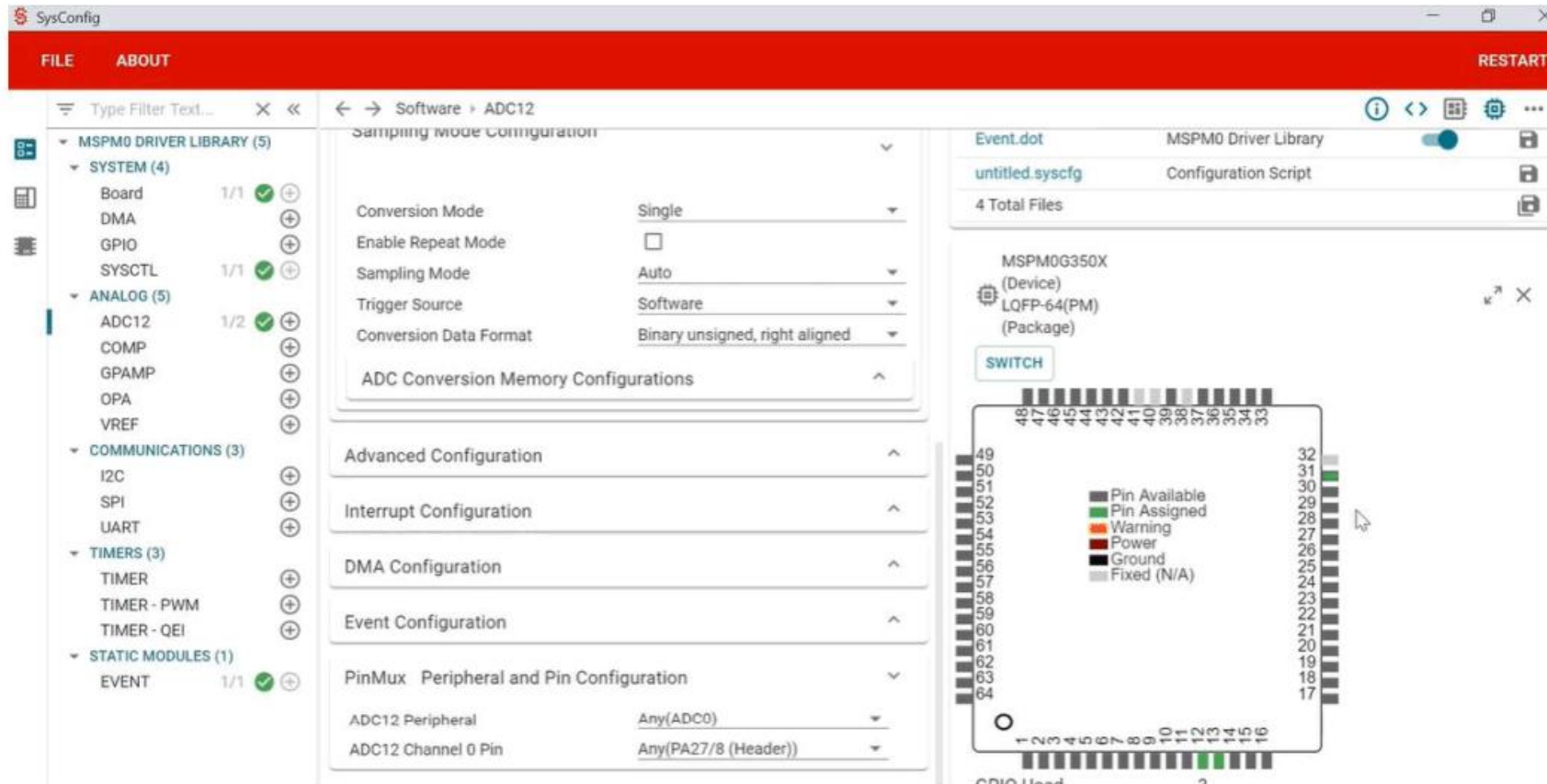
10x

More divide operations per
second

MSPM0 MCUs | Simple, fast development

Rapidly prototype with
a live demo today!

Graphical Configuration Tool – SysConfig

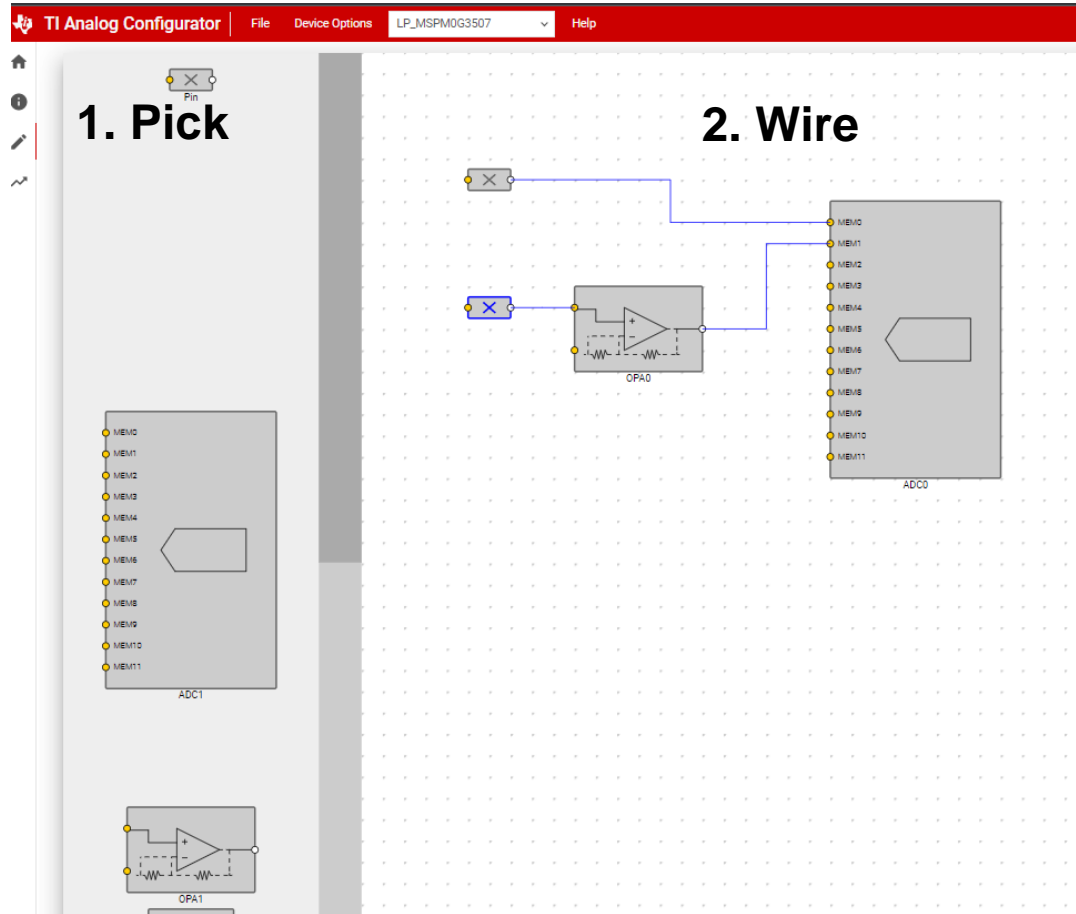


Peripheral initialization
Automatic pin mux resolution
Clock tree configuration
Power estimation

MSPM0 MCUs | Simple, fast development

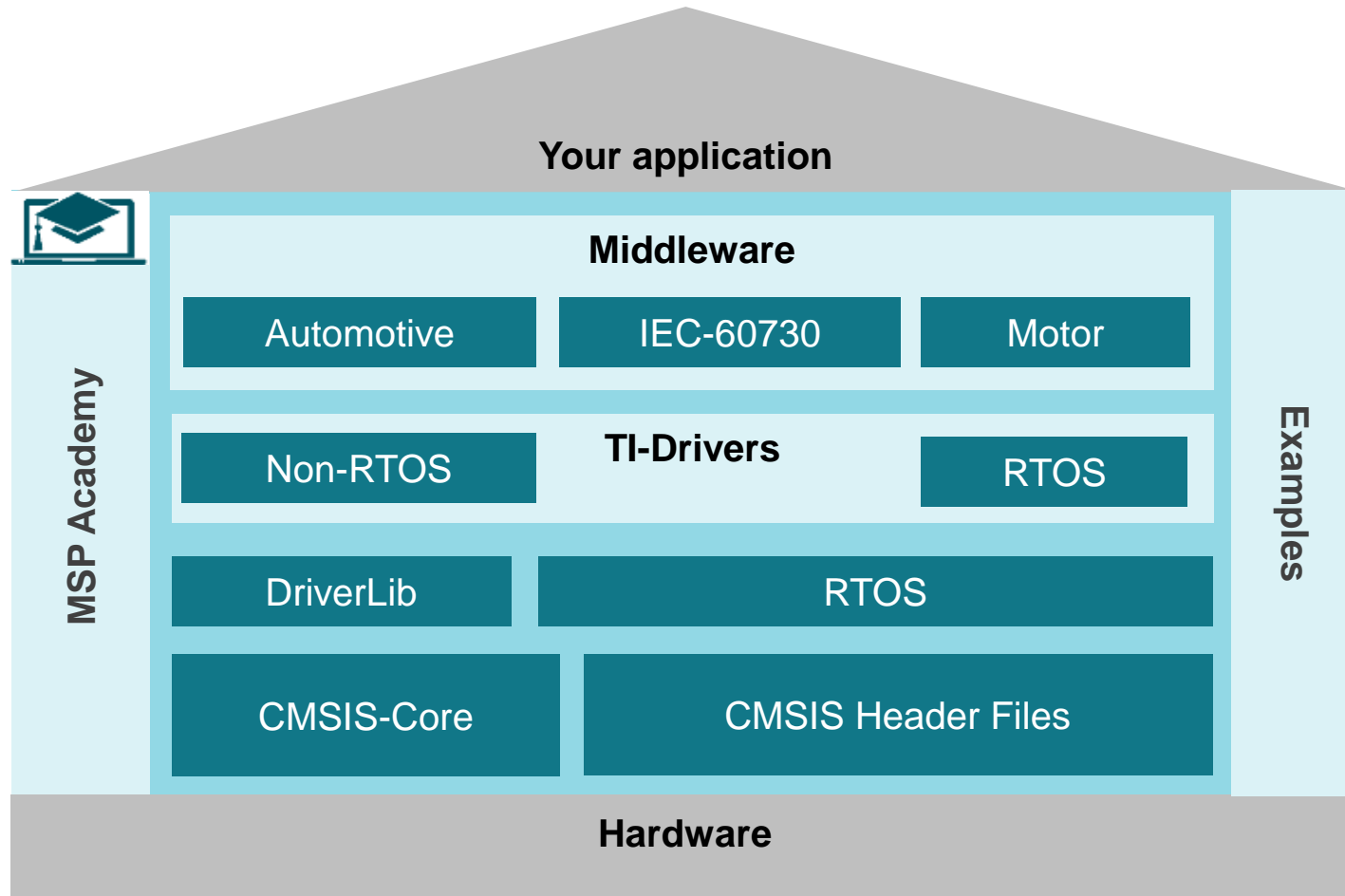
Rapidly prototype with
a live demo today!

Analog Configurator

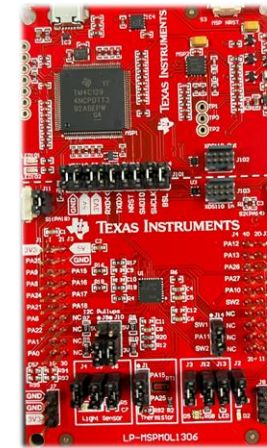


With just a few clicks, users can visualize their signal chain, make modifications to it, and view real-time data for evaluation.

MSPM0 MCUs | Optimized software



Rapid prototyping tools



Launchpads
Available
Today!

3P Tools

Getting started

You can start evaluating this device leveraging the following:

Content type	Content title	Link to content or more details
Portfolio overview	Overview page for MSPM0 MCUs	MSPM0 overview page
Software	MSPM0 software development kit (SDK)	MSPM0-SDK
Customer training series or webinar session	MSPM0 Academy Training	MSPM0 Academy
Technical blog content or white paper	How Arm Cortex-M0+ MCUs optimize general-purpose processing, sensing and control	Technical article
Selection and design tools and models	MSPM0 MCU product selection tool	Product selection tool
Development tool or evaluation kit	MSPM0L1306 LaunchPad™ development kit	LaunchPad development kit

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series, calendar and archived recordings



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