# Welcome! Texas Instruments New Product Update

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- Phone lines will be muted
- Please post questions in the chat or contact your sales person or field applications engineer

# **New Product Update:** Next-gen 4K from TI DLP® Pico™ technology

Amber Scheurer May 6, 2021



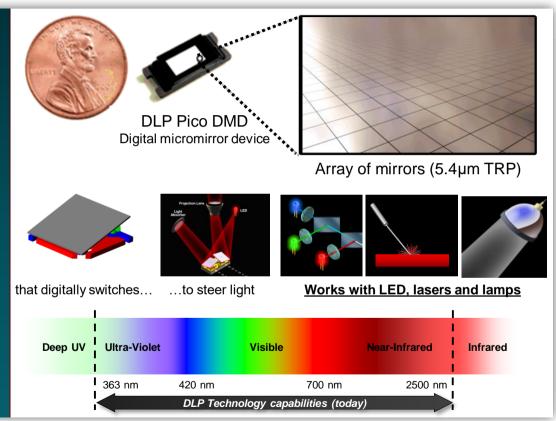


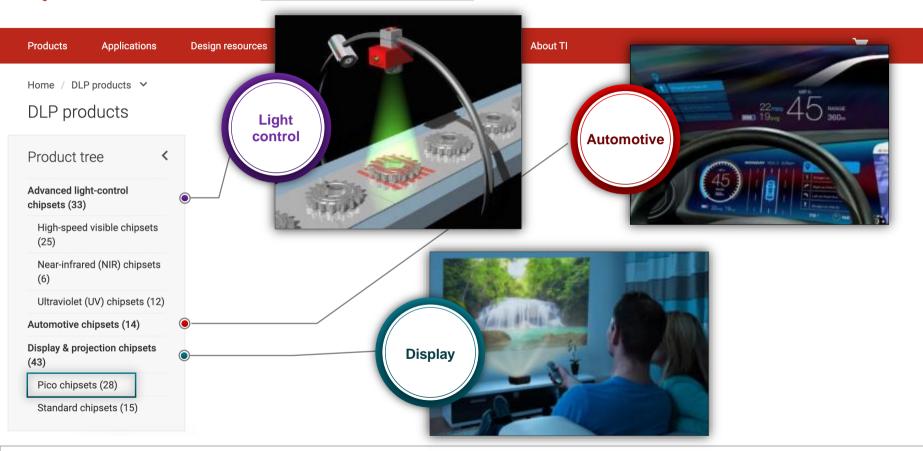
### **DLP Technology** | Millions of mirrors



An industry leader in digital cinema, projection, and MEMS

Extremely flexible and programmable light management







### **DLP Pico** | Display applications











#### Wide range of standalone or embedded display applications:

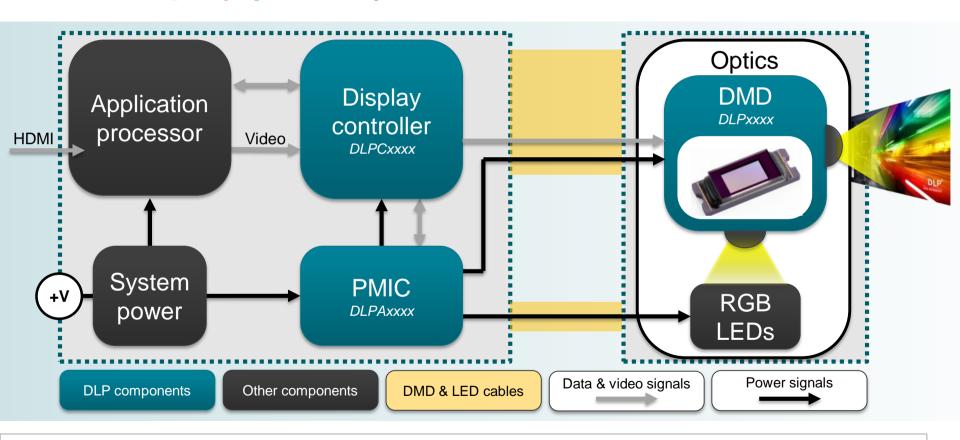
Smartphones & tablets
Smartphone companion
AR glasses
Robotics
Appliances

#### Mobile smart TVs Laser TVs

Smart displays
Educational toys
Enterprise projectors
Digital signage
Interactive kiosks
Commercial gaming
And more...



### **DLP Display** | Sub-system overview

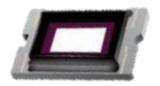




### **DLP471TP chipset** | Second generation 4K

#### Mainstream 4K with smallest form factor and lowest cost under 1,500 lm

Reduced system BOM cost (>\$35) and complexity with integrated controller functionality



#### **DLP471TP DMD**

Optically backward compatible with minor mechanical changes



#### **DLPC6540 Controller**

Most advanced Pico controller including 2D keystone and warping

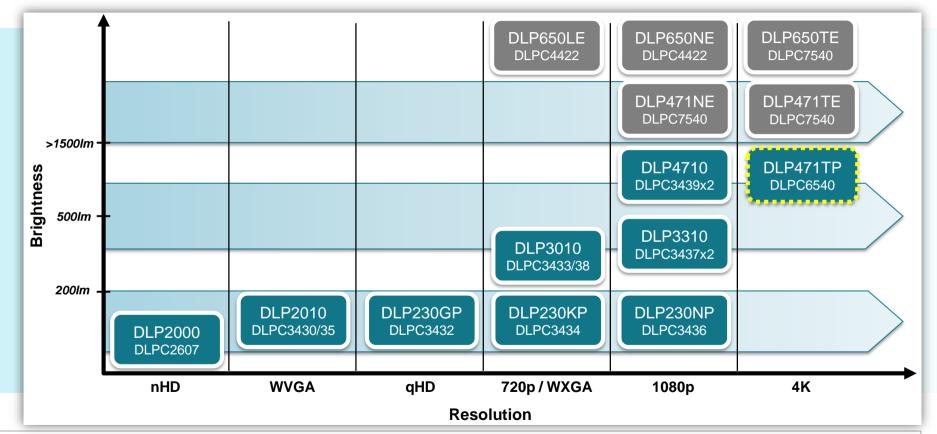


**Evaluation Module** 

Order from TI.com and quickly start development

### **DLP Products** | Display portfolio





#### **DLP471TP Chipset** Introduction

#### **Features**

- 0.47 4K HSSI DMD: DLP471TP
- Bottom illuminated, supports up to 1500 lm
- 3840 x 2160 Display Resolution
- Supports 4K UHD at 60Hz and 1080p at 240Hz

DLPC6540 Controller



- Maintains closed source architecture
- High Speed Serial Interface (HSSI) to DMD
- Consolidates from two controllers to one
- More flexibility for V-by-1 input timing
- 1D, 2D, 3D keystone and manual warping
- HDR and Rec.2020 colorspace

#### **Benefits**

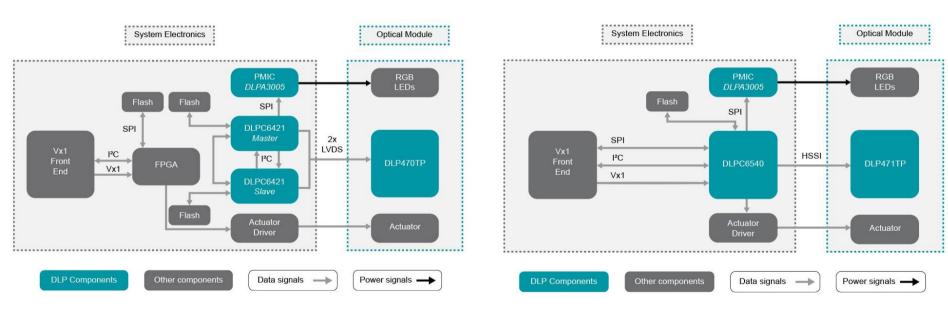
- System BOM cost reduction from previous Pico 4K chipset
  - Reduce system cost and complexity with integrated controller; consolidates dual-DLP6421, FPGA, memory
- DMD is optically backward compatible to existing DLP470TP optics with minor mechanical changes
  - Enables quicker time to market and lower development cost
- 2D keystone and manual warping
  - Advanced features incorporated into controller, reduces external components



### **DLP471TP Chipset** | Second generation 4K

Gen1
DLP470TP/DLPC6421 4K UHD System

Gen2
DLP471TP/DLPC6540 4K UHD System





### **DLP471TP Chipset** | Typical use-case





- Ultra-short throw (UST) or standard throw optics
- Large 4K display from small unit, easier setup
- Most have smart TV capability
- Can also be used in enterprise, commercial, or retail environments

### **DLP471TP Chipset** | Advanced warping







- Ultra-short throw (UST) optics are convenient but more sensitive
- Pair with low-cost camera to create automatic screen-fit features
- Enabled by DLPC6540 warping engine
- Improves ease of use for customers

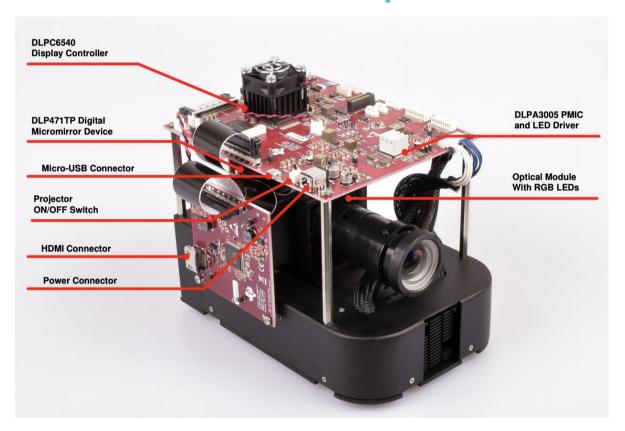
### **DLP471TP Chipset | Advanced warping**





- Project on any surface & dynamically change content
- Warping engine can adjust content to correct shape or even color

#### **DLPDLCR471TPEVM | 4K EVM**



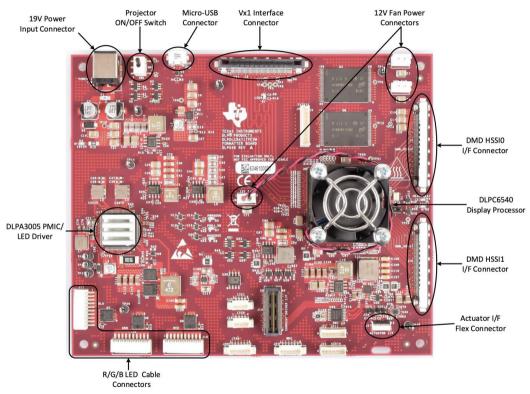
Accelerate development with \$999 evaluation module

Optical engine enables 4K display right out of the box

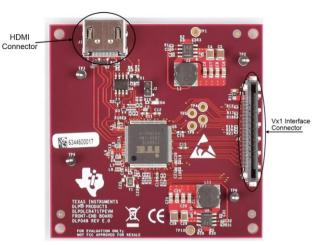
**DLPDLCR471TPEVM** 



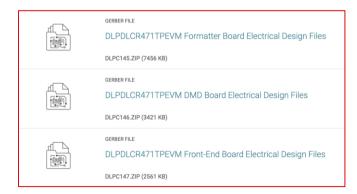
#### **DLPDLCR471TPEVM | 4K EVM**



Formatter board



#### Front-end board



#### **Design files**



### **DLPDLCR471TPEVM** | Key resources



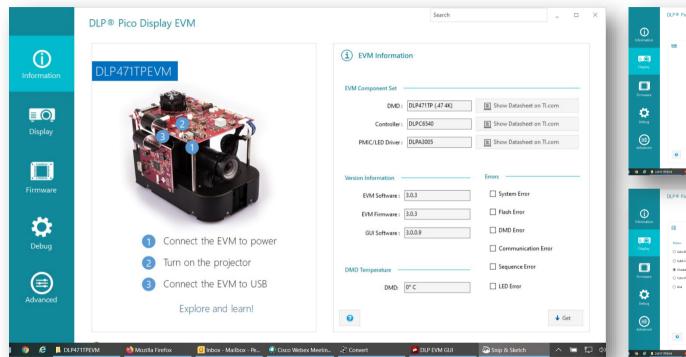
EVM user's guide

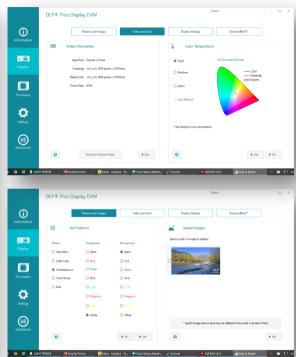
Programmer's Guide DLPC6540 Programmer's Guide	
TEXAS INSTRUMENTS	
	1EXAS INSTRUMENTS
Sanjeev Kumar	
	ABSTRACT
This guide provides details of the software interface requirements for a DLPC6540 controller based system. This descriptions includes the communication protocol, initialization, default settings, common use cases and command descriptions.	
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Programmer's guide



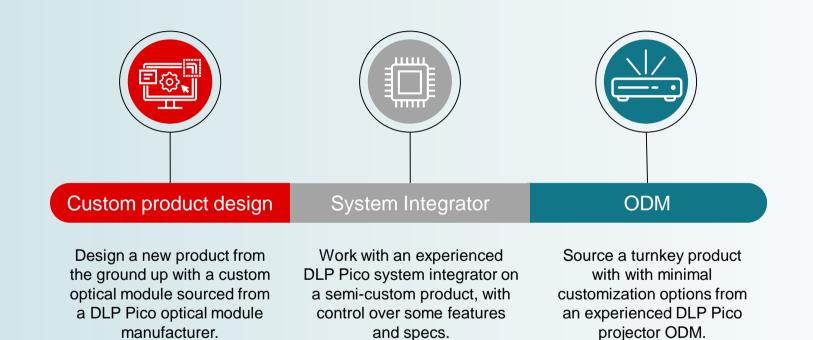
### **DLPDLCR471TPEVM** | EVM GUI





Download the **DLPDLC-GUI** to interface with the EVM and begin development

#### **DLP471TP Chipset** | Development options



#### **DLP471TP Chipset | Additional resources**







**DLPC6540** 



- How to solve two screenless TV design challenges technical article
- TI DLP System Design: Brightness Requirements and Tradeoffs application note
- Standard chipsets 4K product list (for >1,500 lumens)
- Optical module search tool selection guide

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