

Two Phase Interleaved PFC (ILPFC) EVM Specification

Quick Start Guide



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1 Introduction

- Input Voltage (AC Line): 100 V (Min) to 260 V (Max), 47Hz to approximately 63Hz
- Rated Output Voltage: 390 Vdc
- Rated Output Power: 700 Watts @220 V input, 550 Watts @110 V input (forced cooling required)
- Rated Load efficiency: 96% @220 V input, 93% @110 V input
- Power factor at 50% or greater load – 0.98 (Min)
- PWM frequency 200 kHz

2 Getting Started

2.1 Setting up the Hardware

WARNING

There are high voltages present on the ILPFC board. It should only be handled by experienced power supply professionals in a lab environment. To safely evaluate this board, an isolated AC source should be used to power up the unit. Before AC power is applied to the board, a voltmeter and an appropriate resistive load should be attached to the output. This discharges the bus capacitor quickly when the AC power is turned off. The board has not been tested with electronic load and so the use of such load is not recommended. There is no output overcurrent protection implemented on the board and so the user should take appropriate measures for preventing any output short circuit condition.

1. Connect an USB cable to the Piccolo (CC2803x) controller card for emulation. Connect the other end of the cable to the PC USB port.
2. Connect the external +12 Vdc supply (provided with the ILPFC EVM package) at JP1 to power up all the control circuit for now. By default, the Piccolo controller is enabled to boot from FLASH and run the ILPFC code. Do not use the RAM to program or run the ILPFC code.
3. Turn on the switch SW1 by placing it towards the electrolytic caps (C5, C6 and C7). This applies the external +12 V supply to the ILPFC board. The Piccolo controller will now boot from the FLASH and start executing the ILPFC code. Following this, one of the LEDs (LED3 on the CC2803x control card) starts flashing periodically. The flashing LED is an indication of the code being executed from the FLASH memory. However, the PFC bus remains at 0 V since the AC input voltage has not been applied yet.
4. Select an appropriate isolated AC power supply (100 Vac to 260 Vac, 47Hz to approximately 63Hz, 1000VA min) to provide input power to the ILPFC EVM. Connect the AC supply to the ILPFC EVM input connector P1, but do not turn on the AC power at this time.

5. Connect an appropriate resistive load to the ILPFC DC output terminals (PFC-Out and GND terminals). Resistive loads within the range of 10W to approximately 700W are recommended. Set the initial load to about 50W to approximately 80W. A 2K Ω resistor provides about 80W load at 400V dc bus. This board has not been tested with electronic load.
6. Connect an appropriate resistive load to the ILPFC DC output terminals (PFC-Out and GND terminals). Resistive loads within the range of 10W to approximately 700W are recommended. Set the initial load to about 50W to approximately 80W. A 2K Ω resistor provides about 80W load at 400V dc bus. This board has not been tested with electronic load.
7. Turn off the switch SW1 by placing it away from the electrolytic caps (C5, C6 and C7). This allows the isolated bias supply (the small bias supply board mounted at location M2 close to the PFC AC input connector P1) to provide +12 Vdc output when the AC power is turned on.
8. Set the AC input voltage to around 60 Vac, 60Hz, then turn on the AC power source. This allows the isolated bias supply to turn on (after some delay) and provide +12 V output. Piccolo controller will now boot from the FLASH and start executing the ILPFC code. LED3 on the CC2803x control card starts flashing periodically. With the AC input set to 60 Vac, the PFC DC bus voltage will not ramp up to rated output.
9. Increase the AC input voltage to around 130 Vac, 60Hz. The unit detects this minimum input voltage and the corresponding DC bus voltage (160 Vdc minimum). This condition must be met in order to soft-start the PFC
10. The PFC output ramps according to the slew rate to the rated bus voltage (approximately 390 Vdc).
11. Verify the effect on the dc bus voltage and on the input power factor when the input voltage (100 Vrms to approximately 260 Vrms) or the load power (0 W to approximately 550W @110Vin, 0W - to approximately 700W @ 220 Vin,) is varied. The dc bus should be regulated at 390 V under all conditions. Depending on the load (10% and higher) and line conditions, the power factor for the input current will vary from 0.95 to 0.999. [Figure 1](#) shows the PFC input voltage and current when the input voltage is about 110 V and the PFC load is about 550W load. [Figure 2](#) shows the input current and PFC DC bus response for step load between 240W to approximately 400W when the input voltage is set to 120 Vrms.
12. Ensure that the AC power is turned off, then wait for a few minutes for the high voltage PFC dc bus capacitor to discharge completely.

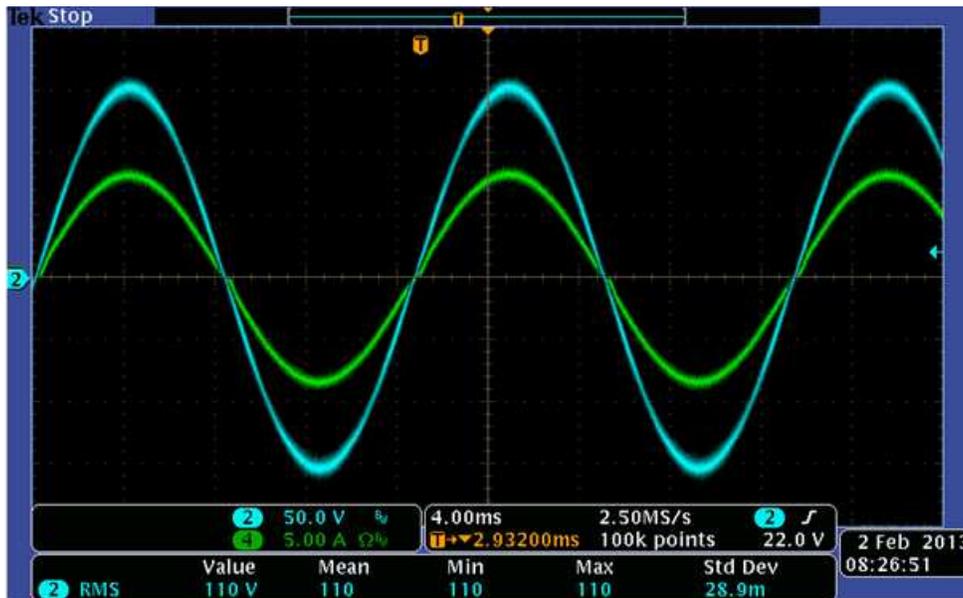


Figure 1. PFC Input Voltage and Current

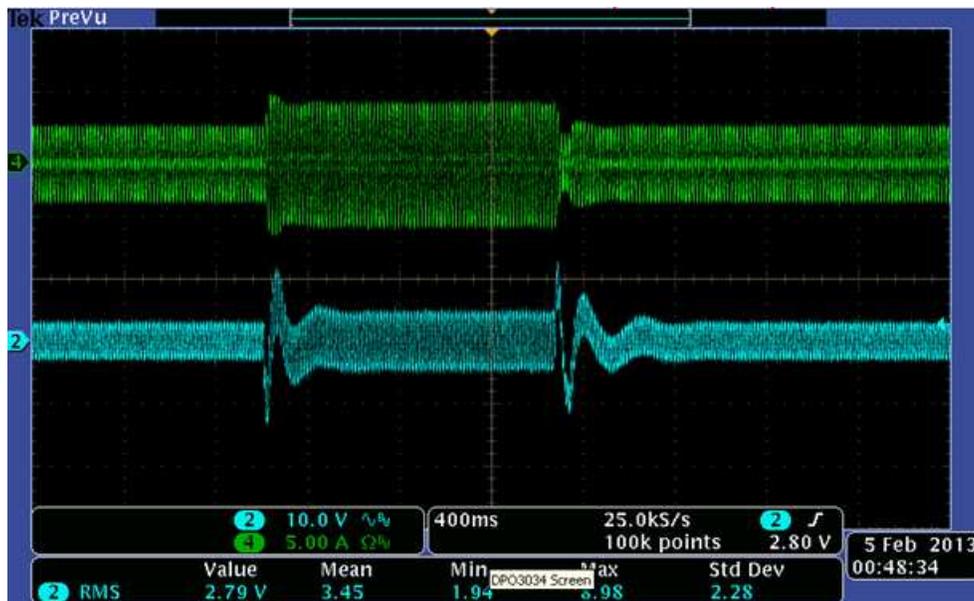


Figure 2. Input Current and PFC DC Bus Response for Step Load

3 References

- ILPFC User's Guide located in <http://www.ti.com/tool/controlsuite> at (..\controlSUITE\development_kits\ILPFC\~Docs\ILPFC.pdf)
- ILPFC_Rel-1.0-HWdevPkg located in <http://www.ti.com/tool/controlsuite> at (..\controlSUITE\development_kits\ILPFC\ILPFC_HWDevPkg)
- TMS320F28030, TMS320F28031, TMS320F28032, TMS320F28033, TMS320F28034, TMS320F28035 *Piccolo Microcontrollers Data Manual* ([SPRS584](#))

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