



Module 10

Introduction: Debugging Real-time Systems



Introduction: Debugging Real-time Systems

Educational Objectives:

REVIEW C programming arrays

UNDERSTAND how flash memory operates

EXPLORE debugging techniques for real-time systems

LEARN how to generate periodic interrupts using SysTick

INTERFACE bump switches to the robot

DESIGN, BUILD & TEST A SYSTEM

Stores input data into a black-box recorder

Prerequisites (Modules 6, 8, and 9)

- GPIO digital inputs (Module 6)
- Switches and LEDs (Module 8)
- SysTick timer (Module 9)

Recommended reading materials for students:

- Volume 1 Sections 2.7, 6.2, 6.9, 9.1, 9.2, 9.4, and 9.6
Embedded Systems: Introduction to the MSP432 Microcontroller
ISBN: 978-1512185676, Jonathan Valvano, copyright (c) 2017
- Volume 2 Sections 2.4, 3.9, 5.1, 5.4, and 5.7
Embedded Systems: Real-Time Interfacing to the MSP432 Microcontroller, ISBN: 978-1514676585, Jonathan Valvano, copyright (c) 2017

System verification is an important task when developing embedded systems, especially if the system is to be deployed in safety critical situations. Furthermore, in a **real-time system**, it is not only important to get the correct answer, it is important to get the correct answer at the correct time. **Latency** is the time between when a service is requested and the time when service is initiated. Similarly, **response time** is the time between when a service is requested and the time when service is complete. A real-time system is one that can guarantee a worst-case latency. Alternatively, we can categorize a system as real time if there is an upper bound on the response time.

Some requests occur periodically, and in this module we will use **SysTick interrupts** to execute tasks on a regular basis.

The second component to this module is to develop debugging techniques for real-time systems. **Intrusiveness** is defined as the degree to which the debugging code itself alters the performance of the system being tested. Breakpoints, single stepping, and printf output are high intrusive, and thus not appropriate for debugging real-time systems. Rather we will learn how to dump strategic information into arrays, providing similar observations as the classical printf statement, but in a minimally intrusive manner. For logging, debugging data for long periods of time, we can dump data into the flash ROM of the microcontroller.

In the lab associated with this module, you will interface bump sensors with the microcontroller, see Figure 1. These switches will allow you to know if and where the robot has contacted an obstacle. Data from the line sensor and bump sensors will be collected periodically using SysTick interrupts. Using interrupts to handle the line sensor provides a processor-efficient solution.

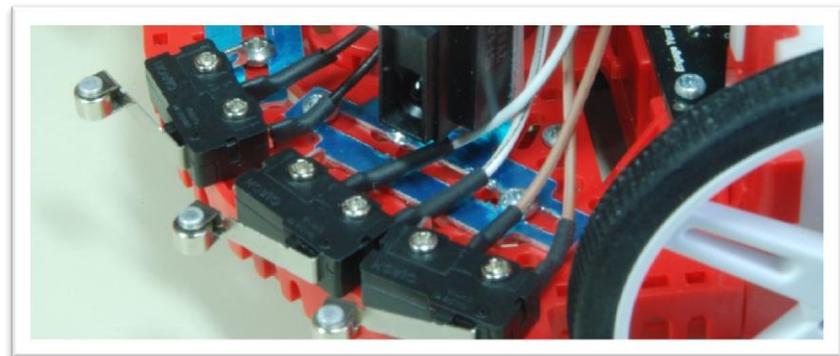


Figure 1. Bump sensors, positioned at the front of the robot.

The basic approach to a system requiring multiple software tasks is to deploy multithreading. One software **thread** is the traditional main program, which runs most of the time. Interrupts will be used to create additional threads. An **interrupt** is a hardware-triggered software execution. In this module, the SysTick interrupt will execute a software task periodically. In Module 13, we will use timers to create PWM outputs. In Module 14, we will use edge-triggered interrupts so a software task is executed immediately if any of the bump sensors are activated.

IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ("TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications that include TI products, you will thoroughly test such applications and the functionality of such TI products as used in such applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your non-compliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products (<http://www.ti.com/sc/docs/stdterms.htm>), [evaluation modules](#), and [samples](http://www.ti.com/sc/docs/sampterm.htm) (<http://www.ti.com/sc/docs/sampterm.htm>).

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2018, Texas Instruments Incorporated