



PCB LOGO
FCC disclaimer

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WEEE logo

OVP: Vin_A > 30V:

VDD UVLO threshold = 2.7 V
Vthresold = 0.8 V_T

OVP: Vout_1 > 6V:

VDD UVLO threshold = 2.7 V

19.2 V - 28.8 V (60 V max)

F0M43229H4M02UNT

Configuration:

- U1 controls U3 (Jumper 5 : Reset1)
 - U1 OVP Vin_A > 30 V
- U2 controls Q1 (Jumper 11 : Reset2)
 - U2 OVP Vout1 > 6 V
- U3 UVLO Vin_A = 14 V
- U4 UVLO Vin_C = 16 V
- REFDES > 100 added on board

fsw = 1Mhz

Orderable:	Designed for:	Mod. Date: 9/15/2025
TPD4E0100	Project Title: Change in menu Project Options/Parameter	
Number: PMP13365	Rev:	
SVN Rev: Not in version control	Assembly Variant: Rtp_Visualisat	Sheet: 1 of 3
Drawn By: F. Muller	File: PMP13365_T4M4_SchDoc	Size: A2
Engineer: F. Muller	Contact: http://www.ti.com/support	

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