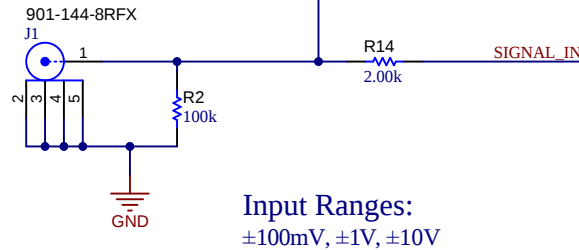
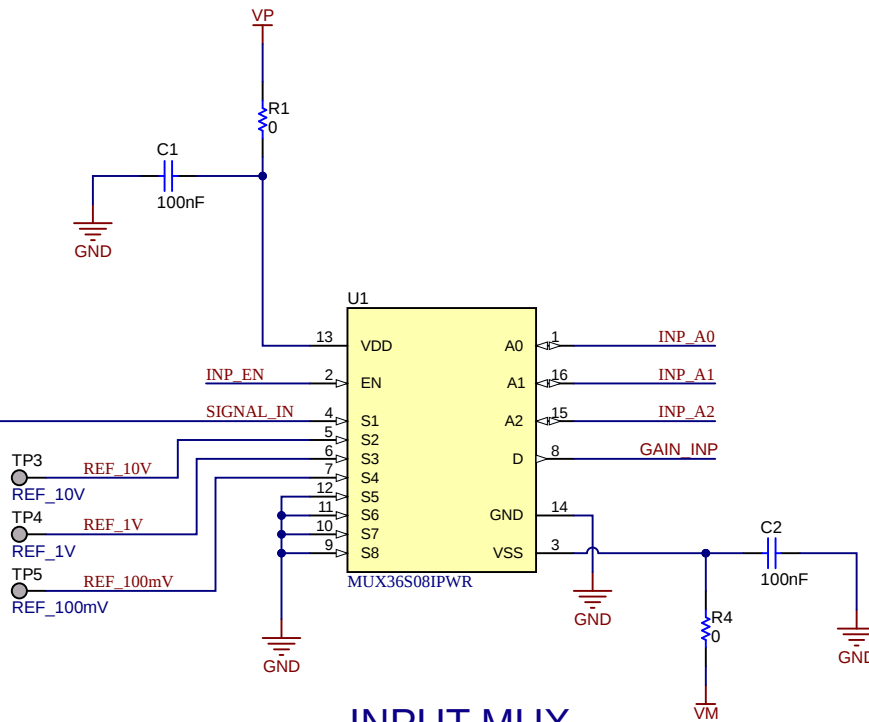


SIGNAL INPUT



Input Ranges:
 $\pm 100\text{mV}$, $\pm 1\text{V}$, $\pm 10\text{V}$

INPUT MUX



INPUT MUX CTRL

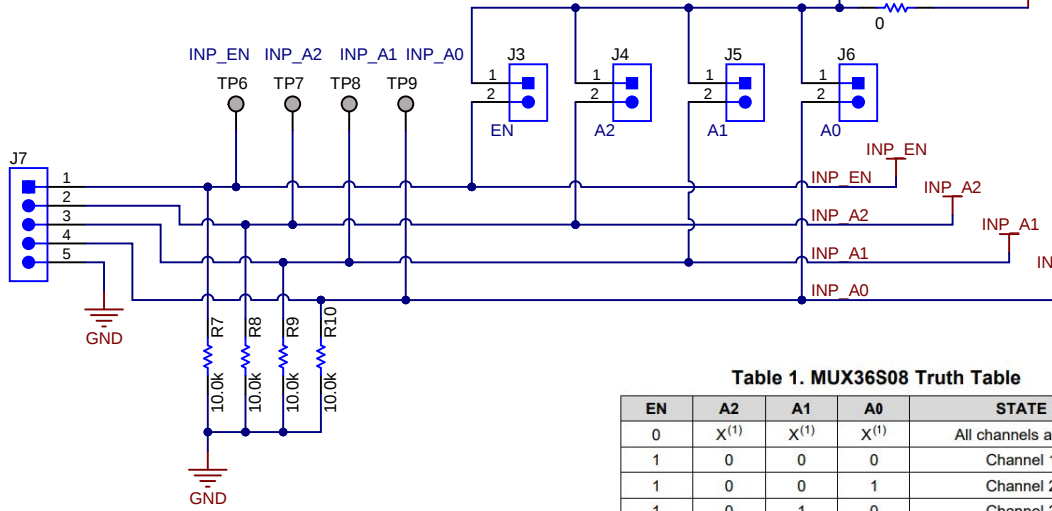
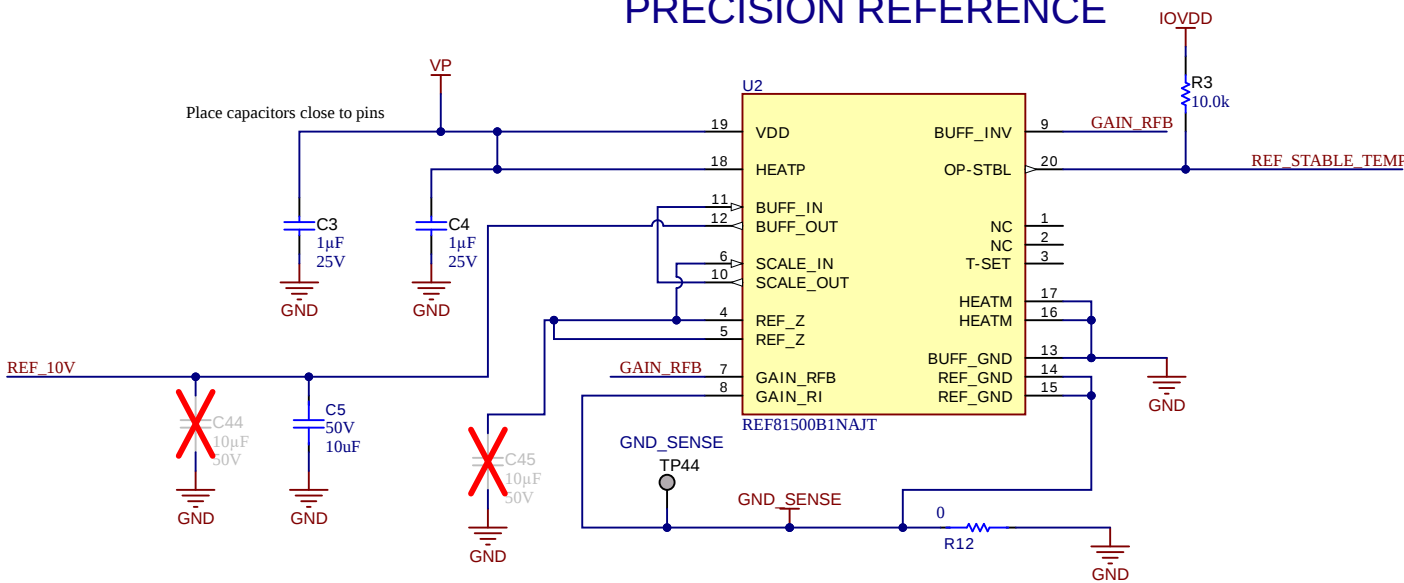


Table 1. MUX36S08 Truth Table

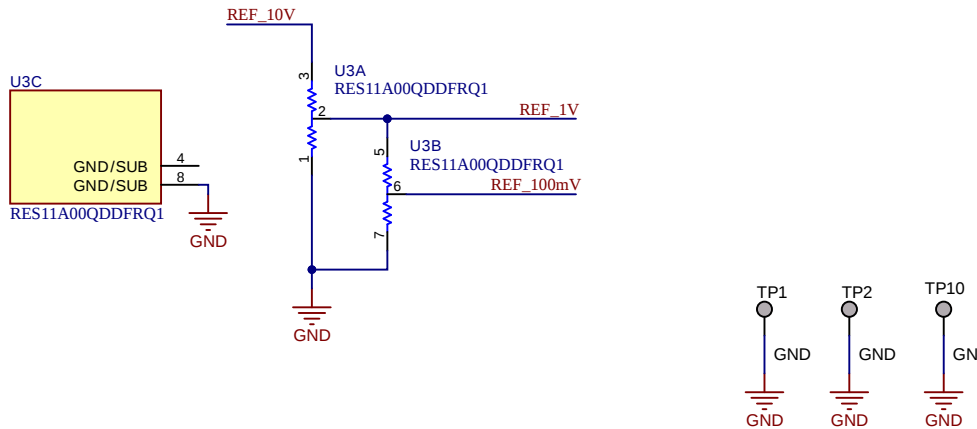
EN	A2	A1	A0	STATE
0	X ⁽¹⁾	X ⁽¹⁾	X ⁽¹⁾	All channels are off
1	0	0	0	Channel 1
1	0	0	1	Channel 2
1	0	1	0	Channel 3
1	0	1	1	Channel 4
1	1	0	0	Channel 5
1	1	0	1	Channel 6
1	1	1	0	Channel 7
1	1	1	1	Channel 8

(1) X denotes don't care..

PRECISION REFERENCE



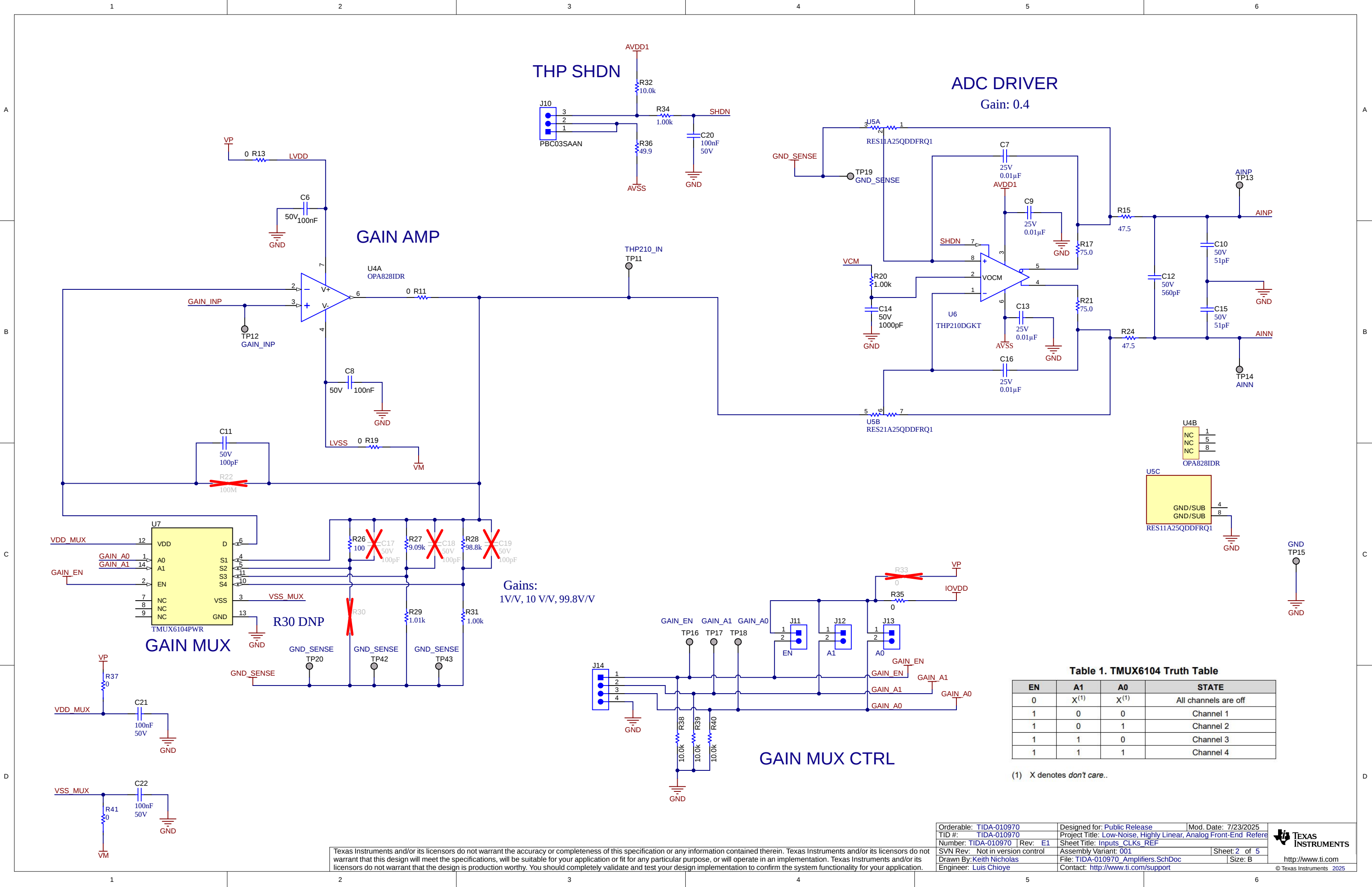
PRECISION RESISTOR DIVIDER



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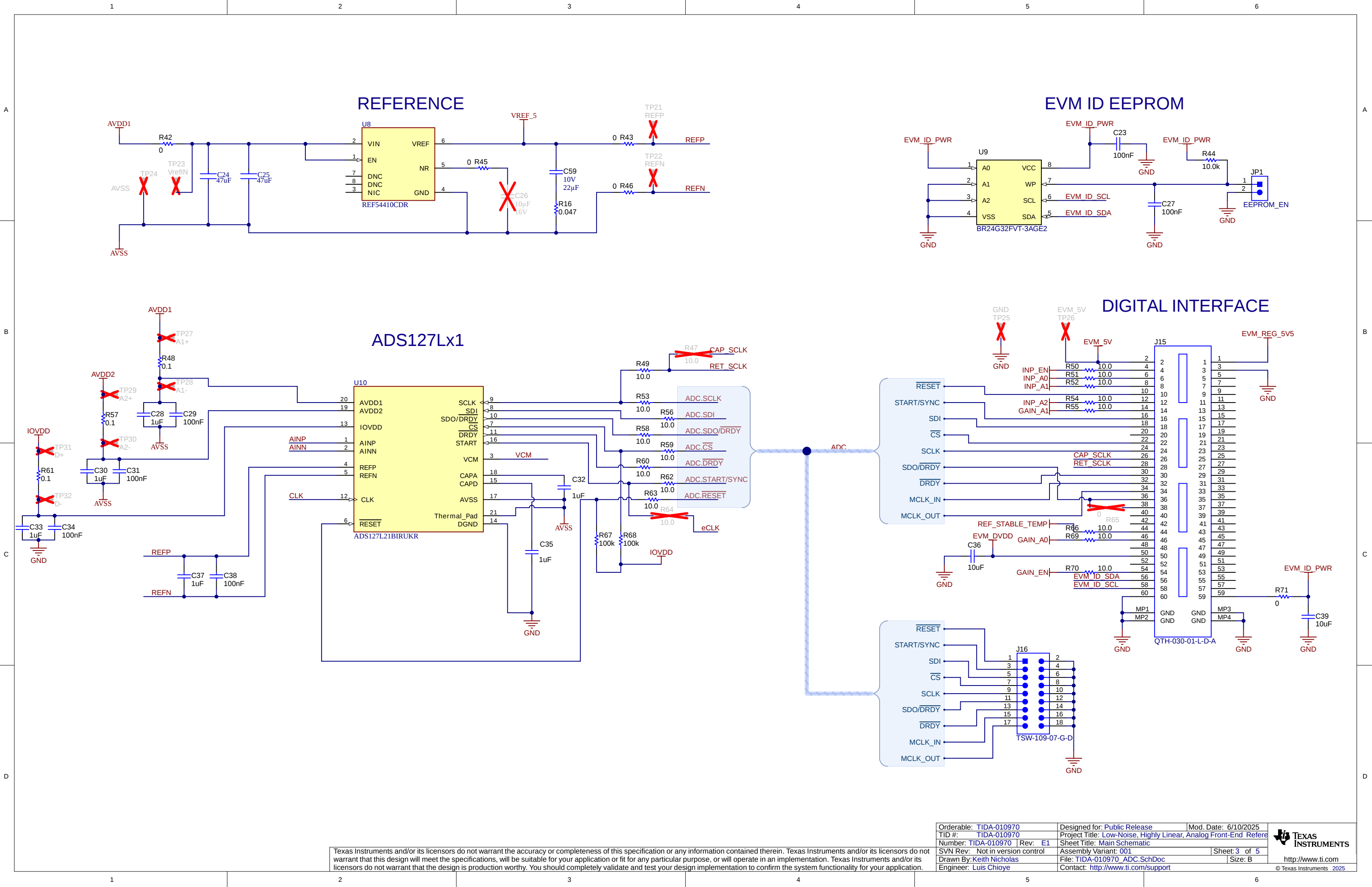
Orderable: TIDA-010970
TID #: TIDA-010970
Number: TIDA-010970
SVN Rev: Not in version control
Engineer: Luis Chioye
Designed for: Public Release
Project Title: Low-Noise, Highly Linear, Analog Front-End Refere
Sheet Title: Inputs_CLKs_REF
Assembly Variant: 001
File: TIDA-010970_Input.SchDoc
Contact: <http://www.ti.com/support>
Mod. Date: 9/4/2025
Sheet: 1 of 5
Size: B

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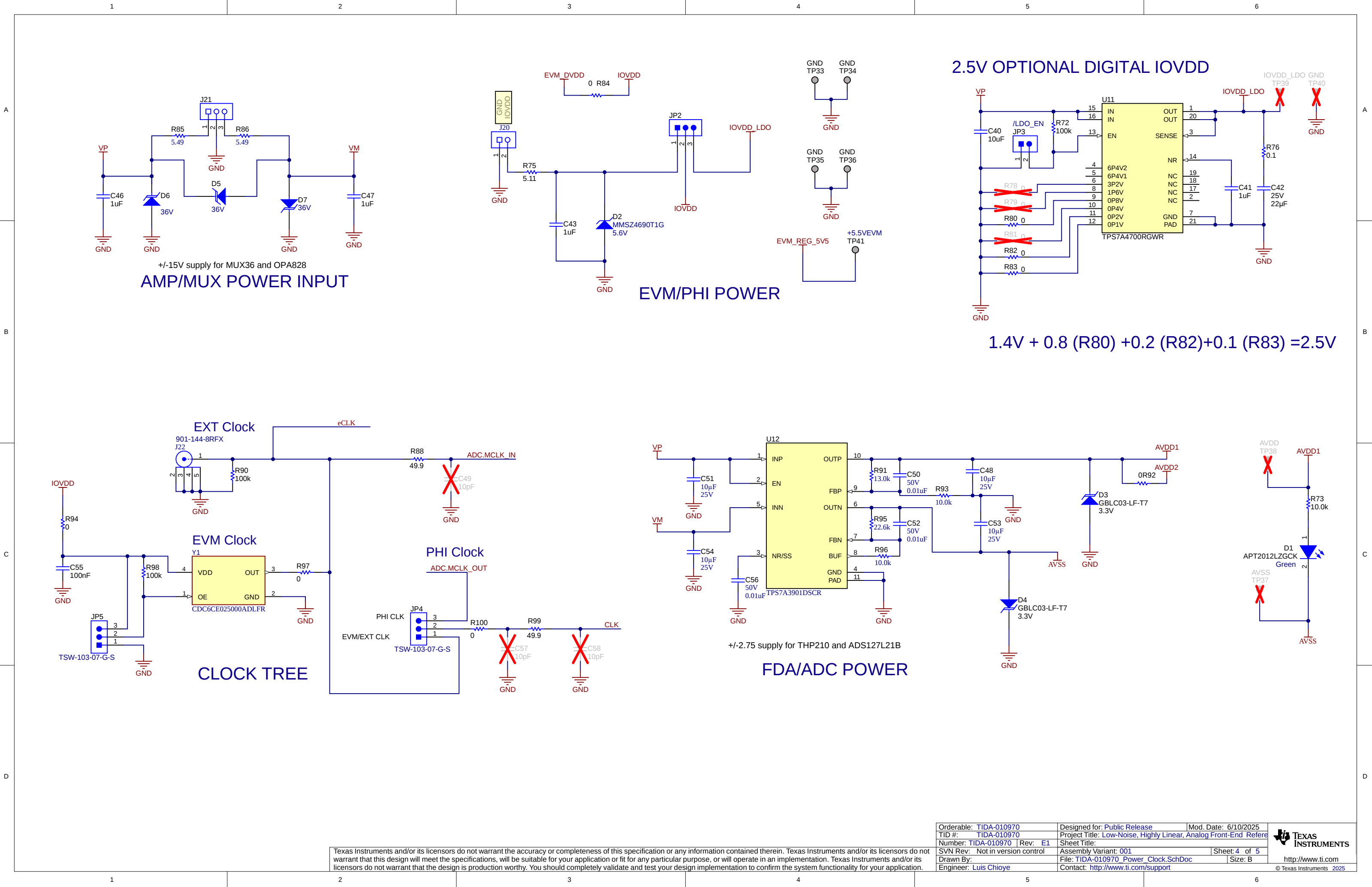
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Number: TIDA-010970	Rev: E1	Sheet Title: Inputs_CLKs_REF
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 2 of 5
Drawn By: Keith Nicholas	File: TIDA-010970_Amplifiers_SchDoc	Size: B
Engineer: Luis Chioye	Contact: http://www.ti.com/support	



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TID #: TIDA-010970	Project Title: Low-Noise, Highly Linear, Analog Front-End Reference	
Number: TIDA-010970	Rev: E1	Sheet Title: Main Schematic
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 3 of 5
Drawn By: Keith Nicholas	File: TIDA-010970_ADC.SchDoc	Size: B
Engineer: Luis Chioye	Contact: http://www.ti.com/support	



AMP/MUX POWER INPUT

EVM/PHI POWER

2.5V OPTIONAL DIGITAL IOVDD

$1.4V + 0.8 (R80) + 0.2 (R82) + 0.1 (R83) = 2.5V$

CLOCK TREE

FDA/ADC POWER

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TID #: TIDA-010970	Project Title: Low-Noise, Highly Linear, Analog Front-End Refere	
Number: TIDA-010970	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: 001	Sheet: 4 of 5
Drawn By:	File: TIDA-010970 Power_Clock.SchDoc	Size: B
Engineer: Luis Chioye	Contact: http://www.ti.com/support	

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