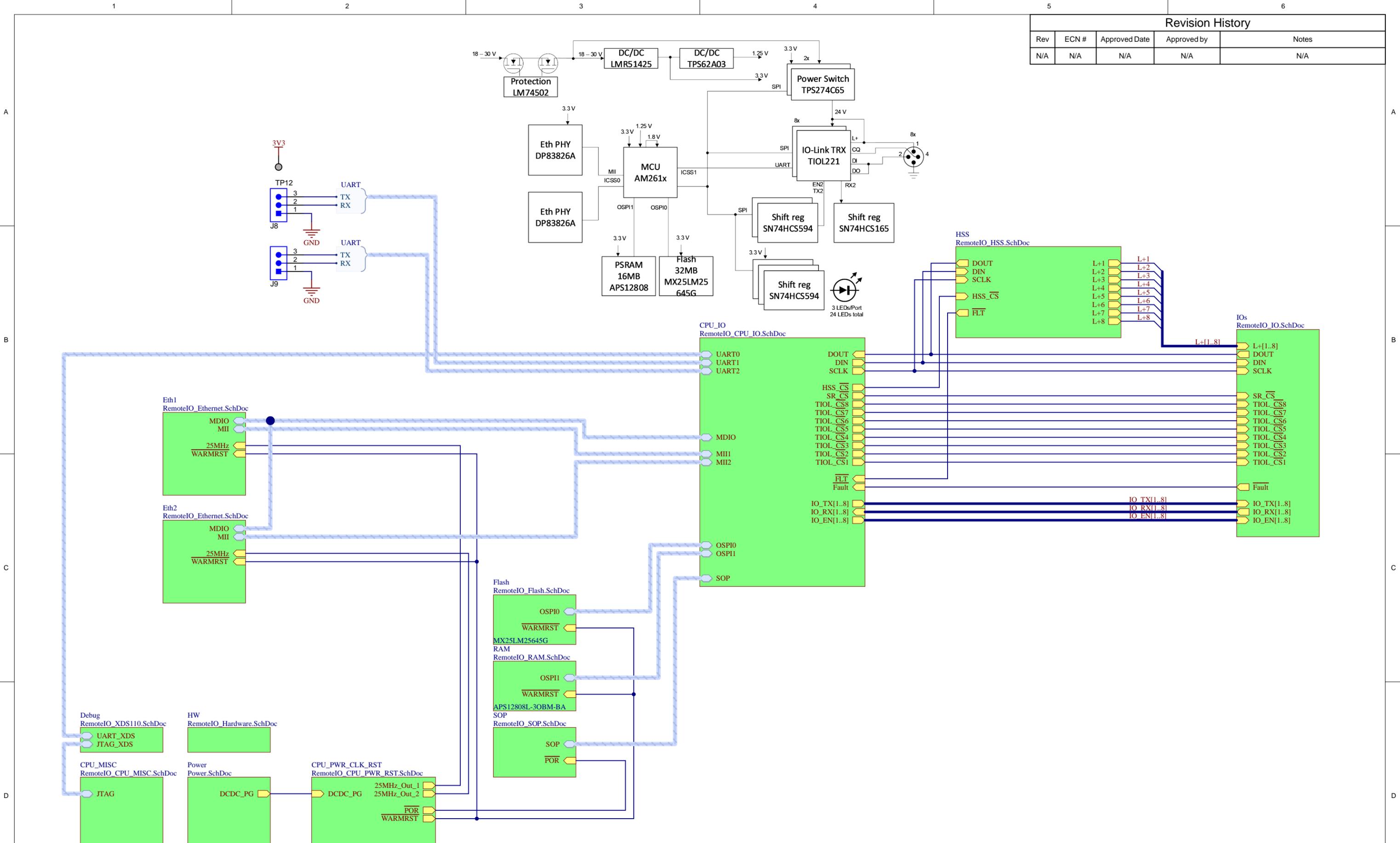


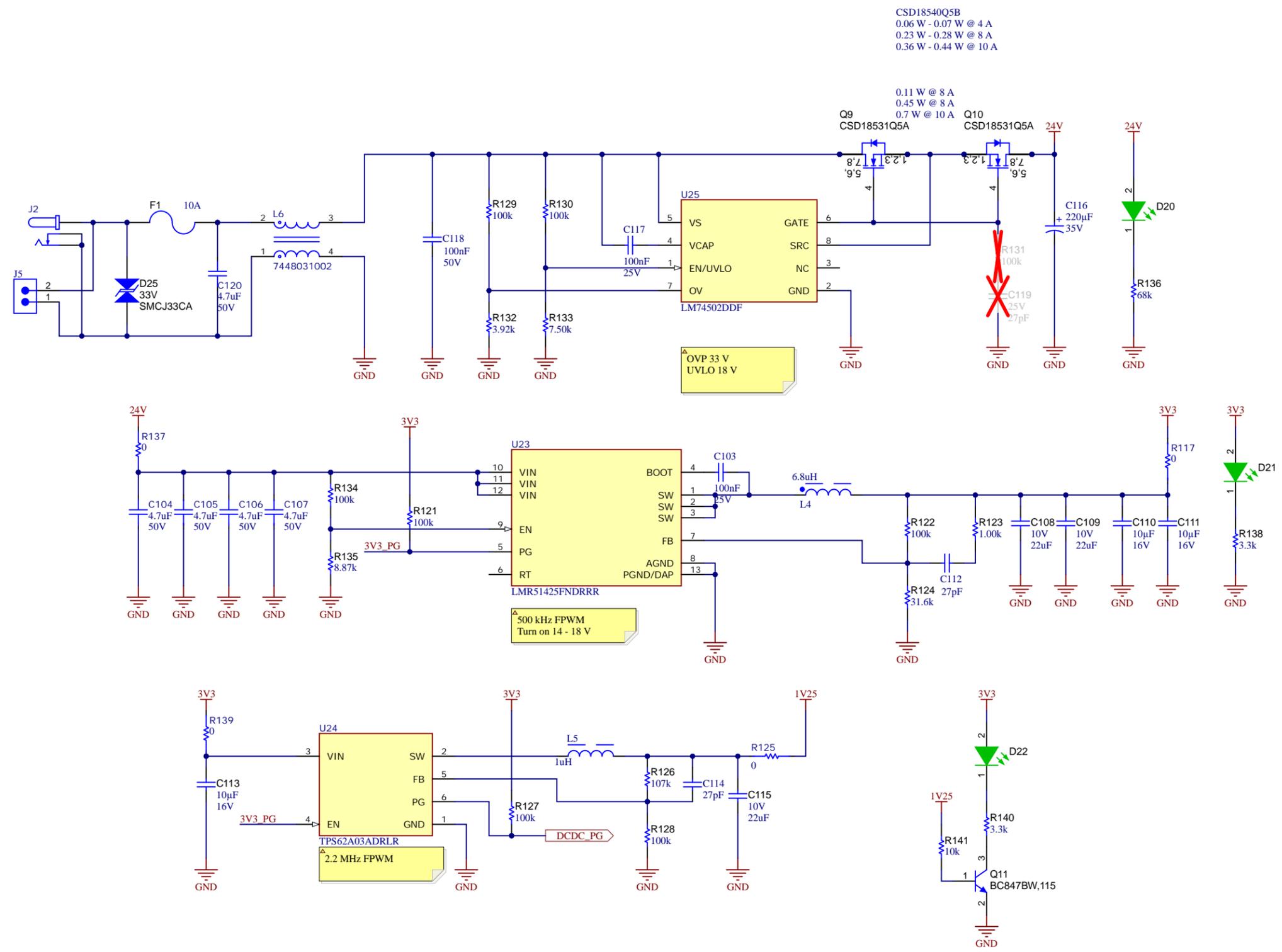
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A



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Orderable: NA	Designed for: Public Release	Mod. Date: 12/3/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143 [Locally Modified]	Assembly Variant: 001	Sheet: 1 of 14
Drawn By:	File: RemoteIO.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	





CSD18540Q5B  
 0.06 W - 0.07 W @ 4 A  
 0.23 W - 0.28 W @ 8 A  
 0.36 W - 0.44 W @ 10 A

0.11 W @ 8 A  
 0.45 W @ 8 A  
 0.7 W @ 10 A

OVP 33 V  
 UVLO 18 V

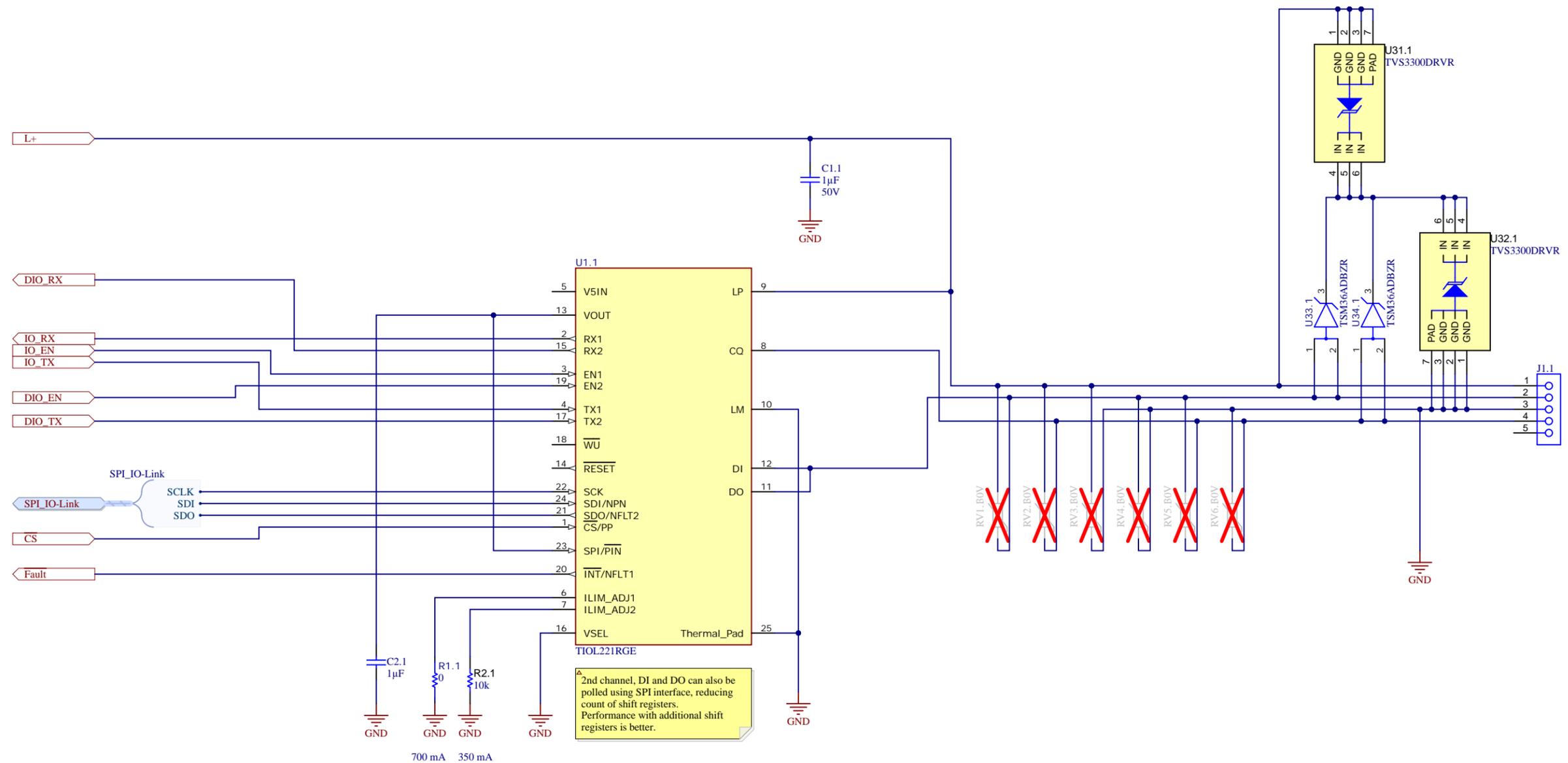
500 kHz FPWM  
 Turn on 14 - 18 V

2.2 MHz FPWM

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet: 2 of 14
SVN Rev:	Assembly Variant: 001	Size: B
Drawn By:	File: Power.SchDoc	
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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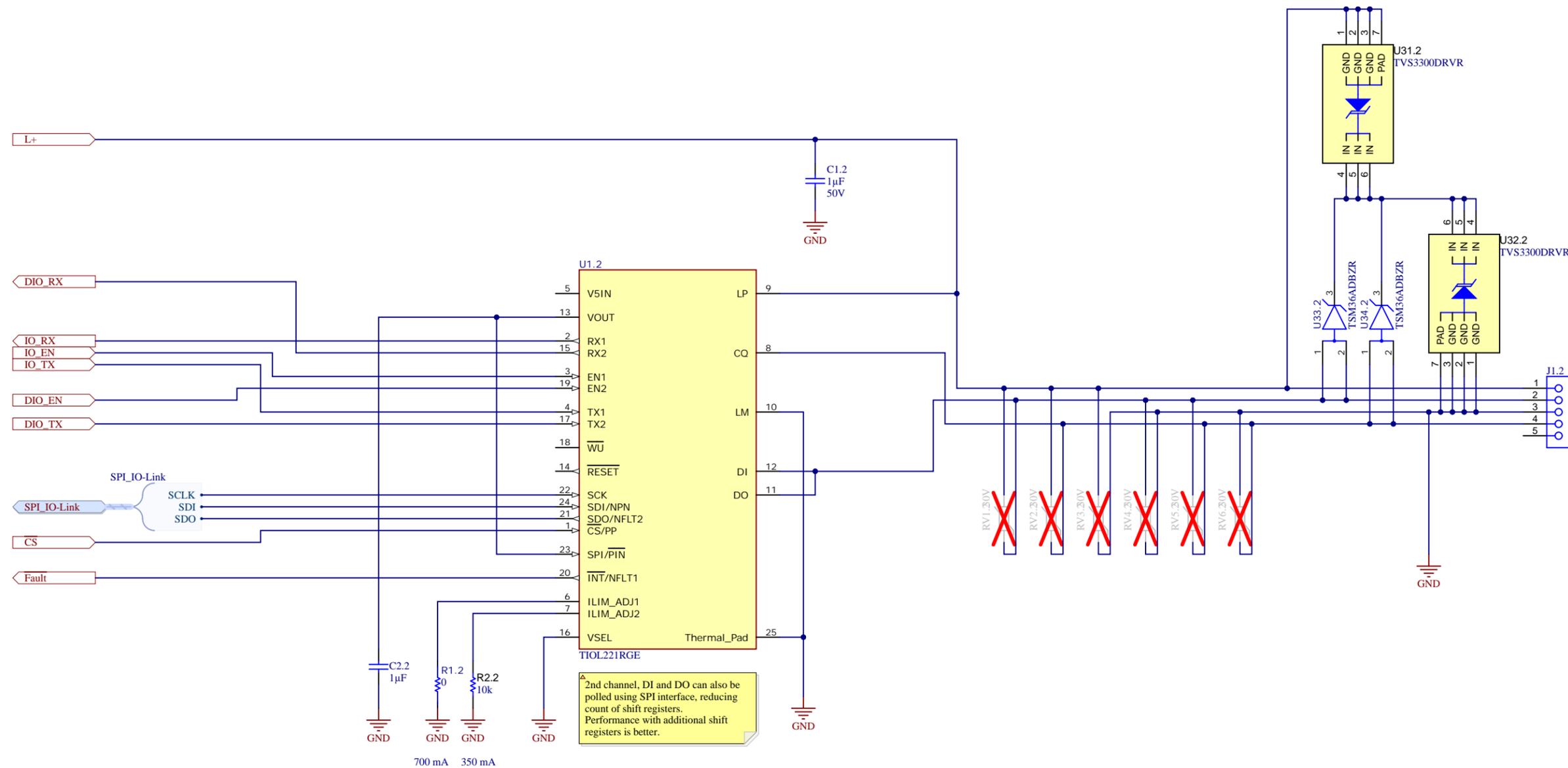


<sup>A</sup> 2nd channel, DI and DO can also be polled using SPI interface, reducing count of shift registers. Performance with additional shift registers is better.

Different protections to test			
Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025	
TID #: N/A	Project Title: AM261 Remote IO	Sheet: 3 of 14	
Number: TIDA-011002	Rev: E1	Assembly Variant: 001	Size: B
SVN Rev: 16143	File: RemoteIO_IOLinkPort.SchDoc	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	
Drawn By:	Engineer: Steffen Graf		

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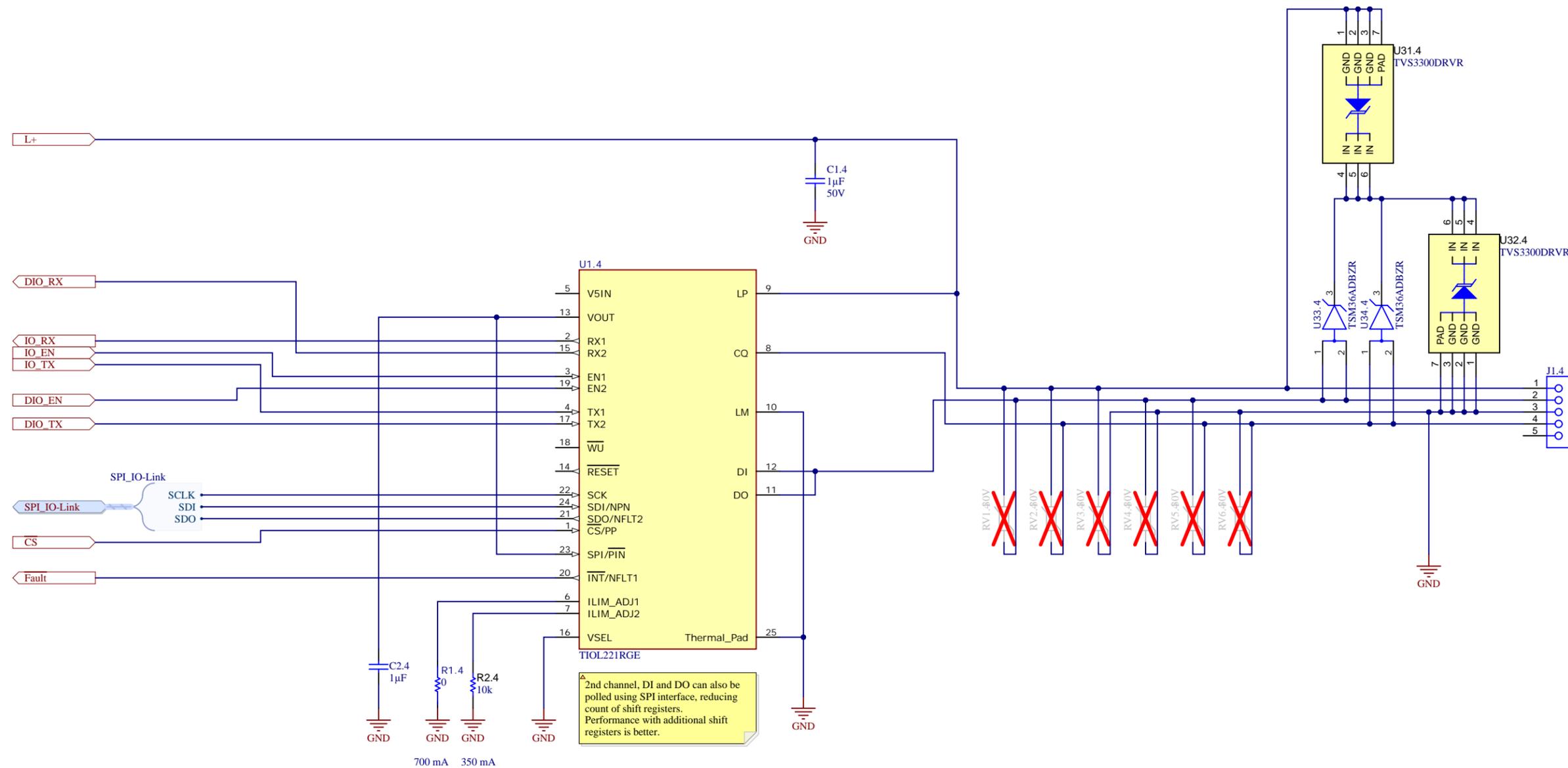
<sup>A</sup> 2nd channel, DI and DO can also be polled using SPI interface, reducing count of shift registers. Performance with additional shift registers is better.

Different protections to test

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 3 of 14
Drawn By:	File: RemoteIO_IOLinkPort.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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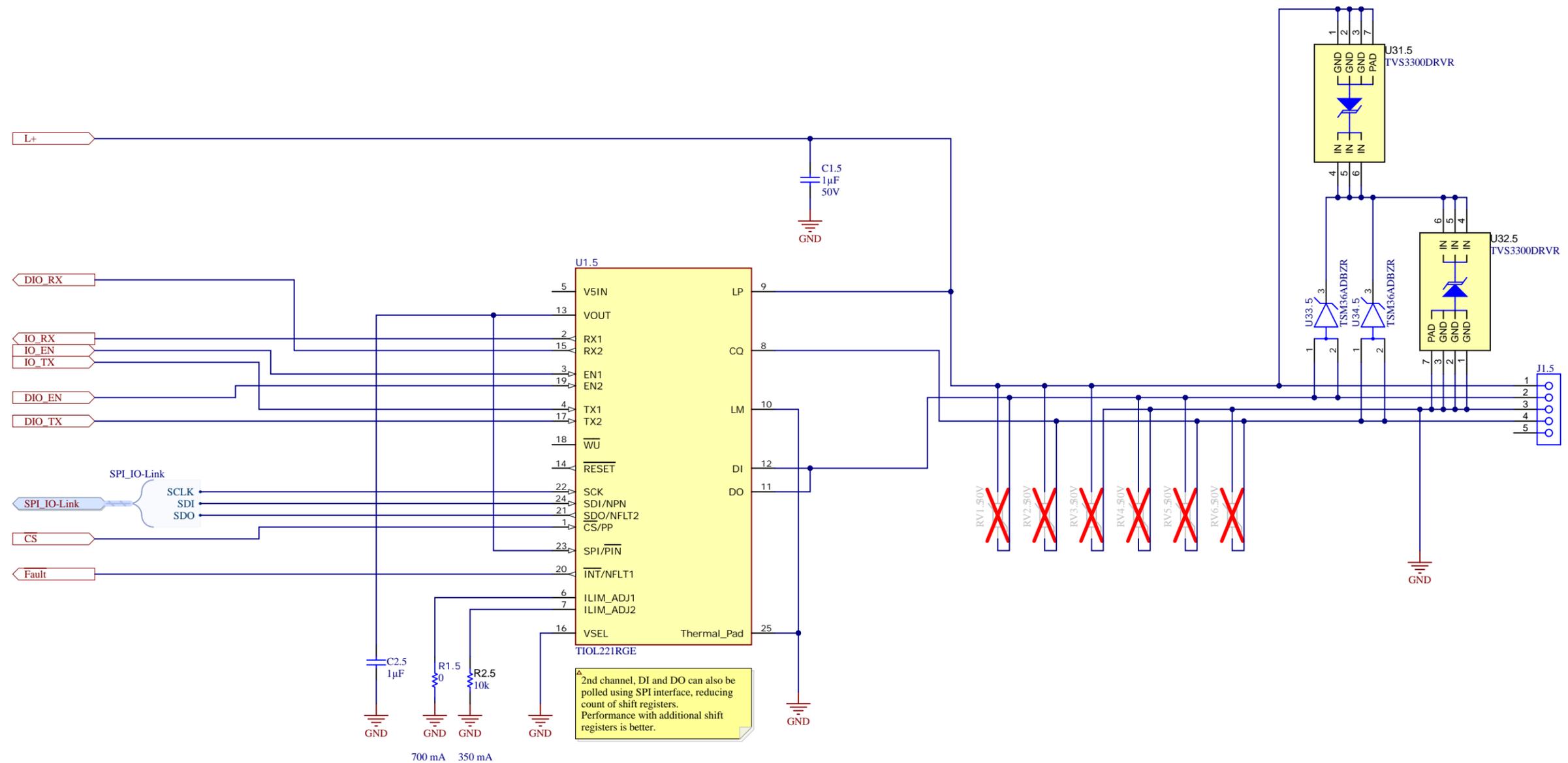


2nd channel, DI and DO can also be polled using SPI interface, reducing count of shift registers. Performance with additional shift registers is better.

Different protections to test

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 3 of 14
Drawn By:	File: RemoteIO_IOLinkPort.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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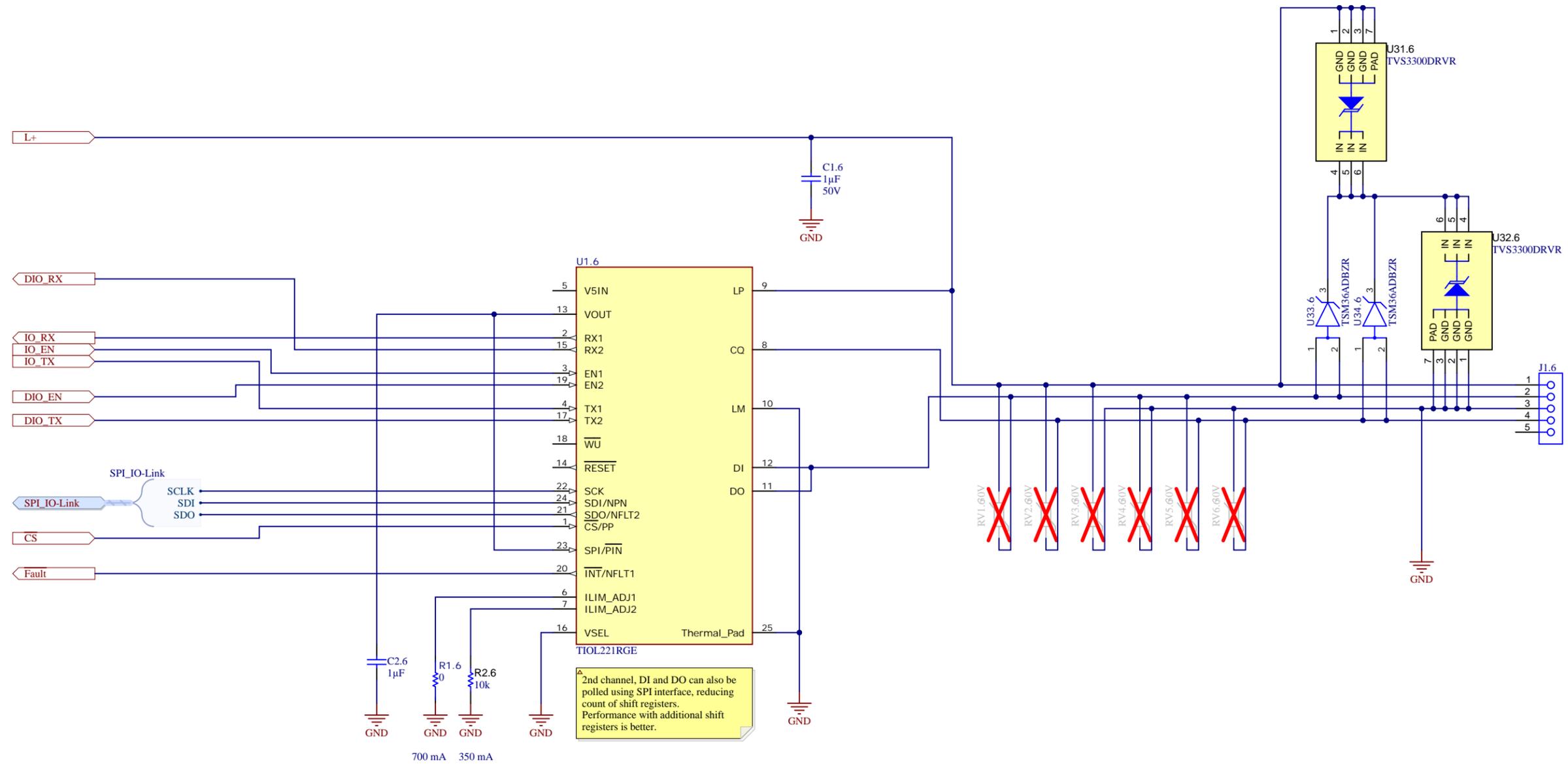
Different protections to test

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 3 of 14
Drawn By:	File: RemoteIO_IOLinkPort.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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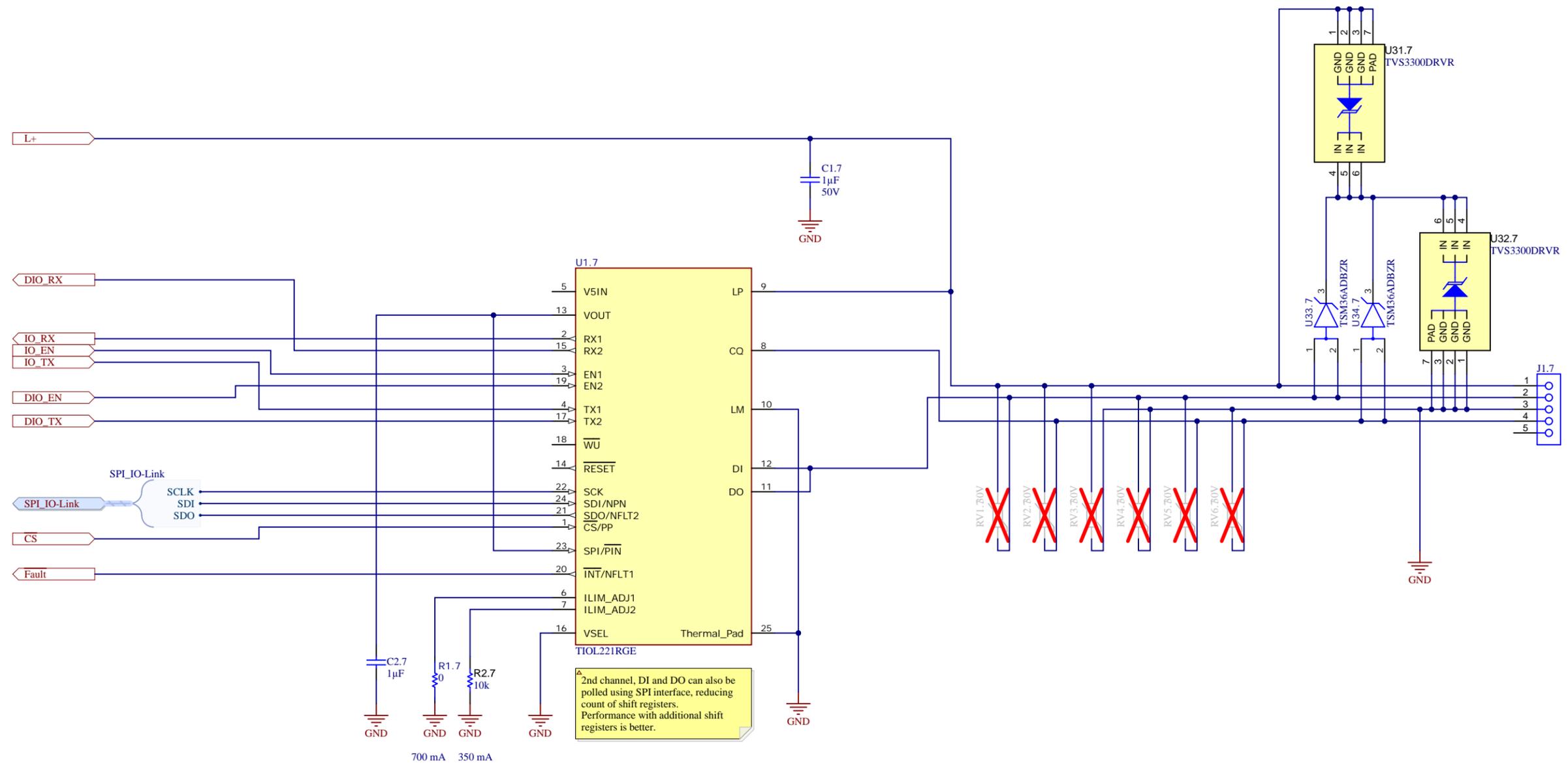


<sup>A</sup> 2nd channel, DI and DO can also be polled using SPI interface, reducing count of shift registers. Performance with additional shift registers is better.

Different protections to test

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 3 of 14
Drawn By:	File: RemotelIO_IOLinkPort.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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<sup>A</sup> 2nd channel, DI and DO can also be polled using SPI interface, reducing count of shift registers. Performance with additional shift registers is better.

700 mA 350 mA

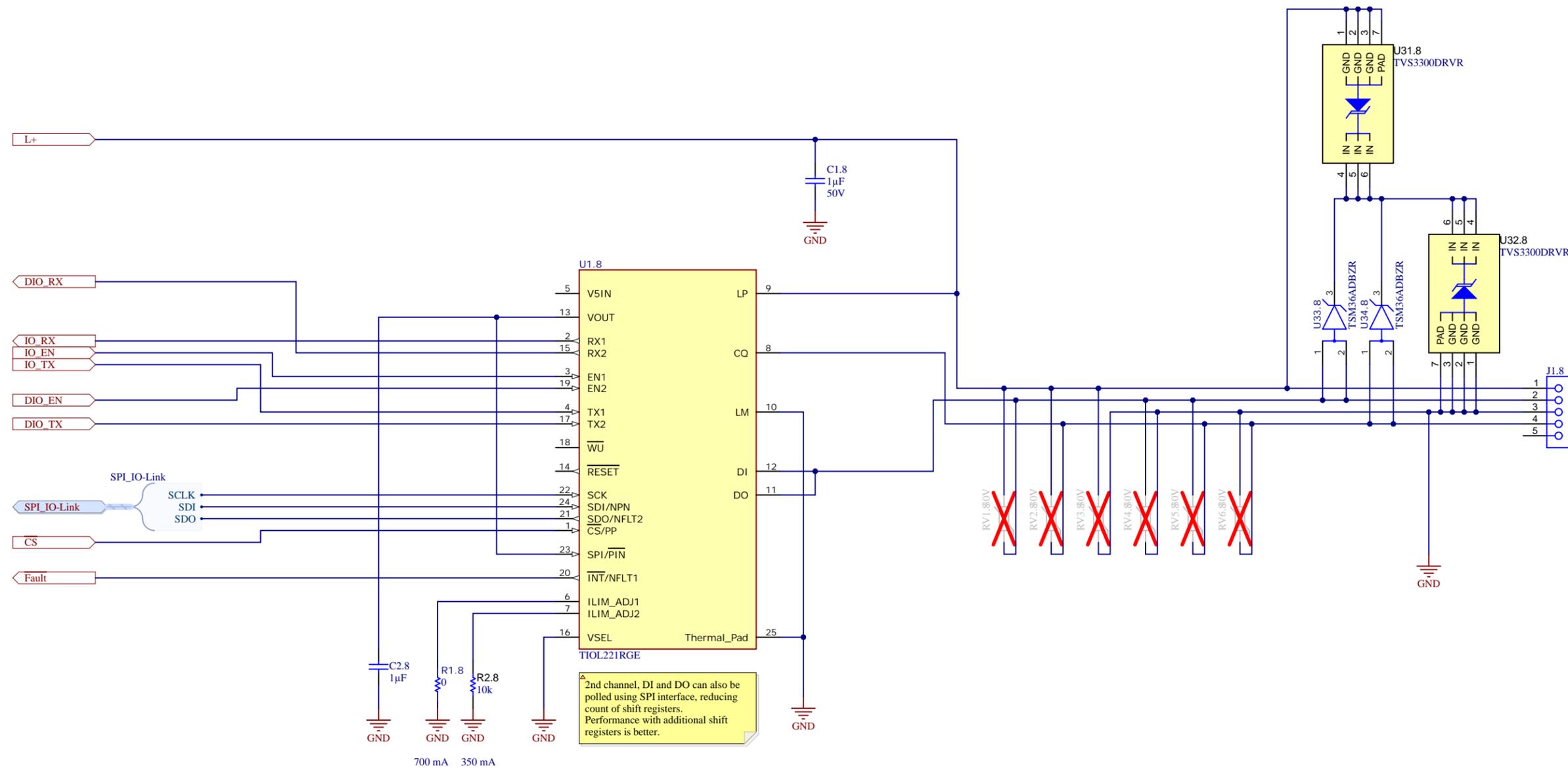
Different protections to test

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 3 of 14
Drawn By:	File: RemoteIO_IOLinkPort.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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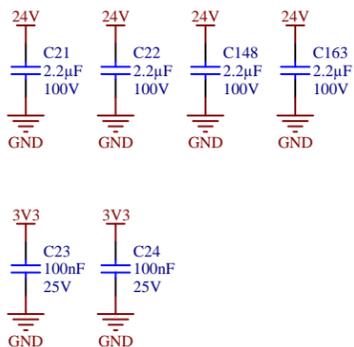
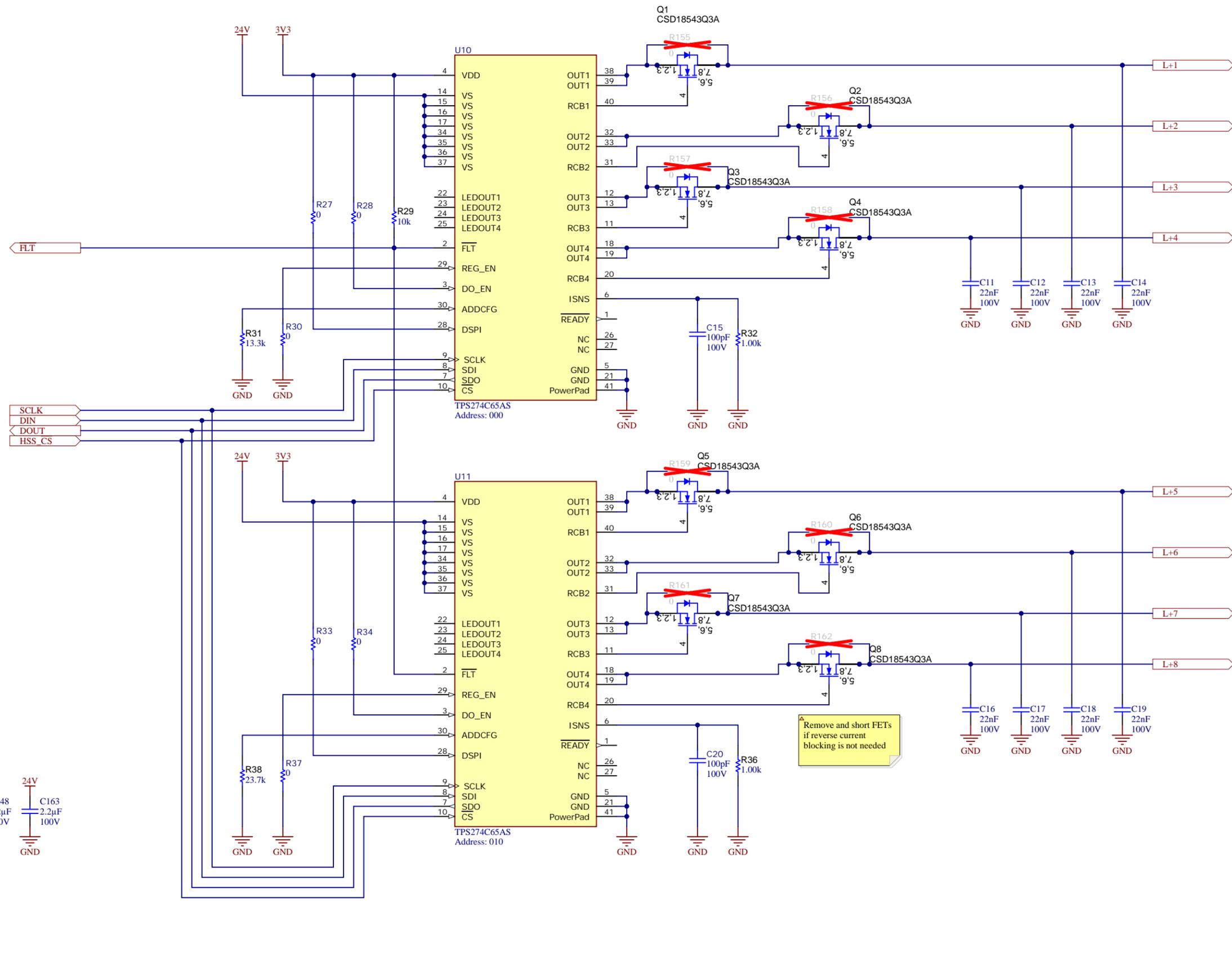
<sup>A</sup> 2nd channel, DI and DO can also be polled using SPI interface, reducing count of shift registers. Performance with additional shift registers is better.

Different protections to test

Orderable: NA	Designed for: Public Release	Mod. Date: 11/19/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 3 of 14
Drawn By:	File: RemoteIO_IOLinkPort.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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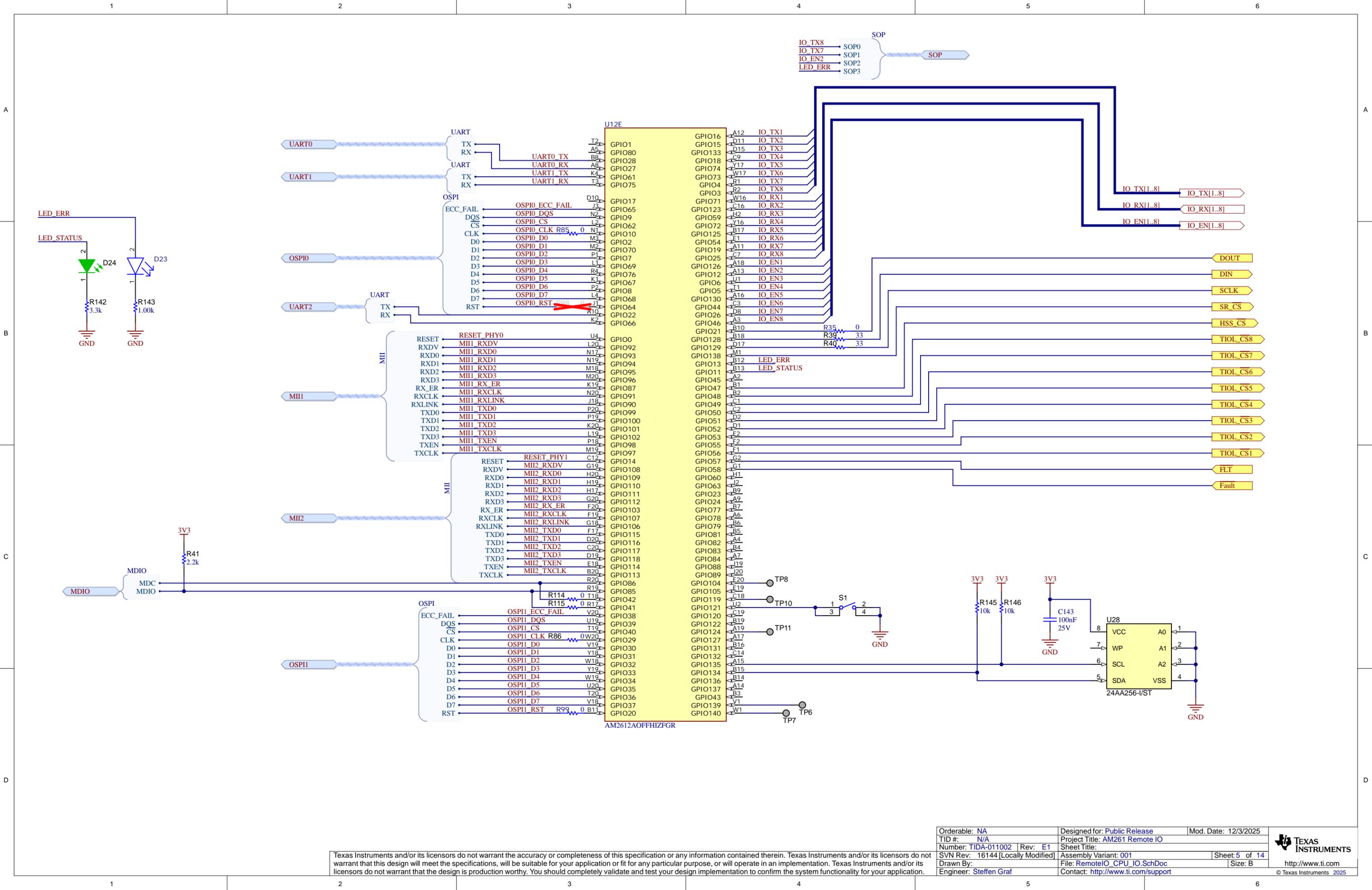
A Remove and short FETs if reverse current blocking is not needed

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TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet: 4 of 14
Drawn By:	File: RemoteIO_HSS.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



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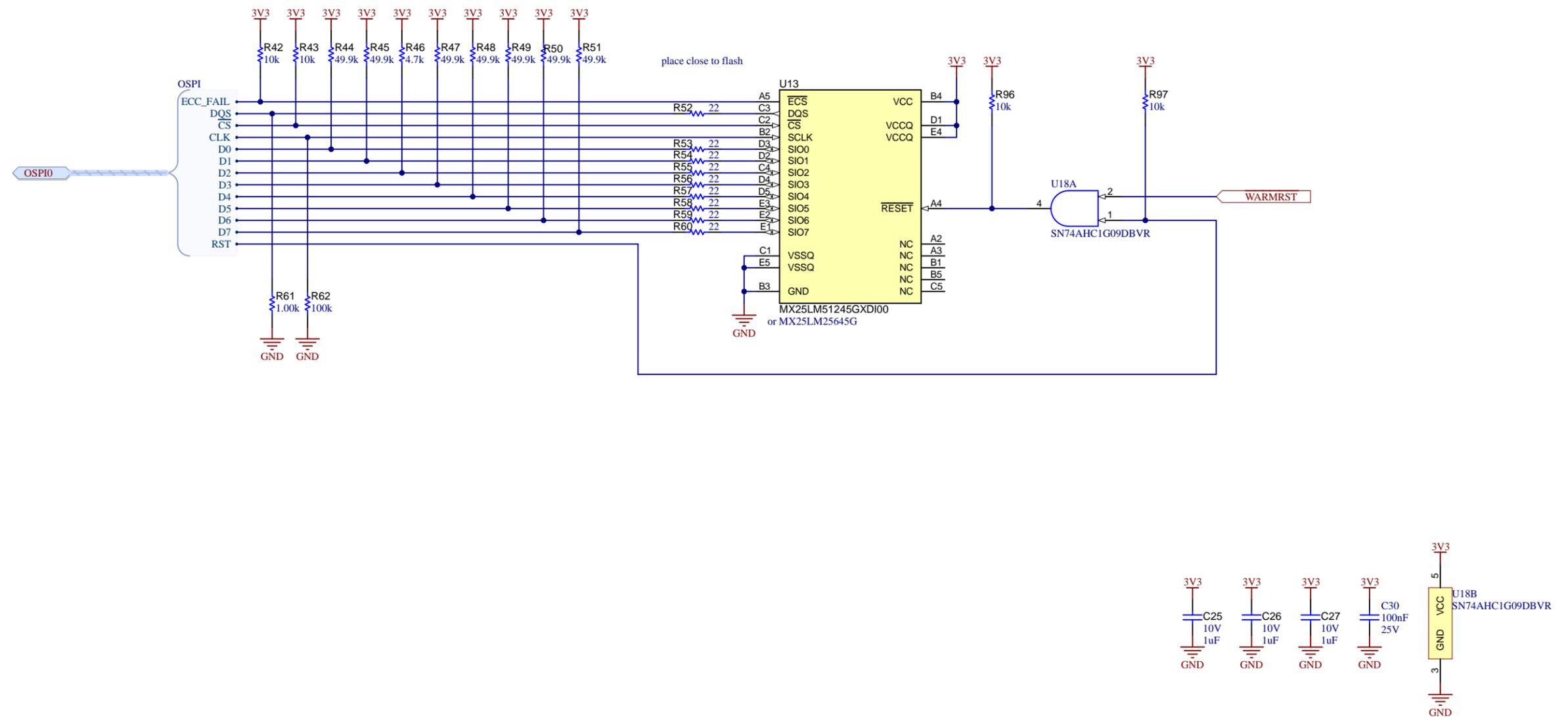


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TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16144 [Locally Modified]	Assembly Variant: 001	Sheet: 5 of 14
Drawn By:	File: RemoteIO_CPU_IO.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



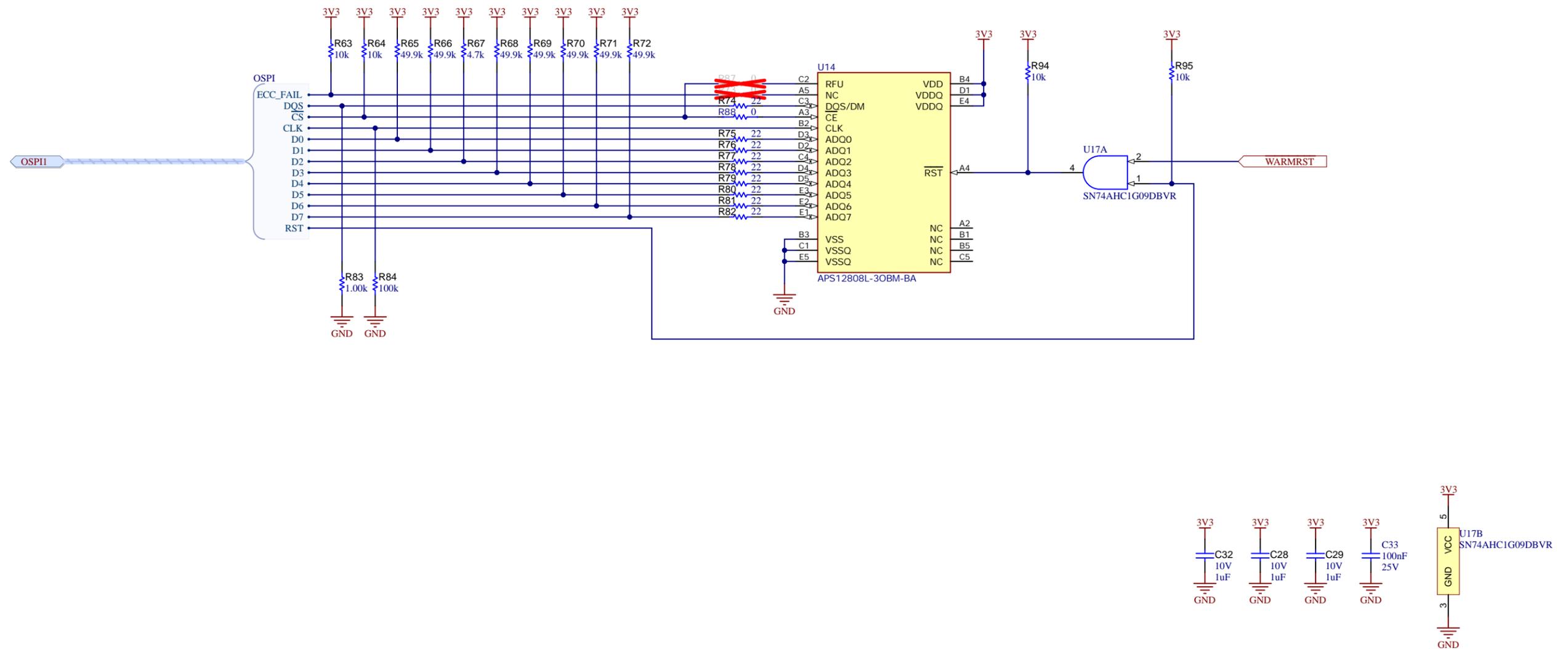
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TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev:	Assembly Variant: 001	Sheet: 6 of 14
Drawn By:	File: RemoteIO_Flash.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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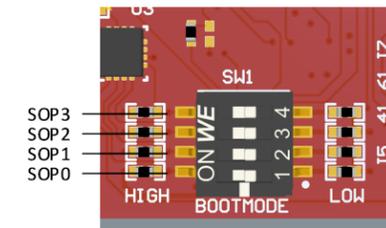
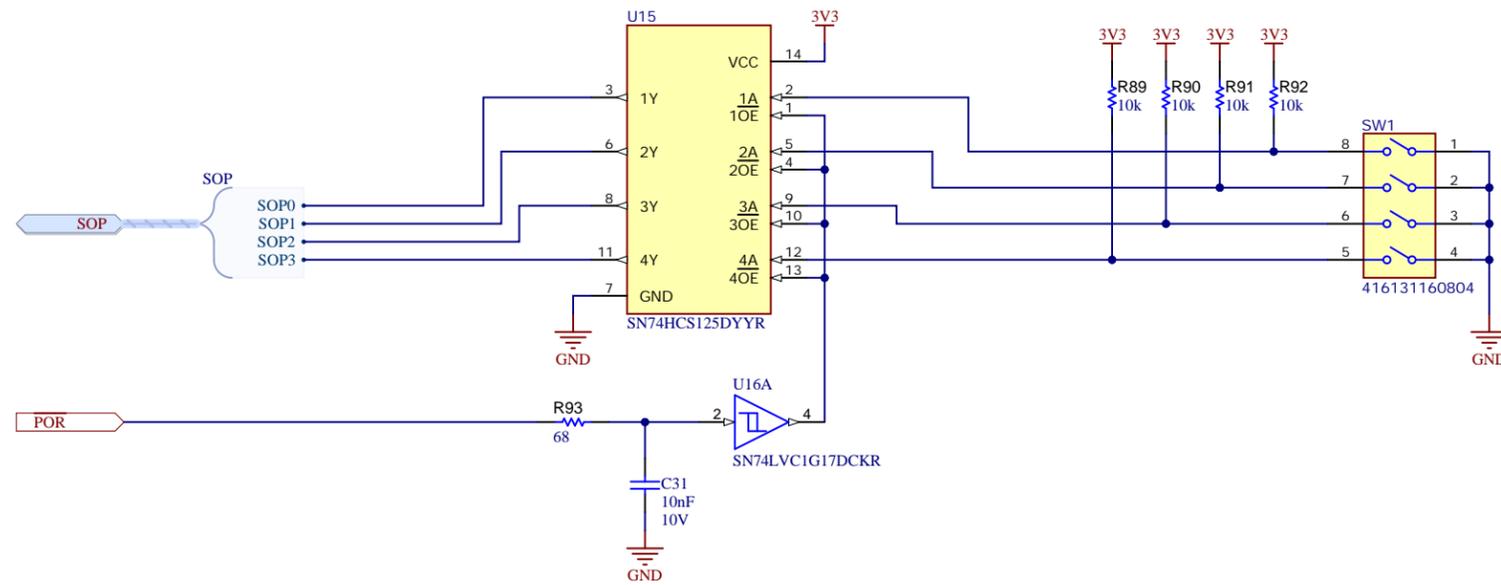




Orderable: NA	Designed for: Public Release	Mod. Date: 10/28/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev:	Assembly Variant: 001	Sheet: 7 of 14
Drawn By:	File: RemoteIO_RAM.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

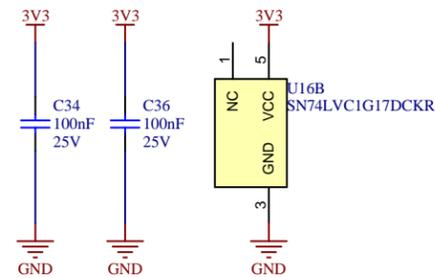
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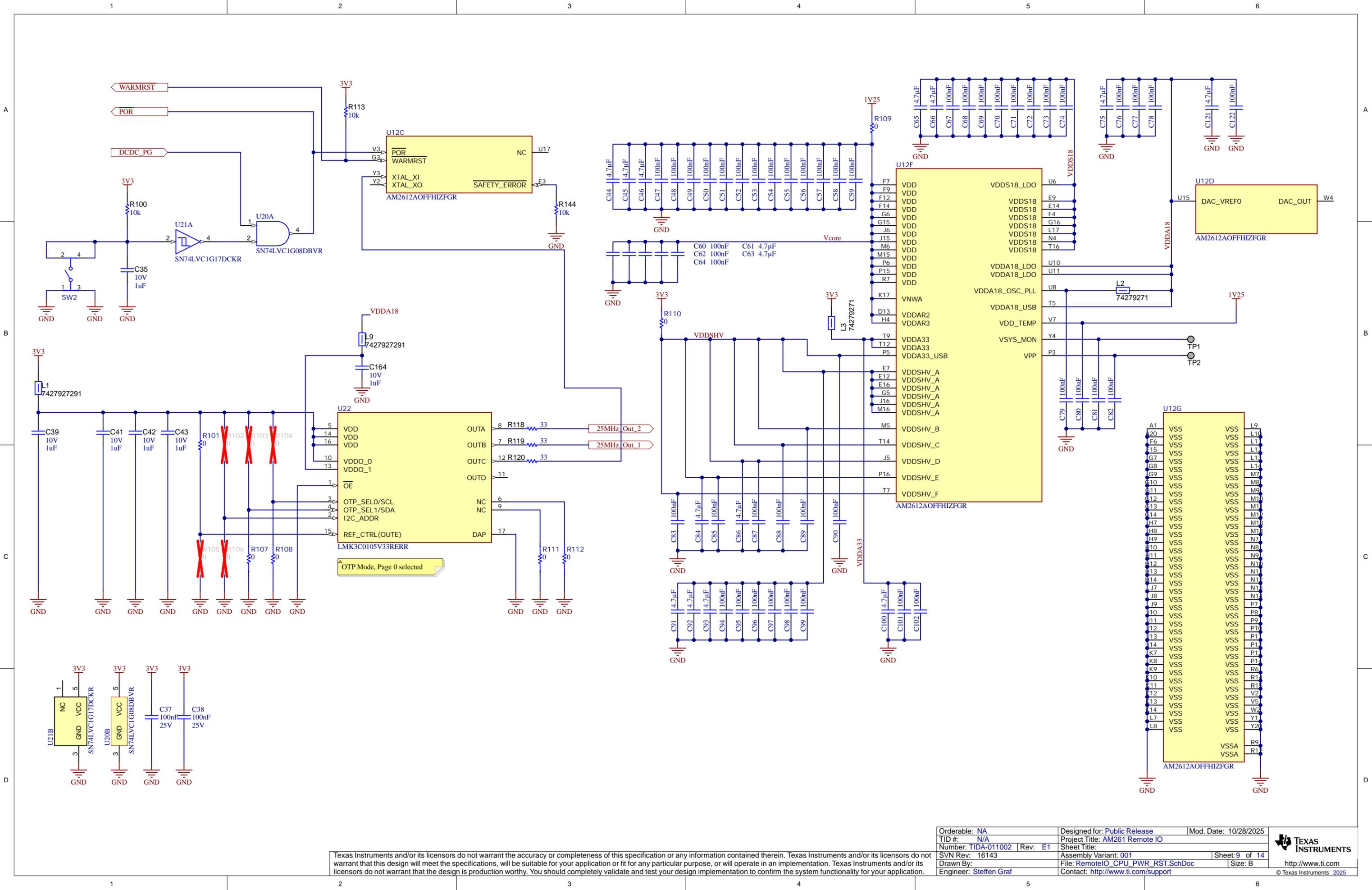
Switch Positions  
 Logic Low   
 Logic High

▲ SOP[3:0] switch description:  
 - Switch "off" SOPx high  
 - Switch "on" SOPx low  
 - SOP[3:0] = 0b0001 -> UART  
 - SOP[3:0] = 0b1100 -> OSP10(1S->8D)  
 - SOP[3:0] = 0b0011 -> OSP10(8S),SDR  
 - SOP[3:0] = 0b1011 -> DevBoot  
 - SOP[3:0] = 0b1110 -> USB2.0 DFU



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TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143 [Locally Modified]	Assembly Variant: 001	Sheet: 8 of 14
Drawn By:	File: Remotelo_SOP.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

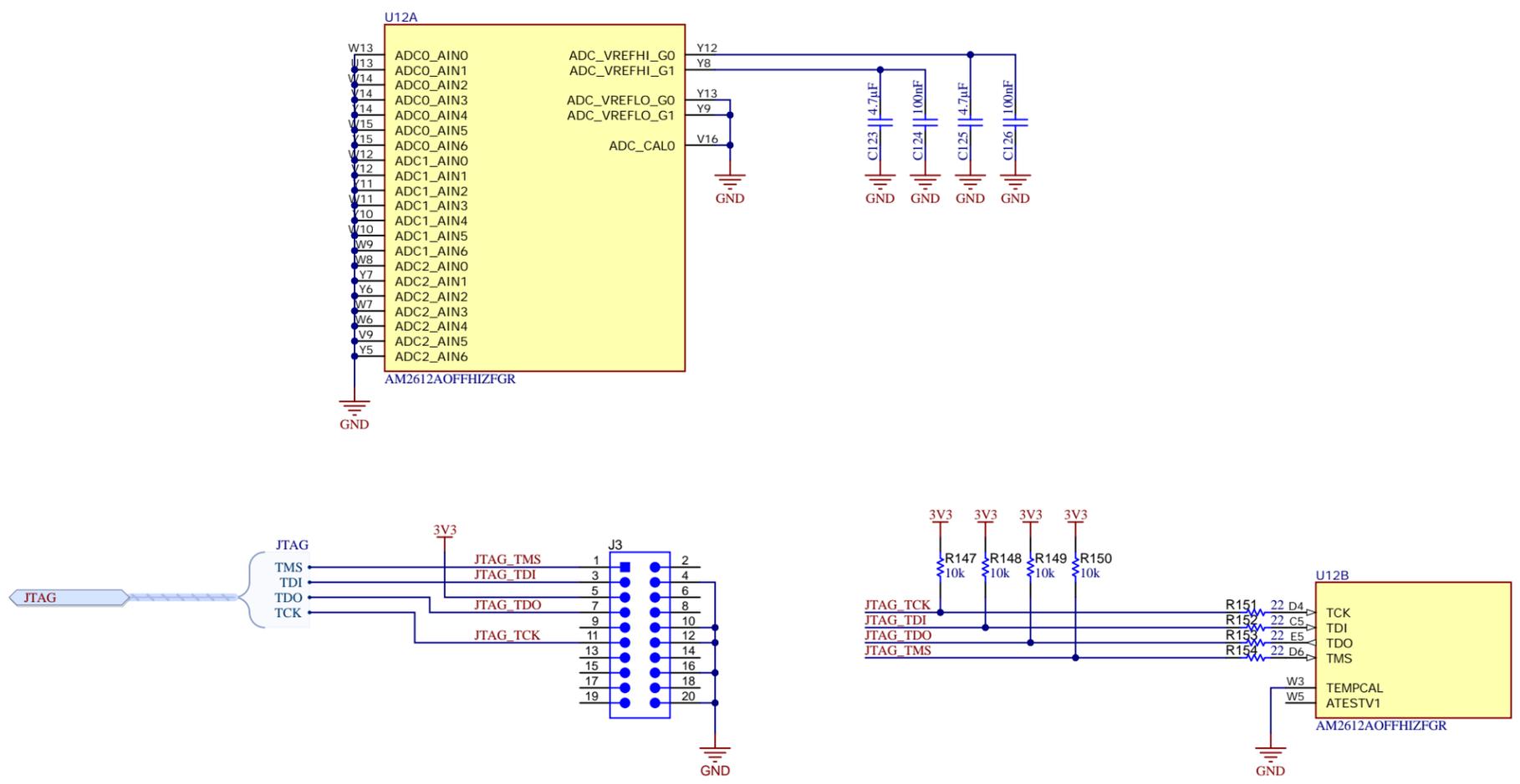


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TID #: N/A	Project Title: AM2612 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143	Assembly Variant: 001	Sheet: 9 of 14
Drawn By:	File: RemoteIO_CPU_PWR_RST.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



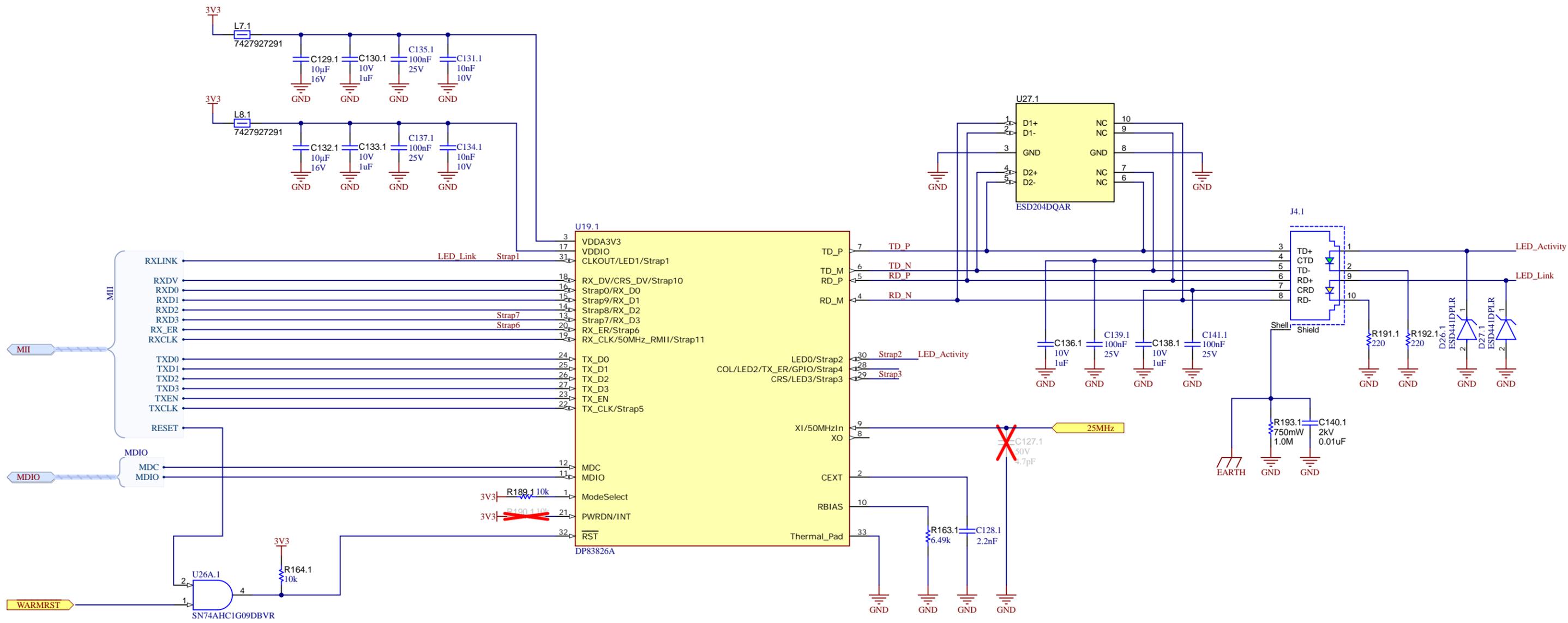
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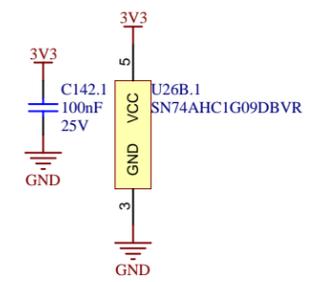
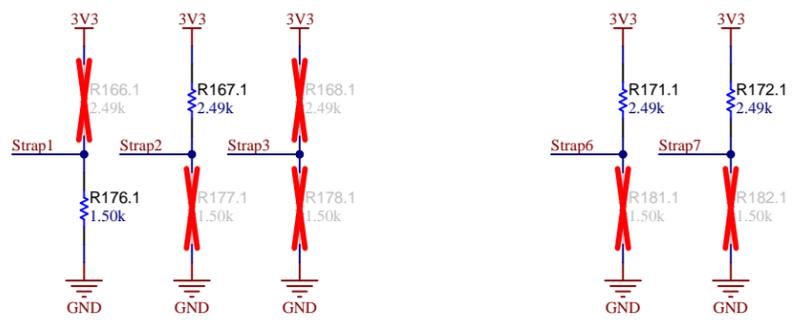
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TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet: 10 of 14
SVN Rev:	Assembly Variant: 001	Size: B
Drawn By:	File: RemoteIO_CPU_MISC.SchDoc	
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	





Odd nibble detection disabled  
 MII MAC  
 Fast Link Drop  
 auto MDIX  
 MDIO 1 and 3

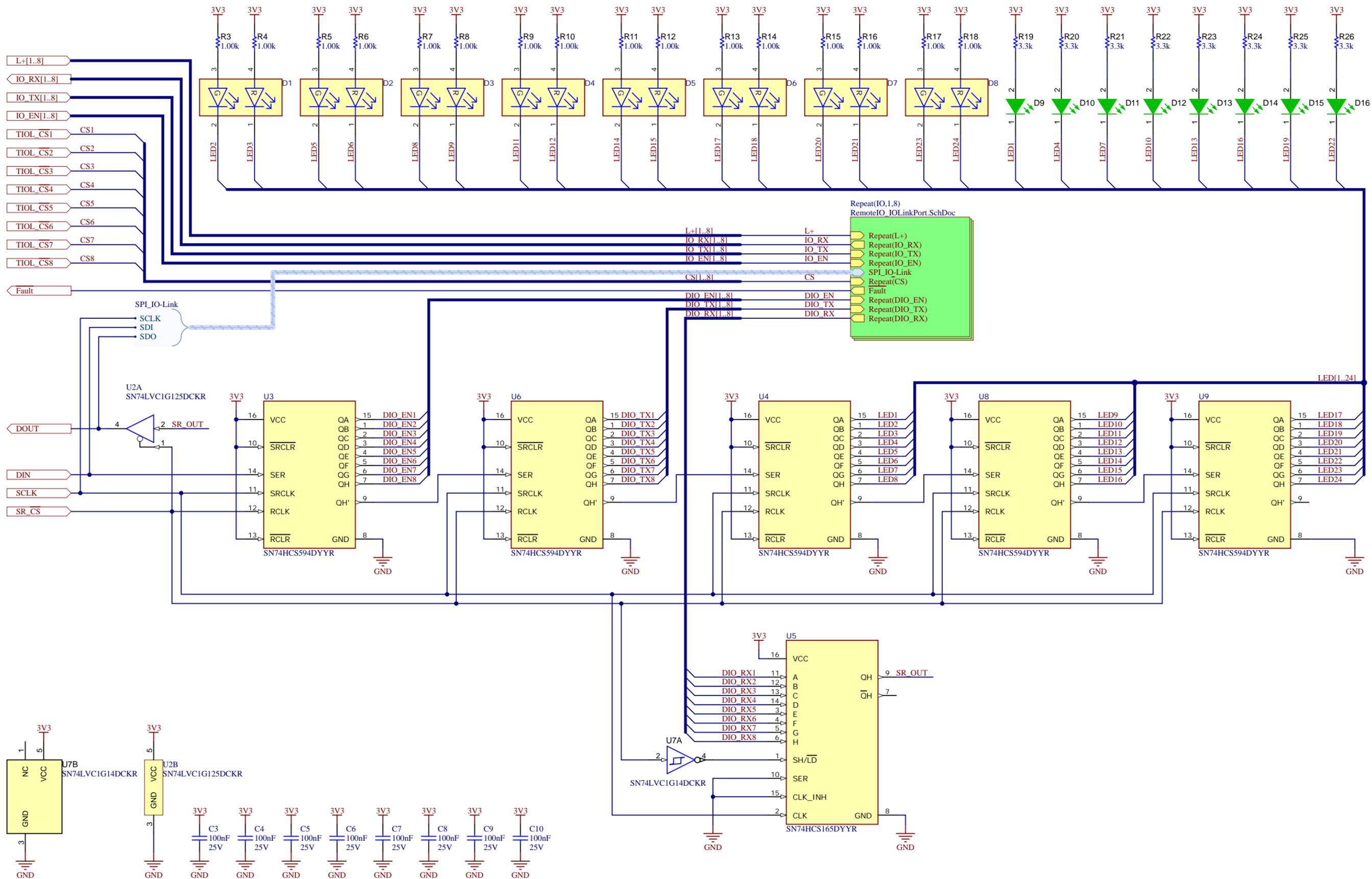


Orderable: NA	Designed for: Public Release	Mod. Date: 10/23/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
Drawn By:	Assembly Variant: 001	Sheet: 11 of 14
Engineer: Steffen Graf	File: RemotelIO_Ethernet.SchDoc	Size: B
	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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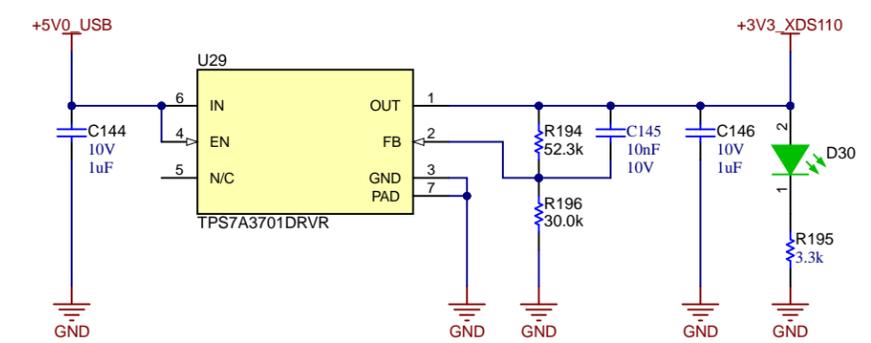
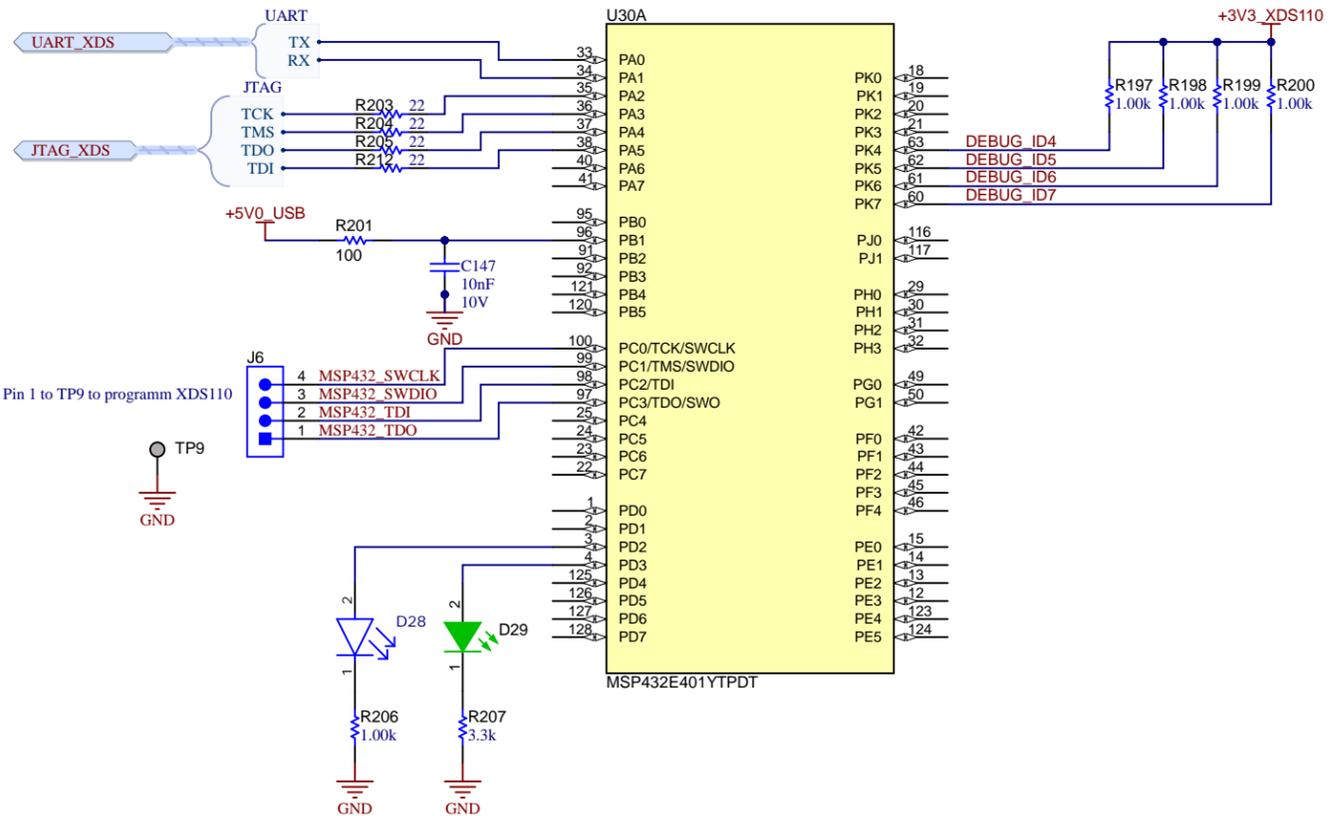




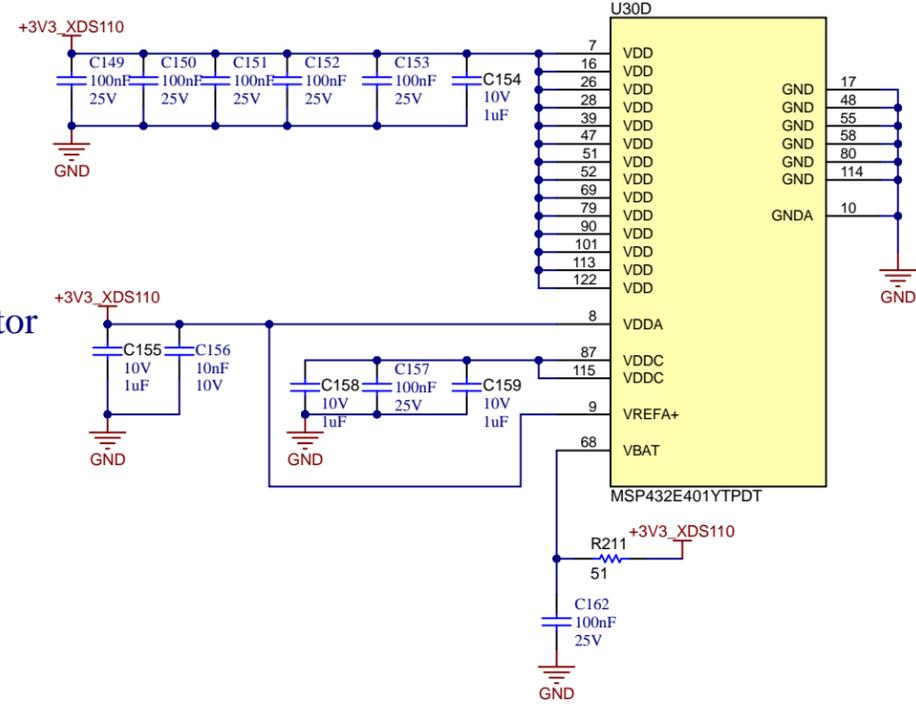
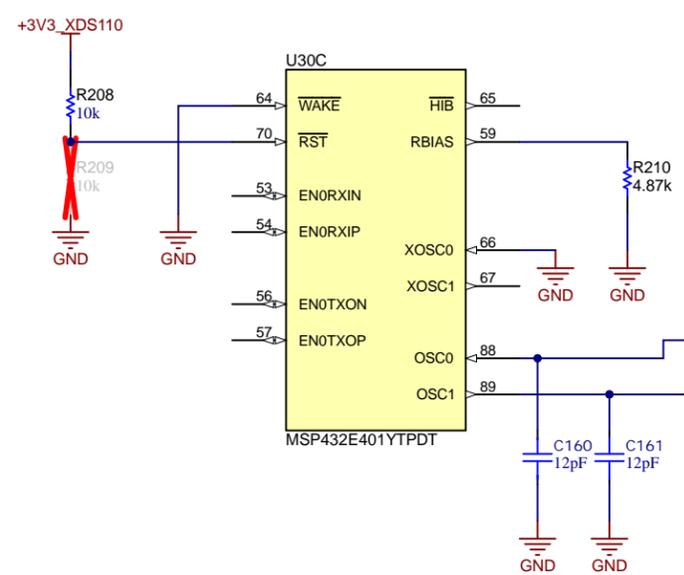
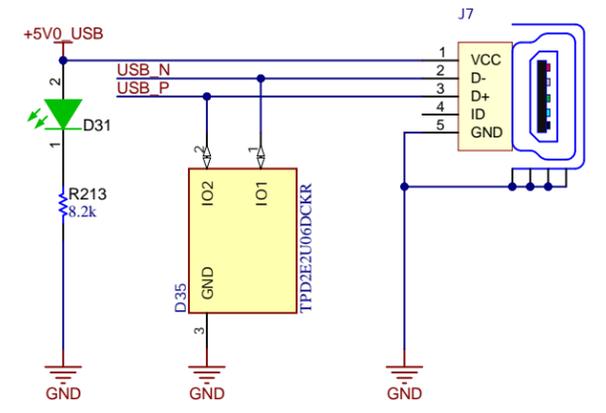
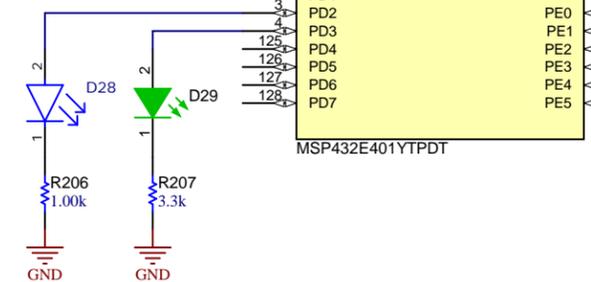
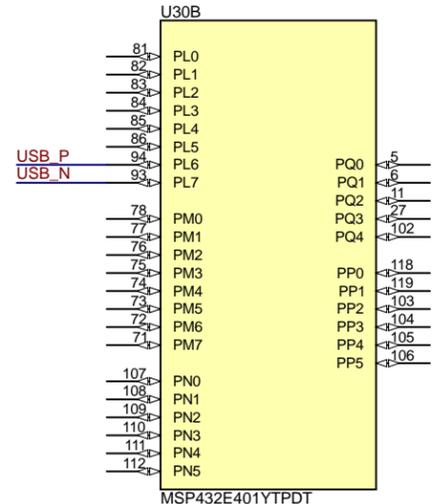
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Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev:	Assembly Variant: 001	Sheet: 12 of 14
Drawn By:	File: RemoteIO_IO.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

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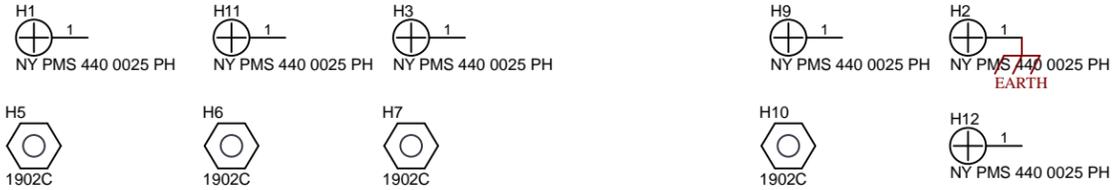
Short J6, Pin 1 to TP9 to programm XDS110



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TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet Title:
SVN Rev: 16143 [Locally Modified]	Assembly Variant: 001	Sheet: 13 of 14
Drawn By:	File: RemoteIO_XDS110.SchDoc	Size: B
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	





PCB Number: TIDA-011002  
PCB Rev: E1

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WEEE logo

Variant/Label Table	
Variant	Label Text
001	Default

LBL1  
PCB Label  
THT-14-423-10  
Size: 0.65" x 0.20 "

ZZ1  
Label Assembly Note  
This Assembly Note is for PCB labels only

ZZ2  
Assembly Note  
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3  
Assembly Note  
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4  
Assembly Note  
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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Orderable: NA	Designed for: Public Release	Mod. Date: 11/27/2025
TID #: N/A	Project Title: AM261 Remote IO	
Number: TIDA-011002	Rev: E1	Sheet: 14 of 14
SVN Rev: 16144 [No modification]	Assembly Variant: 001	Size: B
Drawn By: Steffen Graf	File: RemoteIO_Hardware.SchDoc	
Engineer: Steffen Graf	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



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