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## Trademarks

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## 1 Introduction

The LM2734 demo board is configured to convert 5-V input to 1.8-V output at 1-A load current using the LM2734X 1.6-MHz or the LM2734Y 550-kHz step down DC-DC regulator. The tiny low profile thin SOT23 package allows the demo board to be manufactured using less than 1 square inch of a 4-layer printed circuit board.

The circuit is configured with the boost diode connected to  $V_{IN}$ , and according to the data sheet,  $V_{IN}$  must not exceed the maximum operating limit of  $5.5\text{ V} + V_{fD2}$  using this configuration. This makes sure that the voltage between the Boost and SW pins,  $V_{BOOST} - V_{SW}$ , does not exceed 5.5 V for proper operation. Please see the LM2734 data sheet for more information regarding this requirement.

A bill of materials in [Table 2-1](#) and [Table 2-2](#) describes the parts used on this demo board. A schematic and layout have also been included in [Figure 2-1](#) along with measured performance characteristics. The schematics at the end of this document show how to re-configure this demo board for various input and output conditions as discussed in the LM2734 data sheet. Short or leave open the indicated connection as indicated in the schematics. The above restrictions for the input voltage are valid only for the demo board as shipped with the demo board schematic below.

## 2 Operating Conditions

- $V_{IN} = 5 \text{ V}$
- $V_O = 1.8 \text{ V}$
- $I_O = 1 \text{ A}$

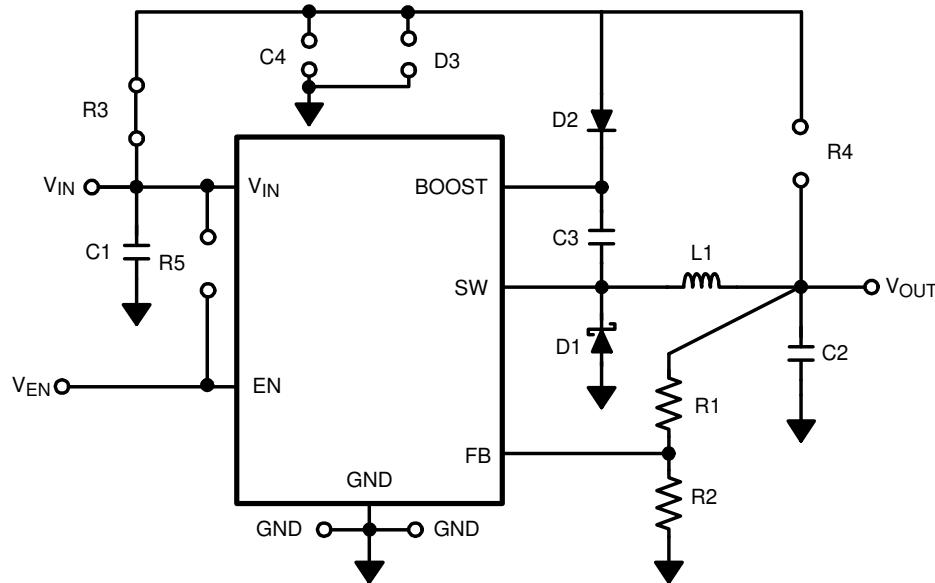


Figure 2-1. LM2734 Demo Board Schematic

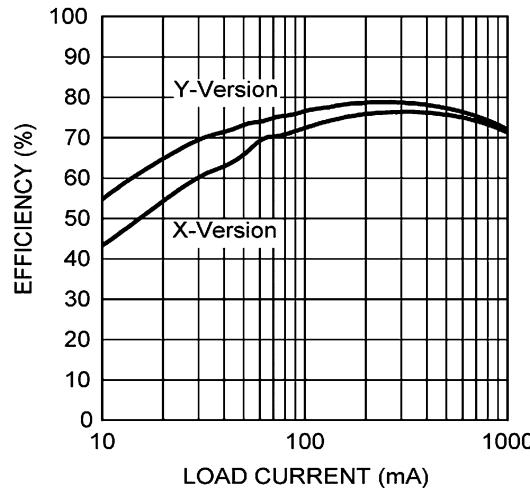


Figure 2-2. Efficiency vs Load Current

Table 2-1. Bill of Materials X-Version

Part ID	Part Value	Manufacturer	Part Number	Package Type
C1, Input Cap	4.7 $\mu\text{F}$ , 10 V, X5R	Murata	GRM42-6X5R475K10	1206
C2, Output Cap	10 $\mu\text{F}$ , 6.3 V, X5R	Murata	GRM42-6X5R106K6.3	1206
C3, Boost Cap	0.01 $\mu\text{F}$	Vishay	VJ0805Y103KXXA	0805
D2, Boost Diode	1 V <sub>f</sub> at 50-mA Diode	Diodes, Inc.	1N4148W	SOD-123
R2	10 k $\Omega$ , 1%	Vishay	CRCW12061002F	1206
U1	1-A Buck Regulator	Texas Instruments	LM2734	Thin SOT23-6
D1, Catch Diode	0.34-V <sub>f</sub> Schottky, 1 A, 20VR	International Rectifier	MBRA120	SMA
L1	2.7 $\mu\text{H}$ , 1.8 A, 22 m $\Omega$	TDK	SLF6028T-2R7M1R8	6028
R1	12.4 k $\Omega$ , 1%	Vishay	CRCW12061242F	1206

**Table 2-1. Bill of Materials X-Version (continued)**

Part ID	Part Value	Manufacturer	Part Number	Package Type
R3	0 $\Omega$	Vishay	CRCW12060R00F	1206
D3, C4, R4, R5	Open			

**Table 2-2. Bill of Materials Y-Version**

Part ID	Part Value	Manufacturer	Part Number	Package Type
C1, Input Cap	10 $\mu$ F, 10 V, X5R	Murata	GRM42-6X5R106K10	1206
C2, Output Cap	10 $\mu$ F, 6.3 V, X5R	Murata	GRM42-6X5R106K6.3	1206
C3, Boost Cap	0.01 $\mu$ F	Vishay	VJ0805Y103KXXA	0805
D2, Boost Diode	1 Vf at 50-mA Diode	Diodes, Inc.	1N4148W	SOD-123
R2	10 k $\Omega$ , 1%	Vishay	CRCW12061002F	1206
U1	1-A Buck Regulator	Texas Instruments	LM2734	Thin SOT23-6
D1, Catch Diode	0.34-Vf Schottky, 1 A, 20VR	International Rectifier	MBRA120	SMA
L1	6.8 $\mu$ H, 1.5 A, 35 m $\Omega$	TDK	SLF6028T-6R8M1R5	6028
R1	12.4 k $\Omega$ , 1%	Vishay	CRCW12061242F	1206
R3	0 $\Omega$	Vishay	CRCW12060R00F	1206
D3, C4, R4, R5	Open			

### 3 PCB Layout

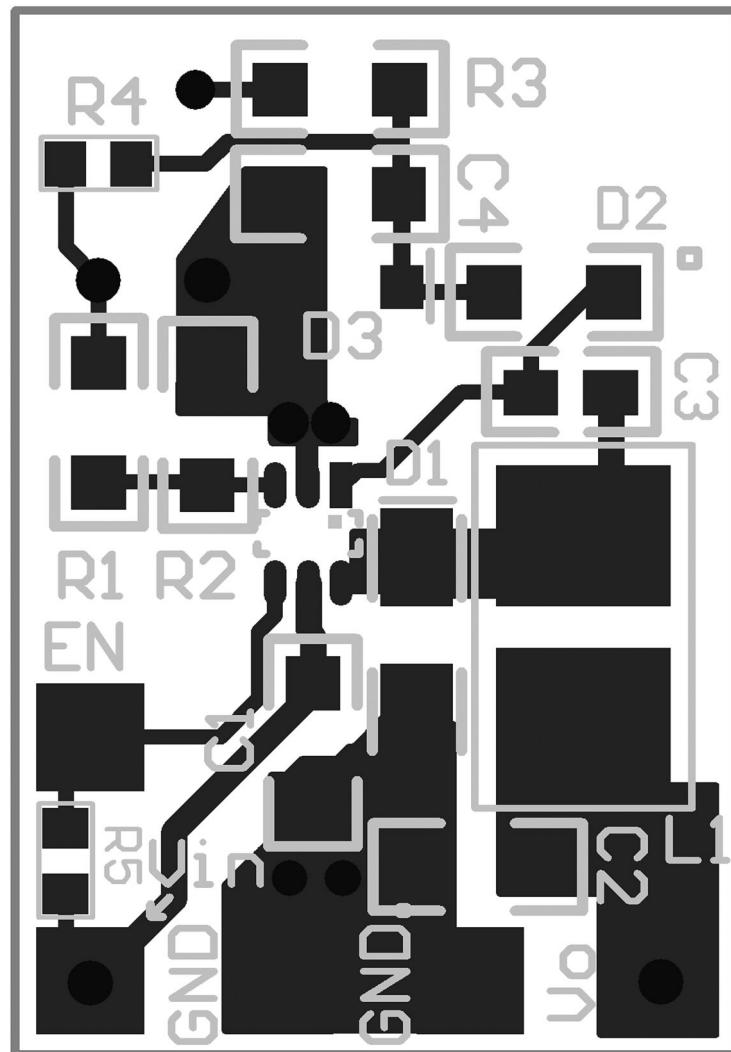
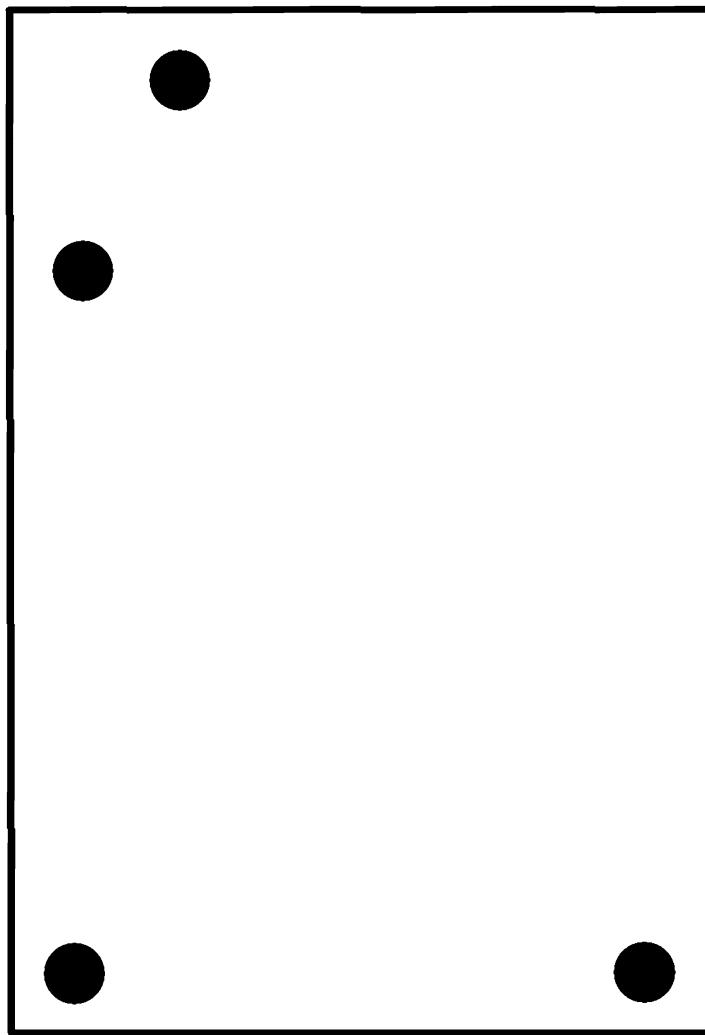
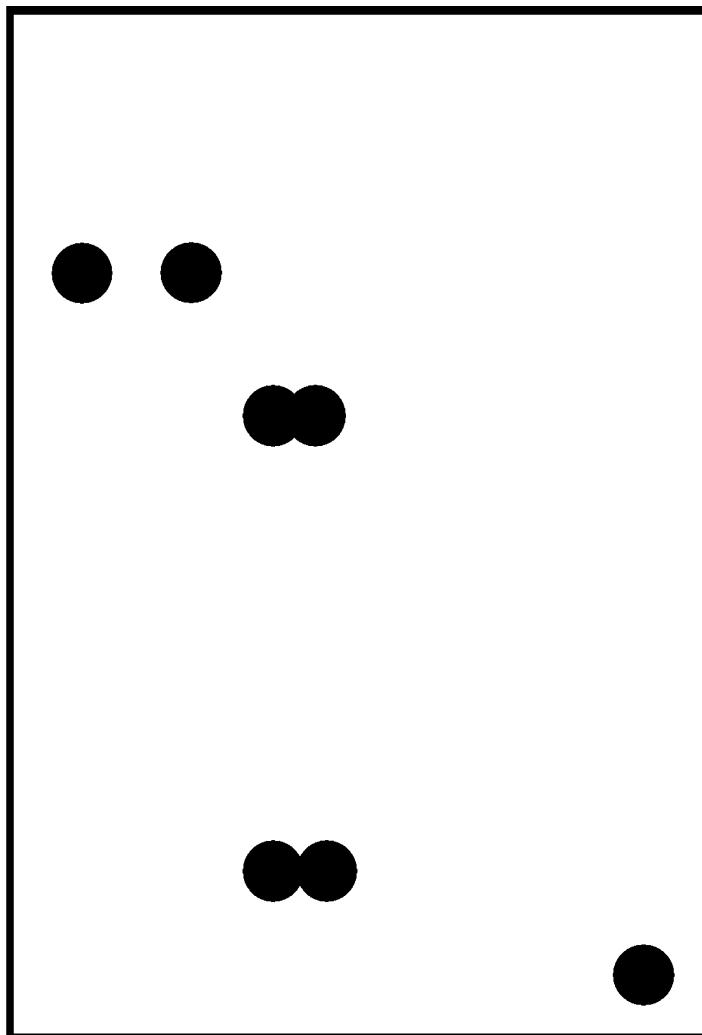


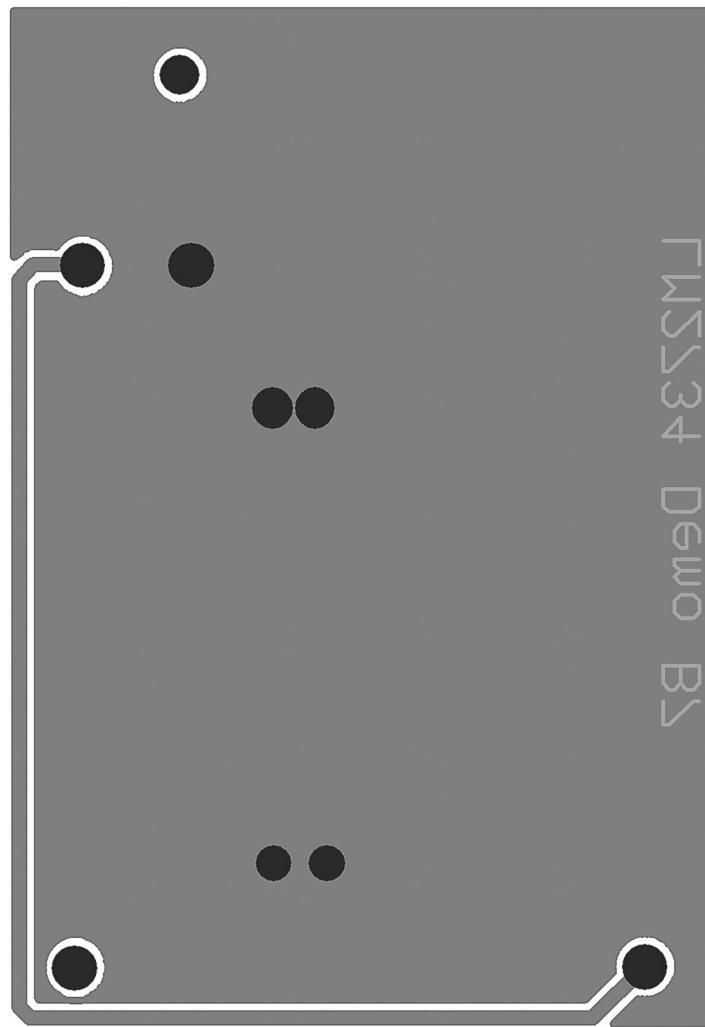
Figure 3-1. Top Layer



**Figure 3-2. Internal Plane 1 (GND)**



**Figure 3-3. Internal Plane 2 ( $V_{IN}$ )**



**Figure 3-4. Bottom Layer**

## 4 Additional Circuit Configuration Schematics

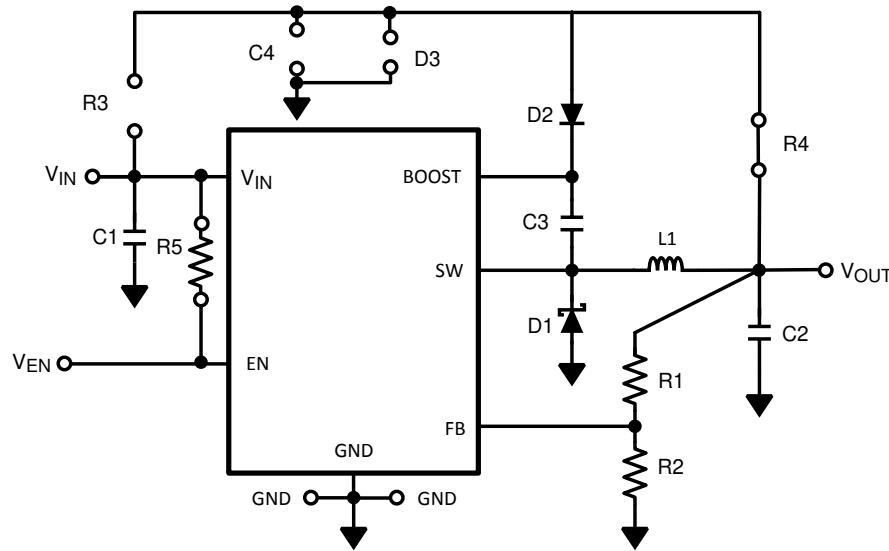


Figure 4-1.  $V_{BOOST}$  Derived from  $V_{OUT}$

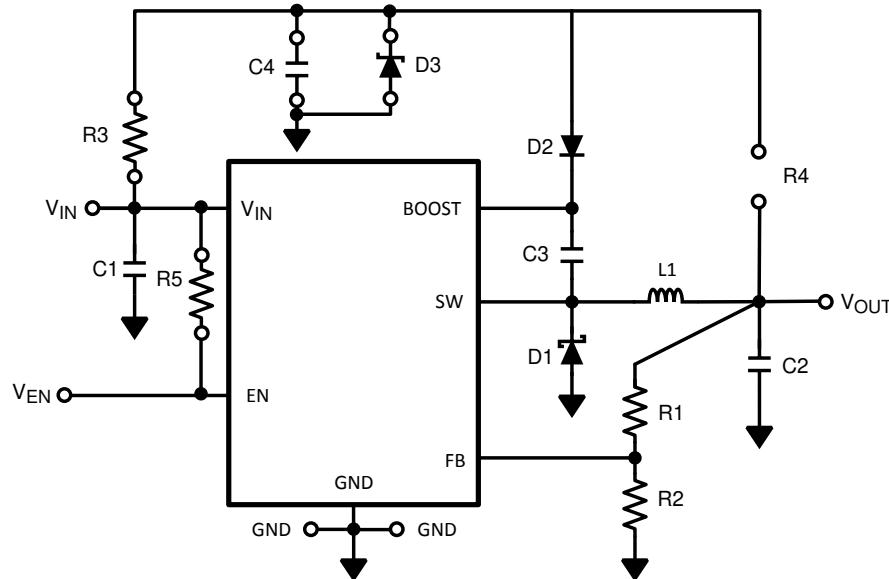


Figure 4-2.  $V_{BOOST}$  Derived from  $V_{SHUNT}$

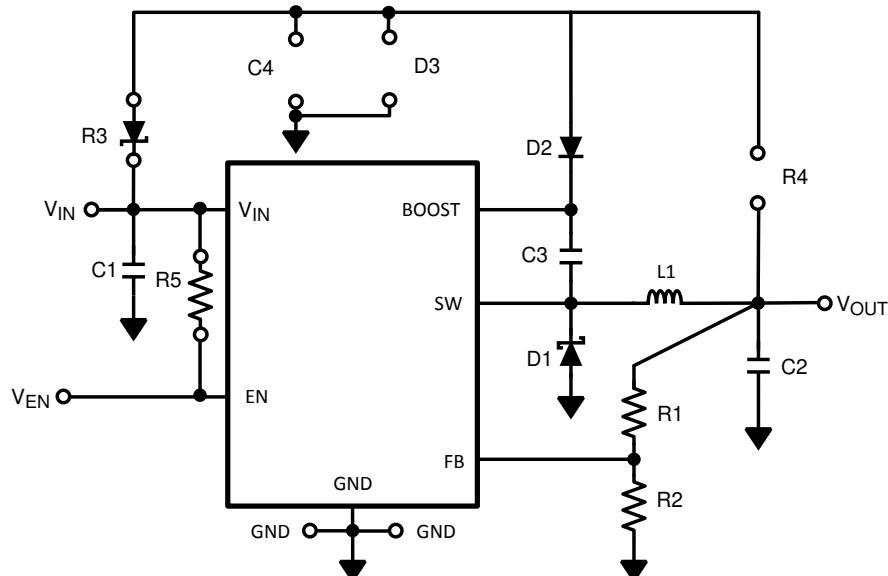


Figure 4-3.  $V_{BOOST}$  Derived from Series Zener Diode ( $V_{IN}$ )

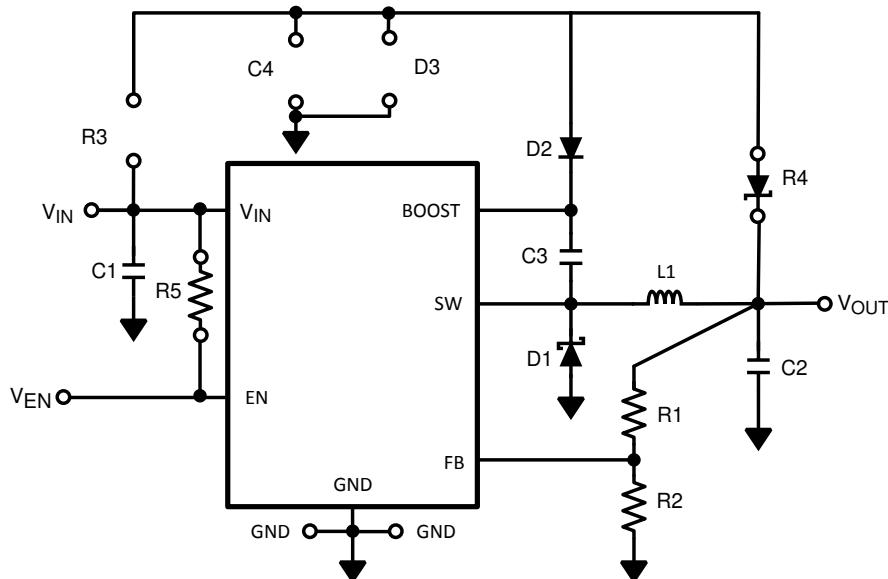


Figure 4-4.  $V_{BOOST}$  Derived from Series Zener Diode ( $V_{OUT}$ )

## 5 Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Revision A (April 2013) to Revision B (December 2021)	Page
Updated the numbering format for tables, figures, and cross-references throughout the document.	2
Updated the user's guide title	2
Edited user's guide for clarity	2

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