# Tl's journey to high-volume copper wire bonding production



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With billions of units shipped, TI offers packaging solutions that deliver the performance, reliability, quality and cost advantages of copper. Copper wire bonding is a proven, standard solution across many of TI's analog and embedded processing and it has gained broad acceptance among customers across a range of end markets, including high-reliability applications like industrial and automotive. This white paper will explore the technology's benefits and technical challenges faced in the ramp of copper wire bonding to high volume production.

# **Delivering reliability, quality** and performance advantages to customers

After years in which the semiconductor industry postponed the introduction of copper wire bonding between silicon die pads and package leads, copper wire bonding technology has finally gained traction as a mainstream IC assembly solution. In the past, chip manufacturers relied on gold wiring because it is easy to work with, but gold has performance limitations that make it less than ideal in some applications. In addition, manufacturers needed to find an alternative solution against the rising cost of gold to ensure a steady, more stable supply to high-volume customers.

While copper might seem to be a natural fit for chipto-package wiring because of its favorable cost,

high electrical conductivity and other characteristics, in fact it presents a number of manufacturing challenges which hindered adoption. But over the past decade, TI and other companies have dedicated development efforts and considerable quality and reliability testing in order to overcome these challenges. Today, copper wire bonding has come into its own as a proven solution for packaging, so much so that it has become standard for TI products.

#### **Early foresight drives development**

TI was one of the early leaders in developing and adopting copper wire bonding technology, and the first to introduce it into high-volume production. The company was well positioned to develop the technology because of its long-established

operations in chip assembly and test (A/T). With a history of building its own chip assembly equipment, TI was able to experiment with copper wire bonding independently to evaluate the results and benefits.

TI's extensive relationships with A/T tool and materials suppliers also provided a conduit for valuable input in the development of next-generation equipment that could support copper. TI's wide portfolio of analog and high-performance digital products, supported by manufacturing flexibility, enabled a careful phasein strategy in A/T facilities worldwide. With an effort that spanned a decade of research, development, process qualification and continuous improvement in manufacturing techniques, TI has played a significant role in getting copper wire bonding technology up and running across the industry.

#### **Advantages of copper**

Copper has normally been the metal of first choice for the wires used for power and communications, where it provides excellent conductivity, durability and ductility, at an economic cost. Copper has also been used in recent years for the interconnections between the transistors on integrated circuits. However, for the ultra-fine, thin pitch wiring that connects an integrated circuit's die pads with its package leads, chip makers have traditionally used gold because it is easy to work with in manufacturing. Gold is relatively soft, non-reactive except with aluminum, and offers sufficient electrical conductivity for many applications, though less than copper.

However, high electrical performance depends on excellent conductivity or, in other words, low resistance. Since copper has less electrical resistance than gold, (Figure 1), its conductivity is up to 40 percent higher which translates into important performance benefits for customers. As a result, copper can provide significant advantages in high-performance communications, computing, industrial equipment and other areas.

Copper's improved thermal conductivity means that it is better at pulling heat away from the die, leading to better performance at elevated temperatures and greater reliability. This performance advantage is particularly evident when copper wire is bonded on die pads plated with thick copper and nickel palladium finish in analog ad power product where support for high currents is essential. In this case, the metal joint formed is a solid-solution weld that is extremely stable at high temperatures. In addition, the metal's hardness means that bonds stay more stable mechanically in stressed operating environments.

Overall, copper wire bonding is technically more challenging than gold, but once achieved, the wiring is more resilient and performs better, especially in high-current analog products such as power MOSFETs.

#### Mastering the technical challenges

Despite the cost, performance, quality and reliability benefits, there were many technical challenges in developing copper as the best choice for chip-topackage thin wiring. One of the most important

Material	Electrical conductivity (mho-cm) at 25C	DC resistance of single wire (m $\Omega$ ) (2mm length, 24.3 um diameter)	Thermal conductivity (W/mK) at 25C
Au	4.1E+05	107.5	~ 300
Cu	5.8E+05	76	~ 398

Figure 1. Conductivity and resistance differences between gold and copper wire.

technical issues had to do with copper's hardness and brittleness relative to gold. Copper wires must be handled very carefully in order to eliminate the risk of damaging bond pads and underlying circuitry. As a result, new-generation wire bonders are designed to bond copper wires more precisely on circuit pads, with more finely tuned force, heat and ultrasound energy applied to create the bond. Modifications to the bonding wire capillaries were also introduced in order to make the wire adhere better to package pads and form a tighter bond.

Another formidable challenge of using copper is its reactivity with oxygen in the surrounding air. Wire bonders now provide an inert atmospheric envelope by means of a forming gas that surrounds the wire tip, thus preventing oxidation during bonding and enabling stronger bonds (Figure 2).

The new bonding techniques for copper required careful development and thorough testing, with characterization under a variety of different manufacturing and environmental conditions, including those designed to be worse than anticipated in actual production. The expertise was shared within the company and with suppliers in order to fan out successful discoveries and speed up technology development. In the meantime, TI worked closely with materials vendors and characterized materials extensively in order to maintain high standards for packaging quality, once copper wiring was in place.

As the process moved from development into manufacturing, success with different types of products did a great deal to drive the learning curve for assembly and testing the reliability of copper in various operating conditions. Analog power ICs,

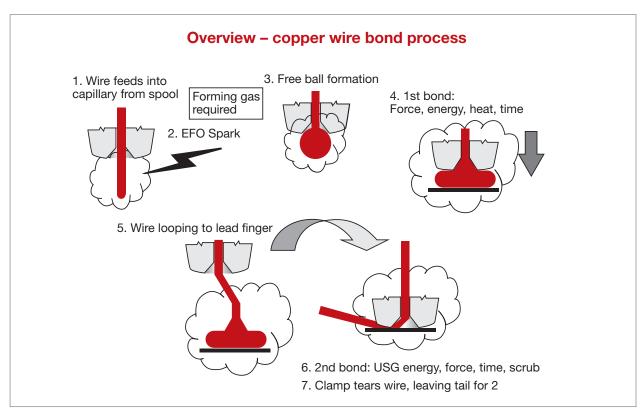


Figure 2. An overview of TI's copper wire bonding process, which uses forming gas throughout.

where copper wires are bonded to thick copper nickel palladium plated pads on the chip, require stable operation with high currents in order to provide a safe electrical environment for the device and the rest of the system.

On the other hand, wireless devices demand the assembly and operational specifications that are characteristic of high-volume digital products, including precise, secure bonding of copper wires to aluminum pads. Additionally, these product areas make use of various types of leaded packages and ball grid arrays, requiring accommodation of wiring techniques to different physical layouts. By mastering the challenges involved in copper wiring with these types of products, TI gained wide-ranging expertise that it continues to build upon and fan out to other products today.

# **Evolution of a new wiring** technology

The change from gold to copper wire bonding is an example of adapting technology slowly and carefully in order to gain significant benefits. Because of the metals' different characteristics, the transition was not a simple switch of materials, but a carefully monitored re-engineering effort. Like every stage in IC manufacturing, the process of selecting, forming and bonding chip-to-package wires had to be extremely precise in order to provide the necessary quality and reliability in the shipped product.

Prepping the copper wire bonding process required several years of preliminary studies and experiments, extensive testing and process qualification that customers can have confidence in today (Figure 3). By 2008, TI entered volume

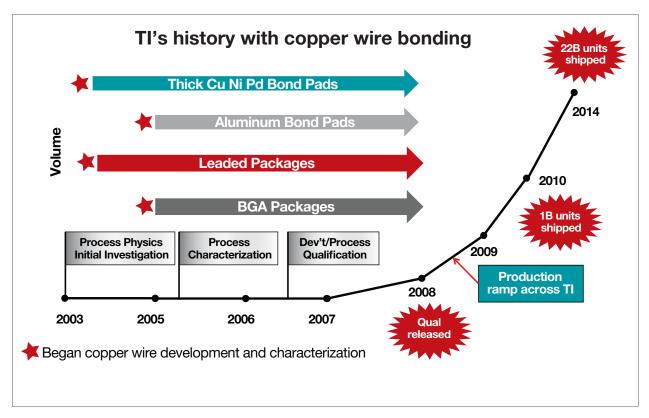


Figure 3. TI's copper wire bonding production ramp.

production of its first products using the technology. Subsequent months were spent improving the process to raise quality and production yields to meet or exceed the levels of traditional gold wire bonding, an improvement that TI's A/T sites were able to achieve in less than a year. By 2010, the first billion units with copper wire bonding had been shipped, a figure that had increased by more than twenty-fold by mid-2014.

Today, TI has qualified copper wire bonding at all its A/T facilities worldwide. All TI's existing analog and CMOS silicon technologies are qualified on copper wire, and all the company's new technologies and packages are being developed on it. The program's success is shown not only by the sites qualified and number of units shipped, but also by widespread customer acceptance of the new technology in TI products. Copper has become TI's standard metal for wire bonding, the default for new products.

## **Readiness for high reliability** applications

Today, TI's copper wire bonding technology is qualified for use across high reliability segments, including industrial and automotive applications such as safety (anti-lock brake systems, power steering, stability control, etc.), infotainment, body and comfort and powertrain. This includes qualifying copper wire bonding for automotive on copper/nickel/palladium bond pad metallization and aluminum bond pad metallization.

Qualification required significant development to meet rigorous standards, such as AEC Q-100, along with strict manufacturing discipline. Implementation also included rigorous production quality and reliability monitoring and manufacturing controls, along with thorough process corner development and extended reliability assessments.

All major package types that are important for automotive and industrial customers are qualified, including ball grid array (BGA), quad flat no-lead (QFN), and leaded packages (QFP, SOIC, TSSOP). Moving forward, TI will continue to qualify copper wire for automotive and high reliability on all new silicon technologies and packages to meet customer needs.

### An important transition for TI customers

Copper wire bonding brings greater reliability, quality and performance in the products that TI customers design into a growing range of applications. In manufacturing operations, using copper for wiring provides a hedge on rising prices that can affect the supply of gold available for industrial uses. Thus, copper helps ensure a steady supply of components that can support the ongoing business needs of TI customers.

Today, with copper wire bonding becoming more widespread, TI remains a leader in innovation and breadth of high-volume production using this technology. In a company where packaging is seen as an integral part of the design process and a strategic product differentiator, the transition to copper wire bonding represents an important step in the evolution of packaging technology that benefits TI's customers today and in the future.

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