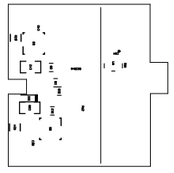
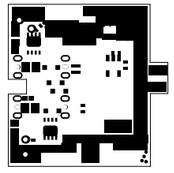
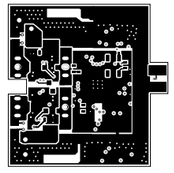


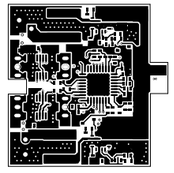
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = Top Overlay	TID #: N/A		
PLOT NAME = Top Overlay	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS



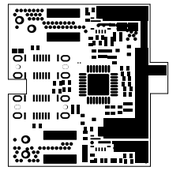
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = **Top Solder**	TID #: N/A		
PLOT NAME = Top Solder Mask	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS



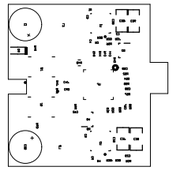
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = Top Layer	TID #: N/A		
PLOT NAME = Top Layer	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = Bottom Layer	TID #: N/A		
PLOT NAME = Bottom Layer	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = Bottom Solder	TID #: N/A		
PLOT NAME = Bottom Solder Mask	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS

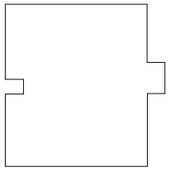


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS

Symbol	Quantity	Finished Hole Size	Plated	Hole Type	Drill Layer Pair	Hole Tolerance
	2	27.95mil (0.710mm)	NPTH	Round	Top Layer - Bottom Layer	
	85	7.87mil (0.200mm)	PTH	Round	Top Layer - Bottom Layer	
	100	9.84mil (0.250mm)	PTH	Round	Top Layer - Bottom Layer	
	27	11.81mil (0.300mm)	PTH	Round	Top Layer - Bottom Layer	
	4	30.00mil (0.762mm)	PTH	Round	Top Layer - Bottom Layer	
	2	27.95mil (0.710mm)	NPTH	Slot	Top Layer - Bottom Layer	
	8	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer	
	228 Total					

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = Drill Drawing	TID #: N/A		
PLOT NAME = Drill Drawing	GENERATED : 12/13/2024 6:11:22 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PMP41117	REV: E1	SVN REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED : 12/13/2024 6:11:23 PM		TEXAS INSTRUMENTS

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