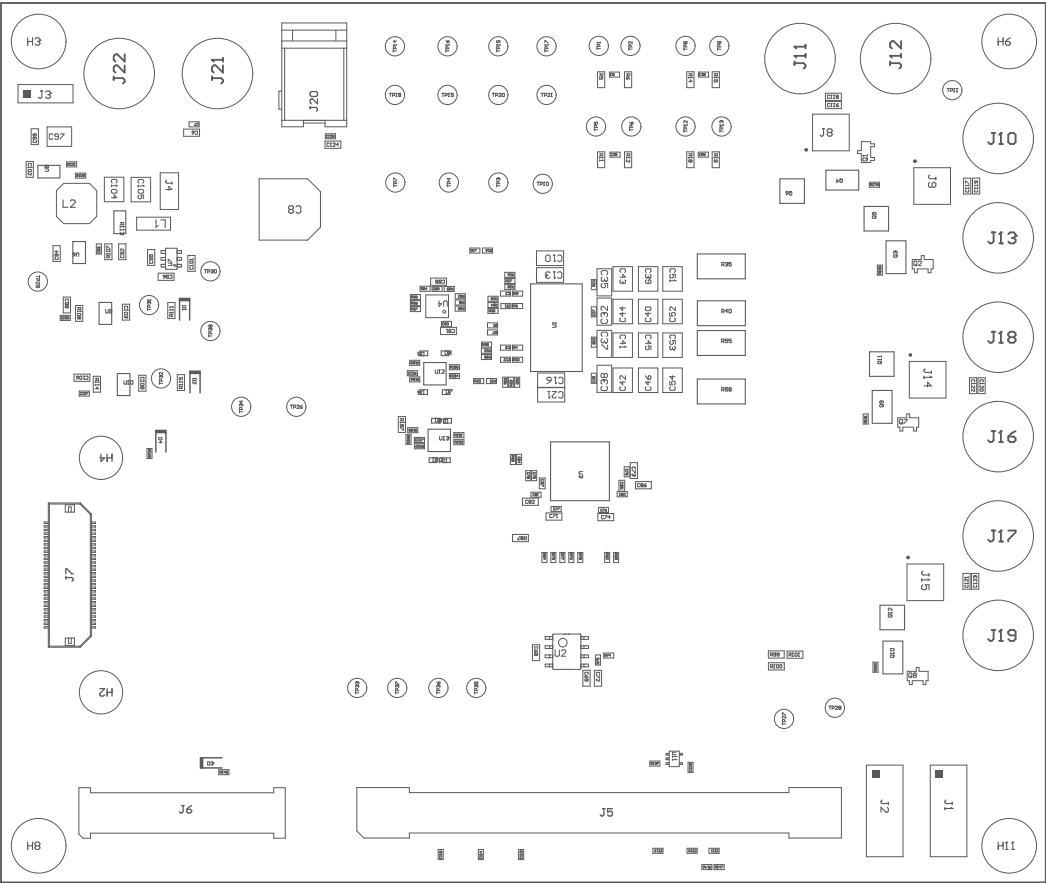


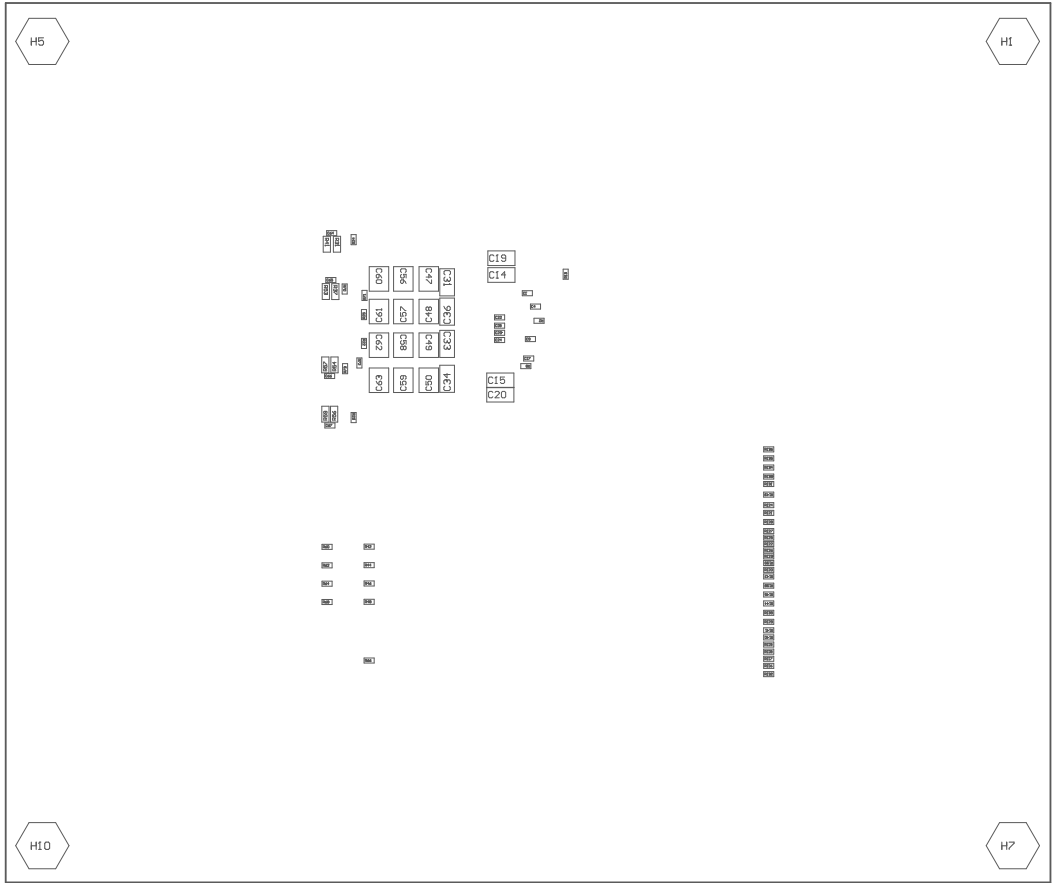


Z21 ■Install label in silkscreened box after final wash. Text shall be 8 pt font. Text shall be per the Label Table in the PDF schematic.
Z22 ■These assemblies are ESD sensitive, ESD precautions shall be observed.
Z23 ■These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
Z24 ■These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001(1+1+1+1)

All vias under the BGA package shilled be tented to keep solder from flowing into them. Filled vias are required for any in-pad-vias.

PCB VIEWED FROM TOP SIDE	BOARD #: DC431	REV: A	SUN REV: Not in version control
	TID #:	N/A	
PLOT NAME = Top Assembly Drawing	GENERATED : 8/30/2026 12:52:32 PM	TEXAS INSTRUMENTS	



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001(1+1+1+1)

PCB VIEWED FROM BOTTOM SIDE	BOARD #: DC431	REV: A	SUN REV: Not in version control
	TID #: N/A		
PLOT NAME = Bottom Assembly Drawing	GENERATED : 8/30/2026 12:52:34 PM	TEXAS INSTRUMENTS	

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