

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

5				6
Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

Rev: E1	Sheet Title:				
rol disabled	Assembly Variant: 001		Shee	et: 1 of 3	
	File: TIDA01373_SB_Cove	erSheet.SchDoc		Size: B	
eider	Contact: http://www.ti.com	/support			
5				6	



## **Test Points**

4

3

2

1

в

С

D

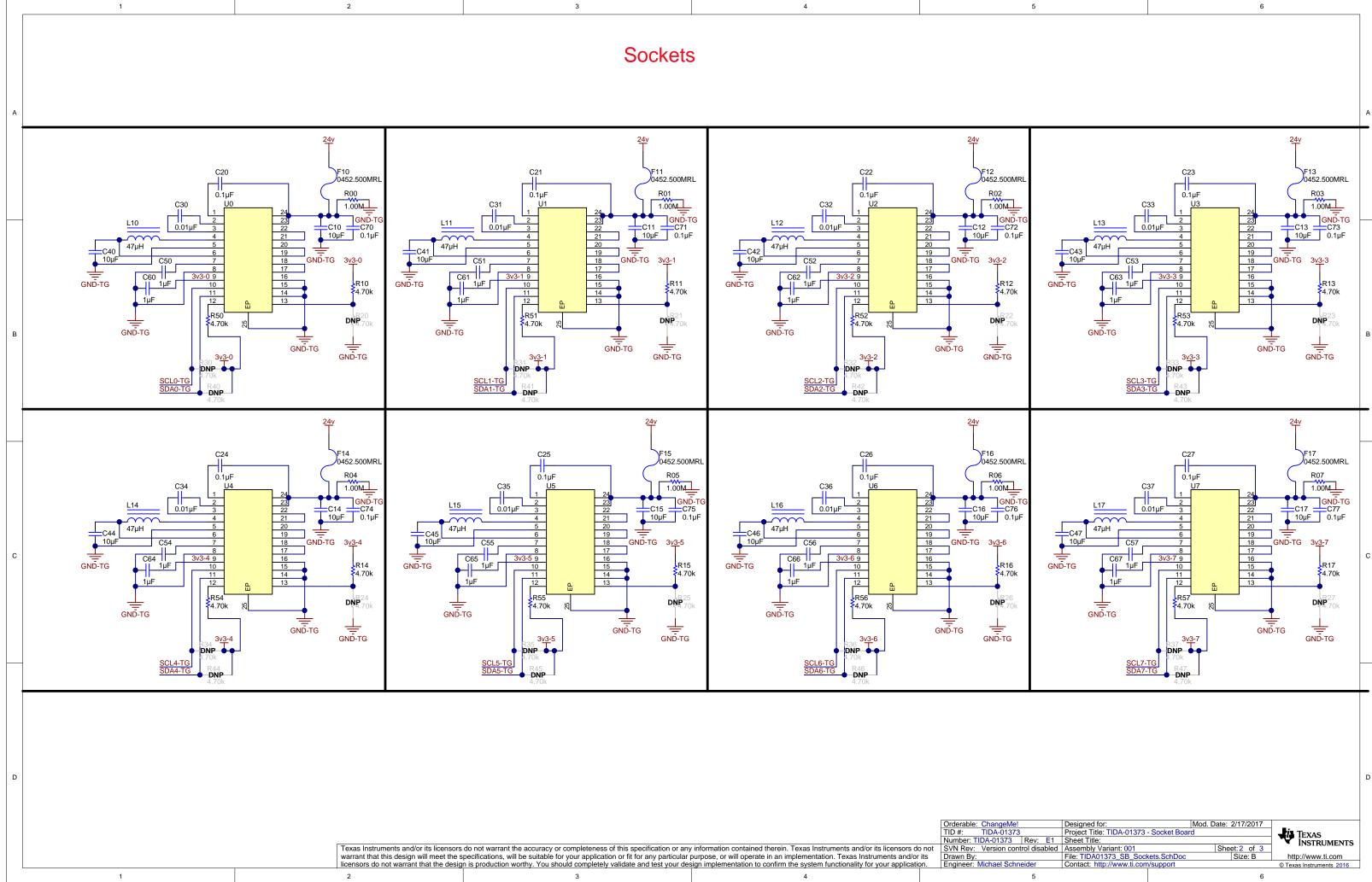
1

O SCL0-TG SCL0-TG	O SCL1-TG SCL1-TG	O SCL2-TG SCL2-TG	O SCL3-TG SCL3-TG
	O SCL5-TG SCL5-TG		
	O SDA1-TG SDA1-TG		
O SDA4-TG SDA4-TG	O SDA5-TG SDA5-TG	O SDA6-TG SDA6-TG	
0 24v 24v	ON/OFF ON/OFF		

Orderable: ChangeMe!	Designed for:	Mod. D	ate: 2/1/2017	
TID #: TIDA-01373	Project Title: TIDA-01373 -	Socket Board		TEXAS
Number: TIDA-01373 Rev: E1	Sheet Title:			INSTRUMENTS
SVN Rev: Version control disabled	Assembly Variant: 001		Sheet: 2 of 3	
Drawn By:	File: TIDA01373_SB_Test	Points.SchDoc	Size: B	http://www.ti.com
Engineer: Michael Schneider	Contact: http://www.ti.com	/support		© Texas Instruments 2016
5		6		

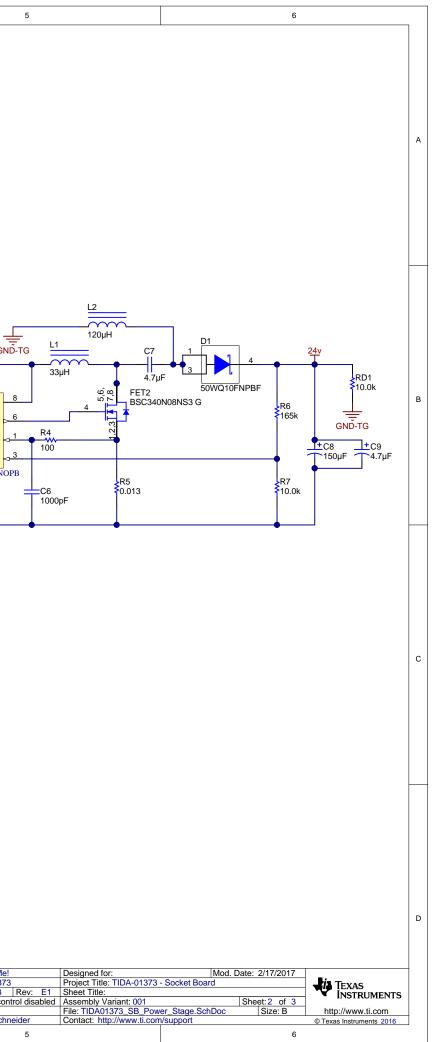
			INUI
Texas Instruments and/or its licensors	do not warrant the accuracy or completeness of this specification or any	information contained therein. Texas Instruments and/or its licensors do	not SVN
warrant that this design will meet the s	pecifications, will be suitable for your application or fit for any particular	purpose, or will operate in an implementation. Texas Instruments and/or i	its Drav
licensors do not warrant that the desig	n is production worthy. You should completely validate and test your des	sign implementation to confirm the system functionality for your application	n. Eng
0			
2	3	4	

5	6	



e!	Designed for:	Mod. D	ate:	2/17/2017	
73	Project Title: TIDA-01373	<ul> <li>Socket Board</li> </ul>			TEXAS
Rev: E1	Sheet Title:				TEXAS INSTRUMENTS
ontrol disabled	Assembly Variant: 001		Shee	et:2 of 3	In STROMENTS
	File: TIDA01373_SB_Sock	ets.SchDoc		Size: B	http://www.ti.com
hneider	Contact: http://www.ti.com	/support			© Texas Instruments 2016
5				6	

1	2	3	4	
A A $Vin \bigcirc Vin$ $Vin \bigcirc Vin \bigcirc Vin$ $Vin \bigcirc Vin \bigcirc $	ECH8310-TL- Vin FET-P 1.2.3 7.8 R0 6.04k R1 4.02k ON/OFF 2.3.5.6.7	H		U10 T FA/SD VIN 8 2 COMP DR 6 4 AGND ISEN 1 5 PGND FB 3 LM3478QMMX/NOPE
C		F1324S-7PPBF	tC1 22µF 4.7µF 0.1µF 54.9k 4700pF	
D	Texas Instruments and/or its licensors warrant that this design will meet the licensors do not warrant that the design 2	s do not warrant the accuracy or completeness of this specification or any specifications, will be suitable for your application or fit for any particular on is production worthy. You should completely validate and test your des 3	y information contained therein. Texas Instruments and/or its licensors de purpose, or will operate in an implementation. Texas Instruments and/or sign implementation to confirm the system functionality for your application 4	Orderable: ChangeMe! TID #: TIDA-01373 Number: TIDA-01373 [ D not SVN Rev: Version contro its Drawn By: Dn. Engineer: Michael Schnei



	1 2 3 4 5 6	
А	I2C	A
	1 2C Download Port	
в	ONOFF SCLOTG 3 SCLOTG 3	В
С		с
D	Orderable         Observation         Designed for:         IMod. Date:         2/2/2017           TiD /r	D
	Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.       SVN Rev: Version control disabled       Assembly Variant: 001       Sheet: 2 of 3       Anttp://www.ti.com         1       2       3       4       5       6	

5	6

H1 1 NY PMS 440 0025 PH	H2 1 NY PMS 440 0025 PH	H3 <u>1</u> NY PMS 440 0025 PH	H4 
H5	H6	H7	H8
()	()	()	()
1902C	1902C	1902C	1902C

1

DNP DNP BNP FID1 FID2 FID3

А

в

С

D

1

2

You should delete the nylon screws/standoffs and/or the bumpons as needed for your design (or substitute other parts from Hardware.IntLib). Bumpons are cheaper, but provide less clearance.

4

Deleting anything else from this page may result in your EVM submission being rejected (until you add them back).

Update the Label Text in the Label Table as needed for each Assembly Variant.

You should delete this note too.

3

	Orderable: ChangeMe!	Designed for:		Date: 2/1/2017	
	TID #: TIDA-01373	Project Title: TIDA-01373 ·	<ul> <li>Socket Board</li> </ul>		TEXAS
	Number: TIDA-01373 Rev: E1	Sheet Title:			INSTRUMENTS
ot	SVN Rev: Version control disabled	Assembly Variant: 001		Sheet: 3 of 3	In OTHER HEIRIG
	Drawn By:	File: TIDA01373_SB_Hard	ware.SchDoc	Size: B	http://www.ti.com
	Engineer: Michael Schneider	Contact: http://www.ti.com	l/support		© Texas Instruments 2016
	5		6		

D

			Number:
Texas Instruments and/or its licensors	do not warrant the accuracy or completeness of this specification or any	/ information contained therein. Texas Instruments and/or its licensors do not	SVN Rev
warrant that this design will meet the s	pecifications, will be suitable for your application or fit for any particular	purpose, or will operate in an implementation. Texas Instruments and/or its	Drawn By
licensors do not warrant that the desig	n is production worthy. You should completely validate and test your dea	sign implementation to confirm the system functionality for your application.	Engineer
2	2		
2	3	4	

5	6

## IMPORTANT NOTICE FOR TI DESIGN INFORMATION AND RESOURCES

Texas Instruments Incorporated ('TI") technical, application or other design advice, services or information, including, but not limited to, reference designs and materials relating to evaluation modules, (collectively, "TI Resources") are intended to assist designers who are developing applications that incorporate TI products; by downloading, accessing or using any particular TI Resource in any way, you (individually or, if you are acting on behalf of a company, your company) agree to use it solely for this purpose and subject to the terms of this Notice.

TI's provision of TI Resources does not expand or otherwise alter TI's applicable published warranties or warranty disclaimers for TI products, and no additional obligations or liabilities arise from TI providing such TI Resources. TI reserves the right to make corrections, enhancements, improvements and other changes to its TI Resources.

You understand and agree that you remain responsible for using your independent analysis, evaluation and judgment in designing your applications and that you have full and exclusive responsibility to assure the safety of your applications and compliance of your applications (and of all TI products used in or for your applications) with all applicable regulations, laws and other applicable requirements. You represent that, with respect to your applications, you have all the necessary expertise to create and implement safeguards that (1) anticipate dangerous consequences of failures, (2) monitor failures and their consequences, and (3) lessen the likelihood of failures that might cause harm and take appropriate actions. You agree that prior to using or distributing any applications. TI has not conducted any testing other than that specifically described in the published documentation for a particular TI Resource.

You are authorized to use, copy and modify any individual TI Resource only in connection with the development of applications that include the TI product(s) identified in such TI Resource. NO OTHER LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE TO ANY OTHER TI INTELLECTUAL PROPERTY RIGHT, AND NO LICENSE TO ANY TECHNOLOGY OR INTELLECTUAL PROPERTY RIGHT OF TI OR ANY THIRD PARTY IS GRANTED HEREIN, including but not limited to any patent right, copyright, mask work right, or other intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information regarding or referencing third-party products or services does not constitute a license to use such products or services, or a warranty or endorsement thereof. Use of TI Resources may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

TI RESOURCES ARE PROVIDED "AS IS" AND WITH ALL FAULTS. TI DISCLAIMS ALL OTHER WARRANTIES OR REPRESENTATIONS, EXPRESS OR IMPLIED, REGARDING TI RESOURCES OR USE THEREOF, INCLUDING BUT NOT LIMITED TO ACCURACY OR COMPLETENESS, TITLE, ANY EPIDEMIC FAILURE WARRANTY AND ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, AND NON-INFRINGEMENT OF ANY THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

TI SHALL NOT BE LIABLE FOR AND SHALL NOT DEFEND OR INDEMNIFY YOU AGAINST ANY CLAIM, INCLUDING BUT NOT LIMITED TO ANY INFRINGEMENT CLAIM THAT RELATES TO OR IS BASED ON ANY COMBINATION OF PRODUCTS EVEN IF DESCRIBED IN TI RESOURCES OR OTHERWISE. IN NO EVENT SHALL TI BE LIABLE FOR ANY ACTUAL, DIRECT, SPECIAL, COLLATERAL, INDIRECT, PUNITIVE, INCIDENTAL, CONSEQUENTIAL OR EXEMPLARY DAMAGES IN CONNECTION WITH OR ARISING OUT OF TI RESOURCES OR USE THEREOF, AND REGARDLESS OF WHETHER TI HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

You agree to fully indemnify TI and its representatives against any damages, costs, losses, and/or liabilities arising out of your noncompliance with the terms and provisions of this Notice.

This Notice applies to TI Resources. Additional terms apply to the use and purchase of certain types of materials, TI products and services. These include; without limitation, TI's standard terms for semiconductor products <a href="http://www.ti.com/sc/docs/stdterms.htm">http://www.ti.com/sc/docs/stdterms.htm</a>), evaluation modules, and samples (<a href="http://www.ti.com/sc/docs/stdterms.htm">http://www.ti.com/sc/docs/stdterms.htm</a>), evaluation

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2017, Texas Instruments Incorporated