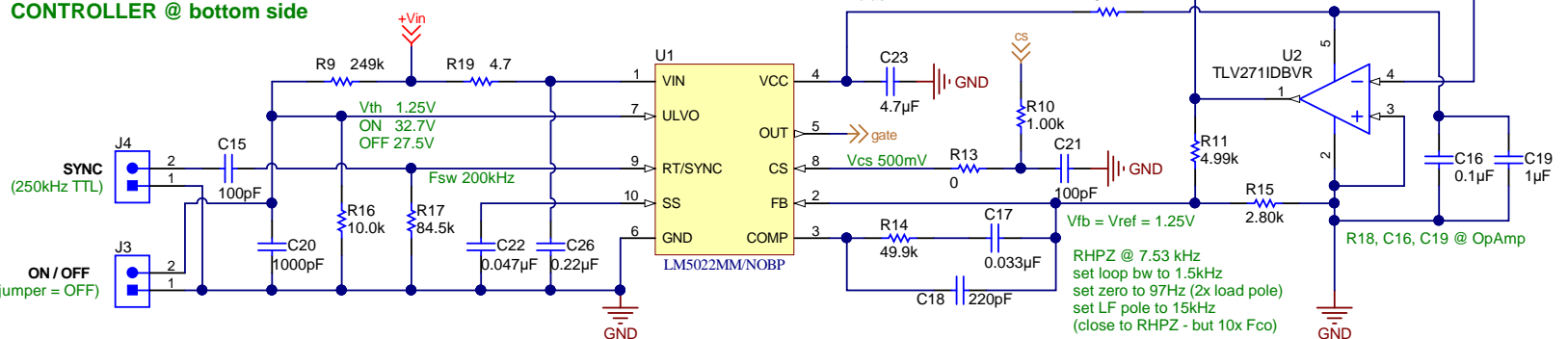


POWER STAGE @ top side

Revision History	
Revision	Notes
A	*** PRELIMINARY *** added gate resistor, changed dual inductor
B	

- * dual inductor MSD-1514-473 has been modified:
- * changed crossed pin out to parallel for layout reasons
- * increased leakage inductance to reduce circulating currents, LL=17uH

CONTROLLER @ bottom side



NOTES:

- 1) R1 for test purposes only
- 2) all calculations done for +36V input voltage
- 3) MSD1514 series from stock has been replaced by custom YA9010-BE

low noise 50W+ Cuk converter

Orderable:	Designed for: Public Release	Mod. Date: 12/7/2018
TID #:	Project Title: inverting SEPIC for sensitive loads	
Number: PMP30487	Rev: A	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 1
Drawn By:	File: PMP30487RevB_SchDoc	Size: A4
Engineer: B. Geck	Contact: http://www.ti.com/support	



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