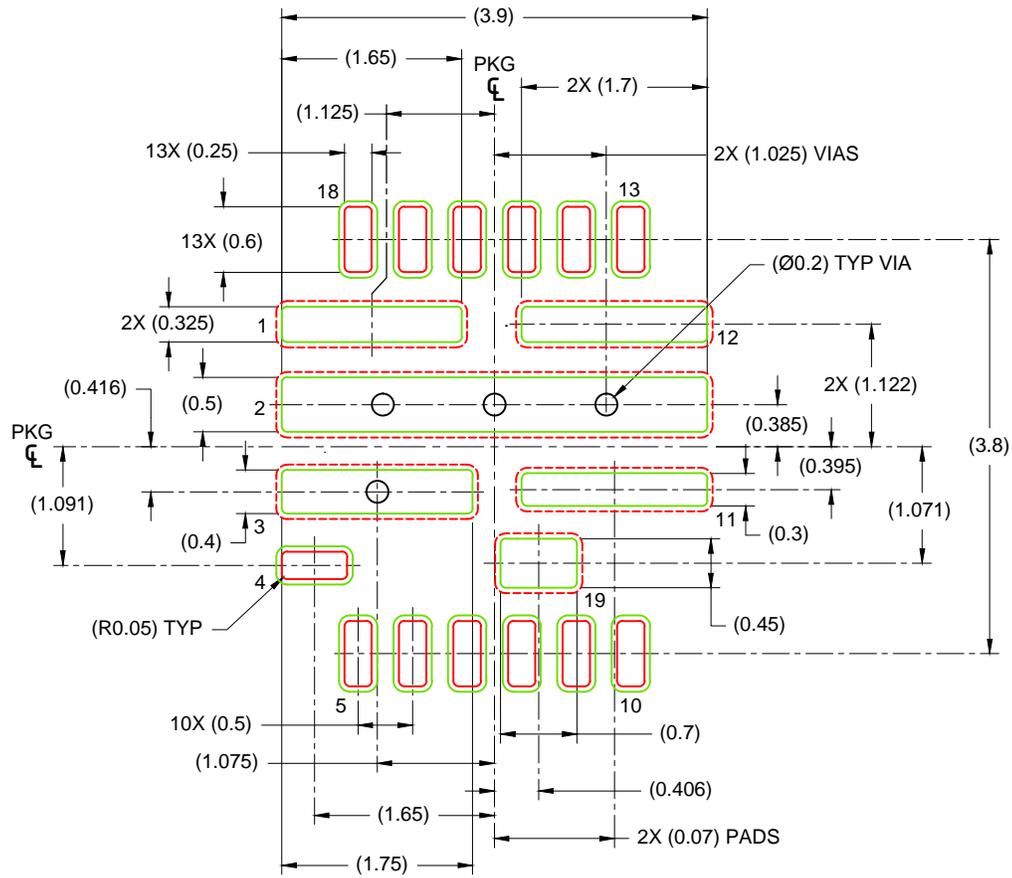


EXAMPLE BOARD LAYOUT

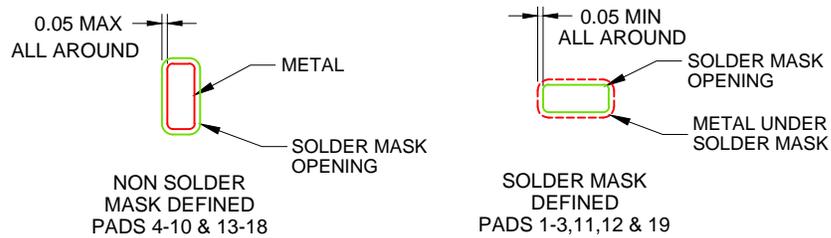
VQFN - 1 mm max height

RNJ0019A

PLASTIC QUAD FLATPACK - NO LEAD



LAND PATTERN EXAMPLE
SCALE: 15X



SOLDER MASK DETAILS

4221851/A 03/2015

NOTES: (continued)

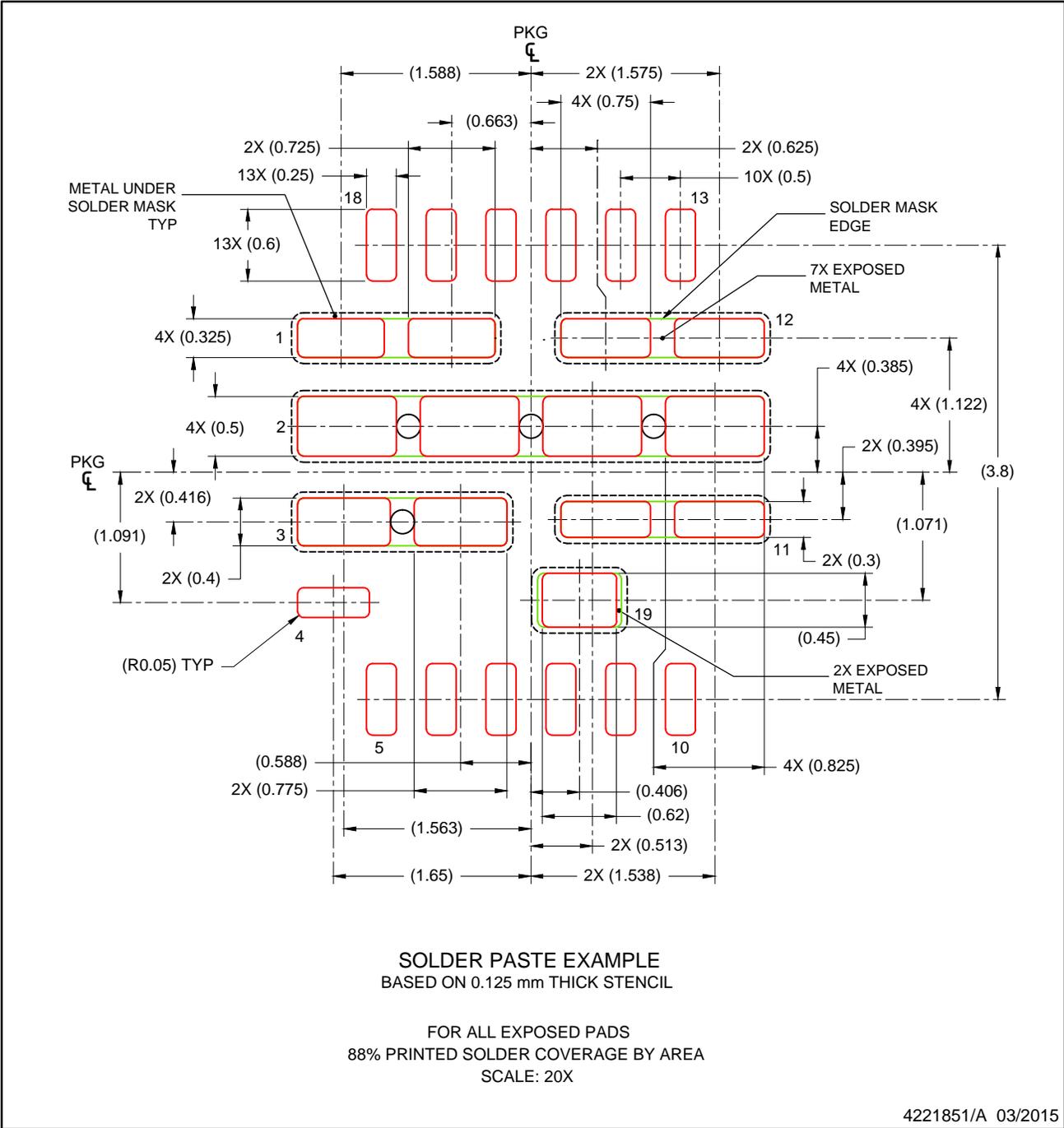
- For more information, refer to QFN/SON PCB application note in literature No. SLUA271 (www.ti.com/lit/sluea271).
- Solder mask tolerances between and around signal pads can vary based on board fabrication site.

EXAMPLE STENCIL DESIGN

VQFN - 1 mm max height

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PLASTIC QUAD FLATPACK - NO LEAD



NOTES: (continued)

- 5. For alternate stencil design recommendations see IPC-7525 or board assembly site preference.

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