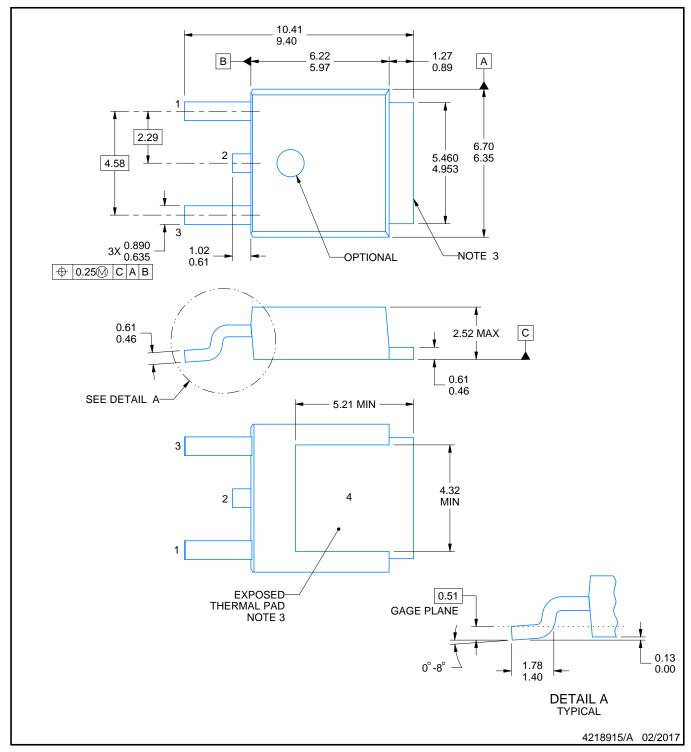


TO-252



## NOTES:

- 1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.

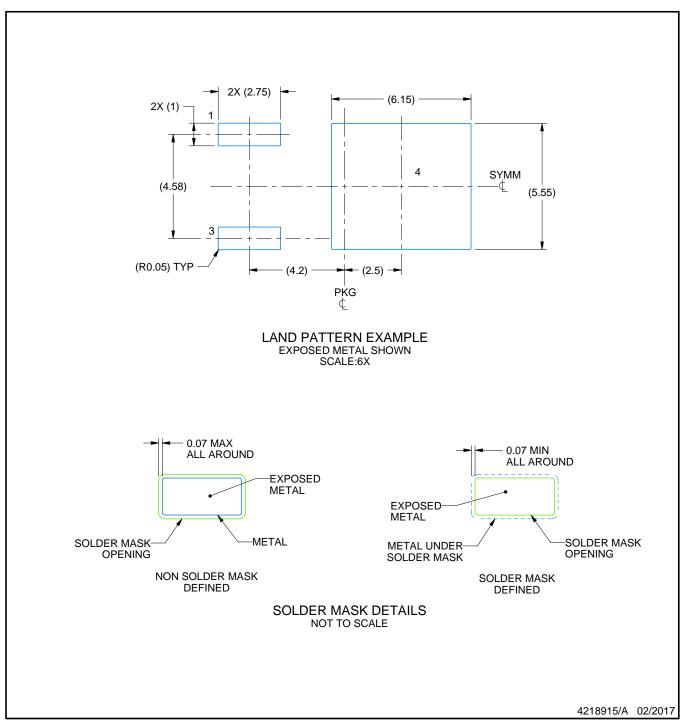
  2. This drawing is subject to change without notice.

  3. Shape may vary per different assembly sites.

  4. Reference JEDEC registration TO-252.



TO-252



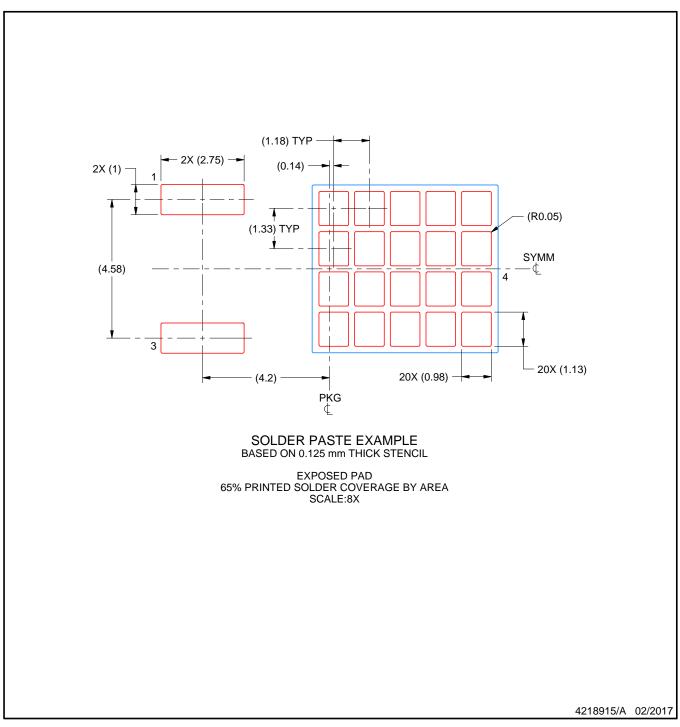
NOTES: (continued)



<sup>5.</sup> This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature numbers SLMA002(www.ti.com/lit/slm002) and SLMA004 (www.ti.com/lit/slma004).

<sup>6.</sup> Vias are optional depending on application, refer to device data sheet. It is recommended that vias under paste be filled, plugged or tented.

TO-252



NOTES: (continued)



<sup>7.</sup> Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.

8. Board assembly site may have different recommendations for stencil design.

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