Application Report INA901-SP Neutron Displacement Damage Characterization

TEXAS INSTRUMENTS

ABSTRACT

This report presents the effect of neutron displacement damage (NDD) on the INA901-SP device. The results show that all devices were fully functional and within production test limits after having been irradiated up to 1 × 10¹³ n/cm² (1-MeV equivalent). A sample size of fifteen units was exposed to radiation testing per (MIL-STD-883, Method 1017 for Neutron Irradiation) and an additional unirradiated sample device was used for correlation. All devices used in the experiment were from lot date code 1839. Electrical testing was performed at Texas Instruments before and after neutron irradiation using the production test program for INA901-SP.

Table of Contents

1 Overview	2
2 Test Procedures	
3 Facility	
4 Results	
Revision History	4
A Test Results	

List of Figures

Figure 1-1. INA901-SF	P Device	2

List of Tables

Table 1-1. Overview Information	2
Table 2-1. Neutron Irradiation Conditions	3
Table 4-1. INA901-SP Specification Table	3

Trademarks

All trademarks are the property of their respective owners.

1



1 Overview

The INA901-SP device is a voltage-output, current-sense amplifier that can sense drops across shunt resistors at common-mode voltages from -15 V to 65 V, independent of the supply voltage. The INA901-SP operates from a single 2.7-V to 16-V supply, drawing 700 μ A (typical) of supply current. The devices are offered in an ultra small, thermally enhanced 8-pin ceramic flatpack package.

General device information and testing conditions are listed in Table 1-1.

Table 1-1. Overview Information

TI Part Number	INA901-SP
Device Function	Synchronous Buck Converter
Technology	LBCSOI
A/T Lot Number / Date Code	1839A
Unbiased Quantity Tested	15
Exposure Facility	VPT Rad
Neutron Fluence (1-MeV equivalent)	1.0 × 10 ¹² , 5.0 × 10 ¹² , 1.0 × 10 ¹³ n/cm ²
Irradiation Temperature	25°C

TI may provide technical, applications or design advice, quality characterization, and reliability data or service providing these items shall not expand or otherwise affect TI's warranties as set forth in the Texas Instruments Incorporated Standard Terms and Conditions of Sale for Semiconductor Products and no obligation or liability shall arise from Semiconductor Products and no obligation or liability shall arise from TI's provision of such items.



Figure 1-1. INA901-SP Device

2 Test Procedures

The INA901-SP was electrically pre-tested using the production automated test equipment program.

General test procedures were IAW MIL-STD-883, Method 1017 for Neutron Irradiation of INA901-SP.

Table 2-1. Neutron madiation conditions				
Group	Sample Qty	Neutron Fluence (n/cm ²)	Bias	
А	5	1.0 × 10 ¹²	Unbiased	
В	5	5.0 × 10 ¹²	Unbiased	
С	5	1.0 × 10 ¹³	Unbiased	

Table 2-1. Neutron Irradiation Conditions

3 Facility

Devices were exposed via fast neutron irradiation (FNI) at the University of Massachusetts's Lowell Research Reactor (UMLRR). The facility is designed to give a fast flux level \geq 1011 n/cm²–s, with relatively low thermal fluence and gamma dose rates. Samples with a cross-sectional area as large as 30 cm (12 in) × 30 cm (12 in) and up to 15-cm (6-in) thick can be irradiated. The fast neutron flux is designed to be nearly uniform over the 30-cm (12-in) × 30-cm (12-in) area facing the core, and the fast fluence variation through the sample thickness is minimized via a single 180° rotation of the sample canister at the midpoint of the irradiation period. The FNI facility offers a significantly larger sample volume than previously available within the University of Massachusetts Lowell Research Reactor (UMLRR).

The fluences are calculated based on 1-MeV equivalences.

Detailed information of the radiation facility is available at the following link:

www.uml.edu/docs/FNI%20Brochure_tcm18-90375.pdf

4 Results

There were no functional failures at any irradiation level. All parametric measurements remained well within all *INA901-SP Radiation Hardened*, –15-V to 65-V Common Mode, Unidirectional Current-Shunt Monitor Data Sheet limits for all exposure levels. All parametric measurements remained well within the production test limits which are guard-banded from the data sheet limits. The full parameter list and graphs are found in Appendix A.

Table 4-1 lists the INA901-SP specification compliance matrix.

PARAMETER	TEST CONDITION	MIN	ТҮР	МАХ	UNIT	TEST NUMBER	
INPUT	NPUT						
Common-mode rejection ratio	VIN+ = -16 V to 65 V	80	120		dB	5.1	
Offset voltage			±0.5	±2.5	mV	3.1	
Vos vs power-supply			5	250	μV/V	4.0	
Input bias current, Vin-pin			±8	±16	μA	7.2	
OUTPUT	OUTPUT						
Total gain error	VSENSE = 20 mV to 100 mV		±0.2%	±1.5%		3.0	
Total output error			±0.75%	±2%		3.2	
VOLTAGE OUTPUT	VOLTAGE OUTPUT						
Swing to V+ power-supply rail			(V+) – 0.05	(V+) – 0.2	V	6.1	
Swing to GND			VGND + 0.003	VGND + 0.05	V	6.0	
POWER SUPPLY	POWER SUPPLY						
Quiescent current	VOUT = 2 V		700	900	μA	2.4	

Table 4-1. INA901-SP S	pecification Table
------------------------	--------------------

3

Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

С	hanges from Revision A (February 2019) to Revision B (September 2020)	Page
•	Updated the numbering format for tables, figures, and cross-references throughout the document	2
•	Updated Overview Information table	2
•	Updated the Specification Table	3



A Test Results

This appendix contains the detailed test results.

5

IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATA SHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, regulatory or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

TI objects to and rejects any additional or different terms you may have proposed.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2022, Texas Instruments Incorporated